

FCC ID: SSO-EAPS

Circuit Description

The 315MHz LCR oscillator drives the base of Q1 the final/buffer amplifier. The modulation provided by IC. The output of Q1 has the matching network consisting of L2 and C4 that limit the harmonic content and effect the proper coupling of the antenna to the output stage.

Antenna, Ground and Power Source

There is no external antenna. There is no external ground connection. The ground is only that of the printed circuit board. Electric current is supplied by a 3Volt ("CR2032" button cell x 1) primary battery