

Technical Description

ARCHOS 605 WiFi

Explanation of 605F block diagram (model 54V00F)

1- The main CPU (DM420 with Flash eeprom and DDRAM) manage all peripherals on the board. It has 2 oscillators, 27MHz and 24MHz.

2- The auxiliary Controller manages

- the power supply (+1.2v/+1.8v/+3.3v/+5v)
- the real time clock with 32.768khz crystal
- the battery charger.

3- Memory of the product is provided by DDR2 memory (DDRAM), Flash memory and Nand Flash Memory.

A SD card can be connected to increase the capacity of storage memory.

4- The LCD is controlled by main CPU through LCD driver, LCD power

5- Audio functionality (Input /Output) managed by Audio Codec

6- WiFi is provided by on board circuits, including Base Band, Power Amplifier, switches and antennae.