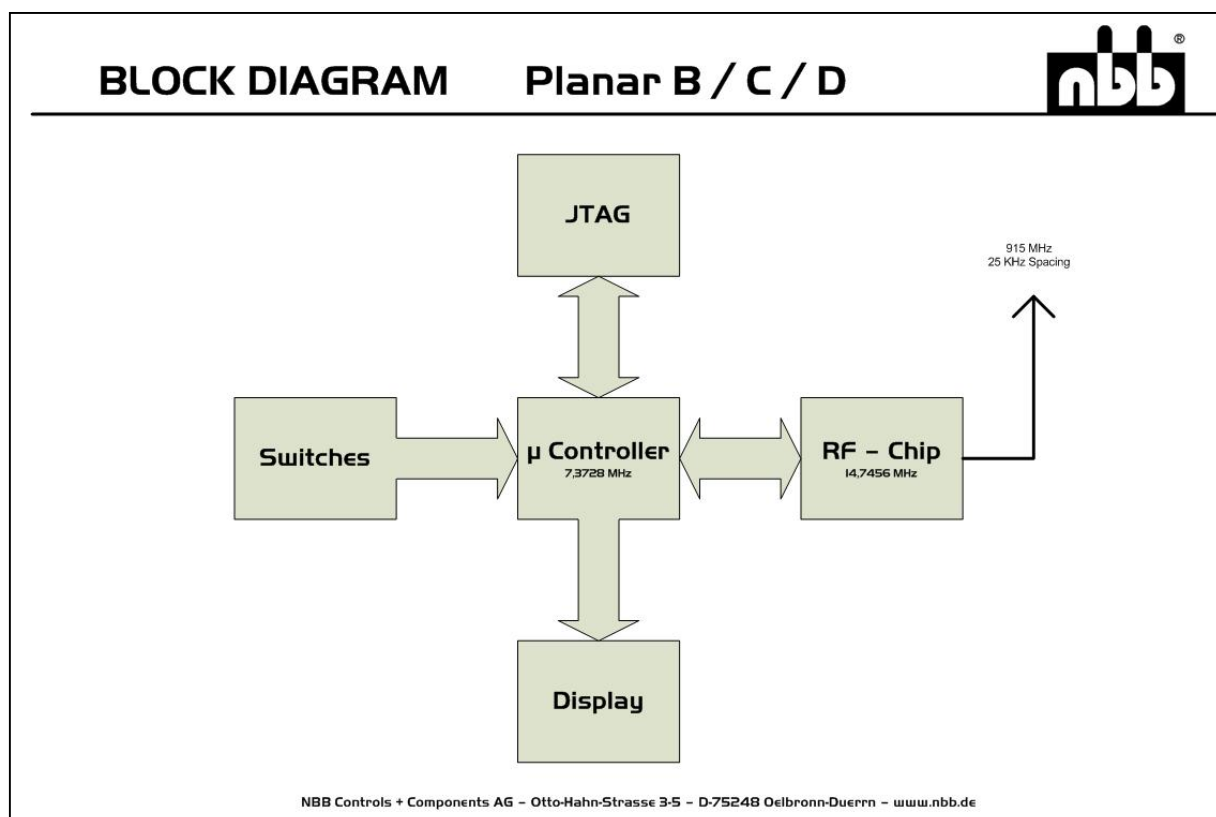


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PLANAR B C D Radio Control Transmitter Blockdiagram

-Theory of Operation-



Theory of Operation

The radio control transmitter family "PLANAR B, C, D" is based on a single electronic architecture consisting of a microcontroller (U5), a radio frequency module with the rf chip (U1), power voltage regulating devices (U4, U11, U1 etc.) and further supporting components. The B,C,D family is generated by scaling in terms of the number of control keys interfaced to U5. Only with the PLANAR B also a LCD display interfaces (serial protocol) to U5. The key input is formatted to a serial telegram by U5 which is modulated (frequency modulation) upon the rf carrier generated by U1. The management (frequency, start up, data I/F etc.) and parametrisation of U1 is handled by U5. The JTAG I/F serves for programming and downloading of the embedded software and is not used during normal operation.