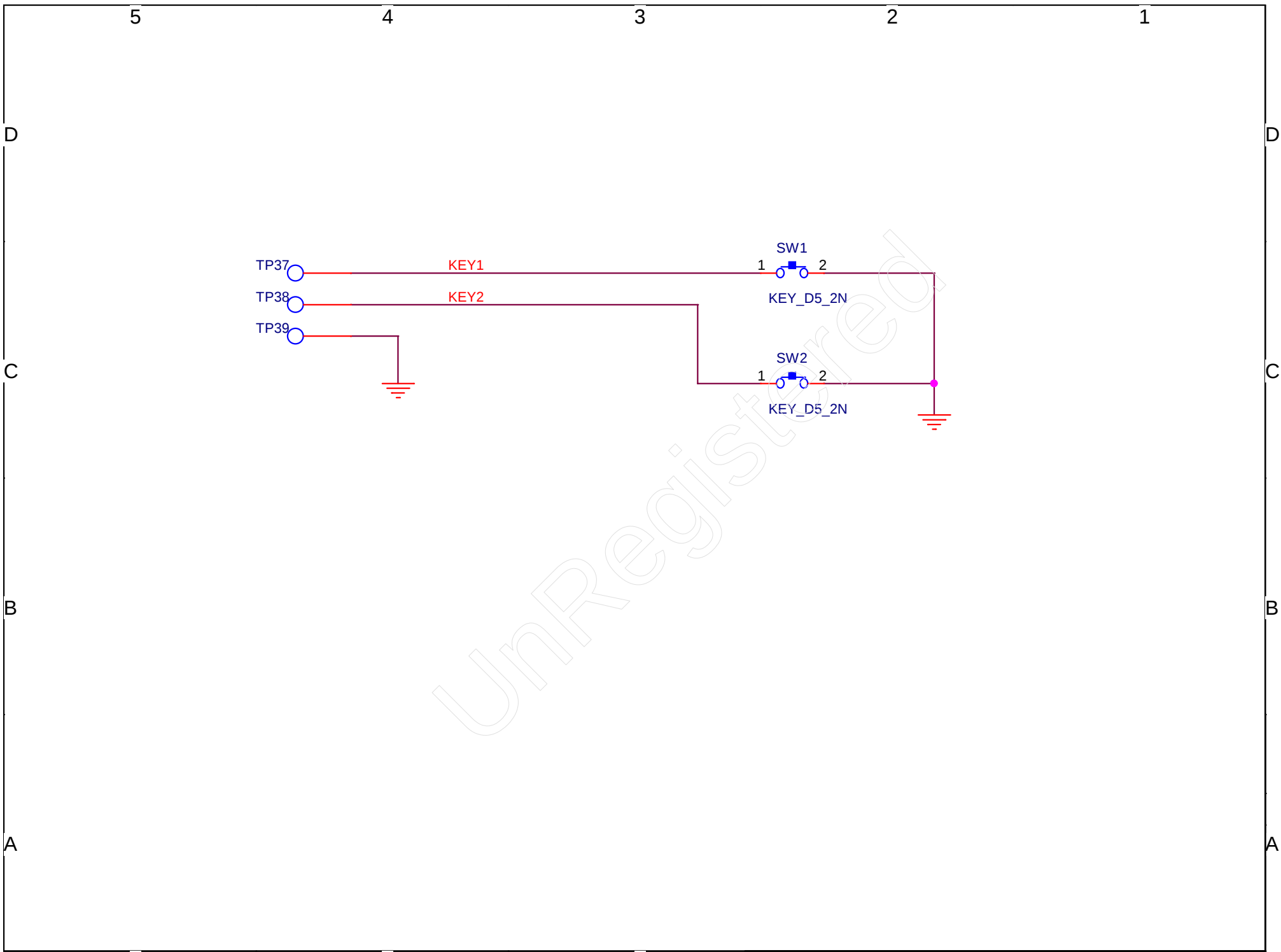
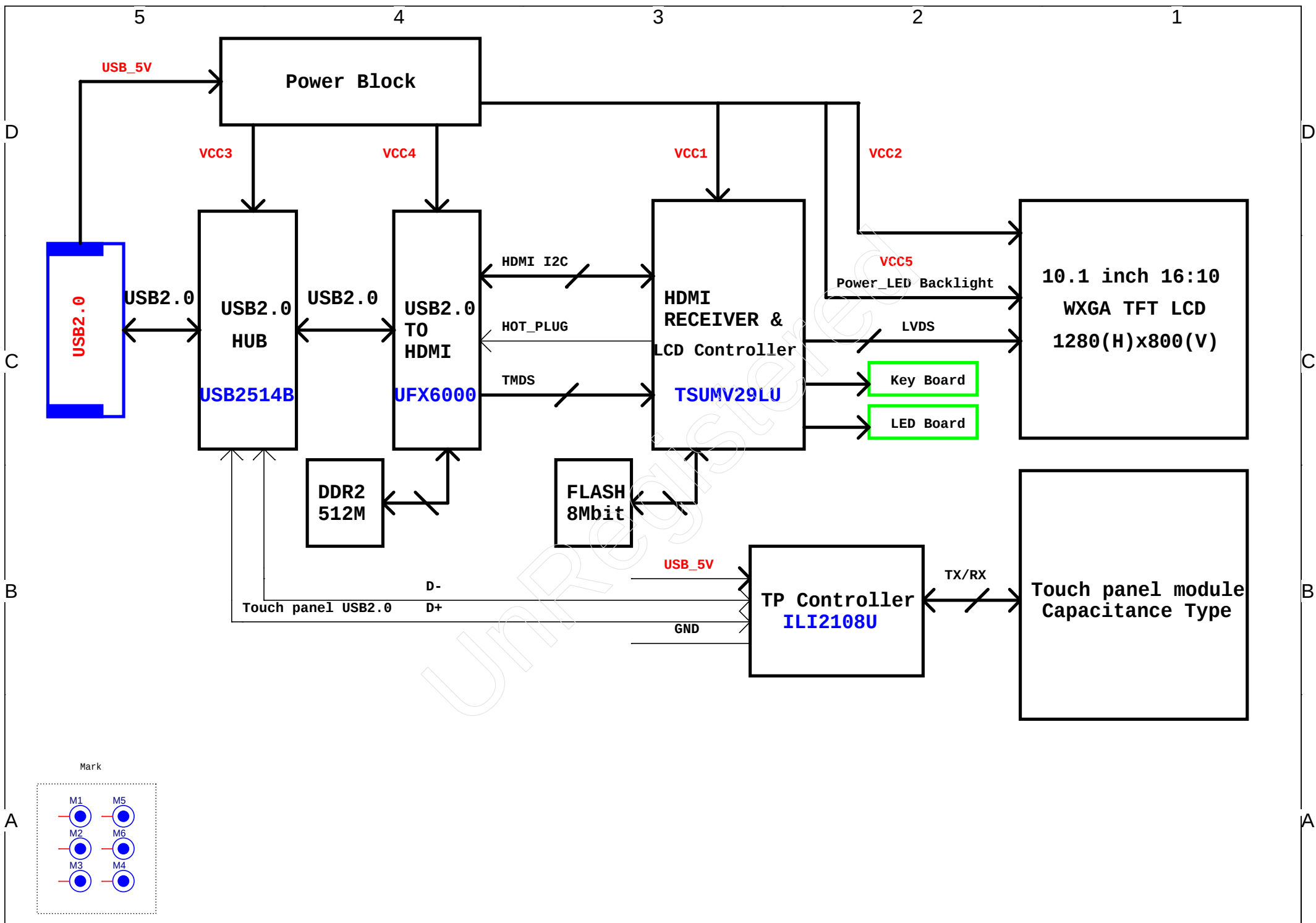






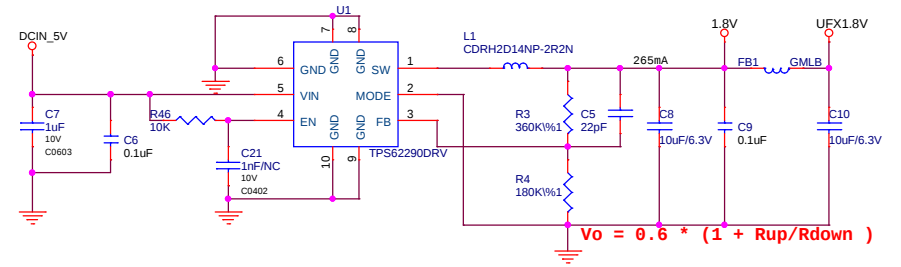
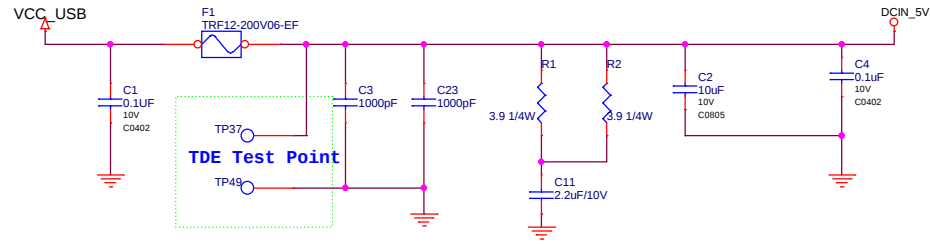
5

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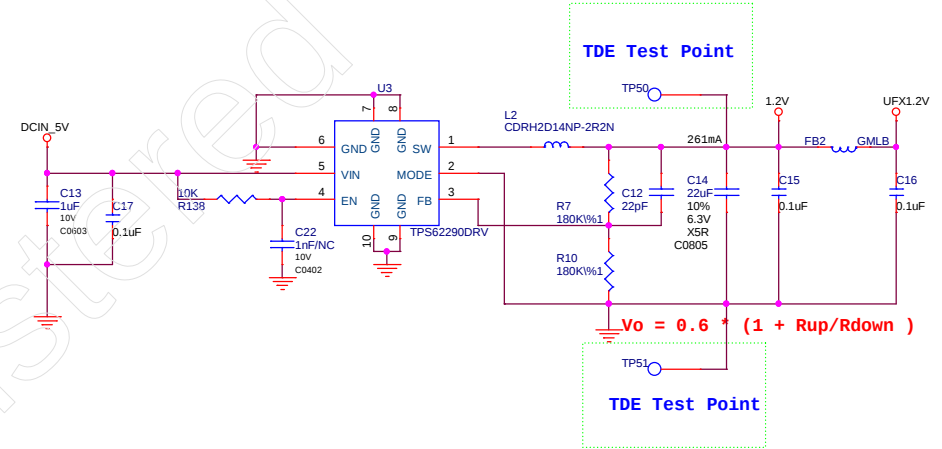
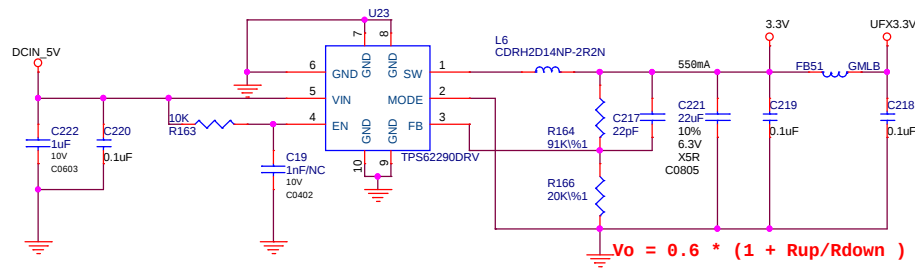


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D

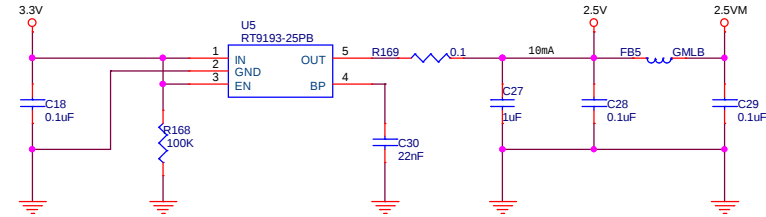
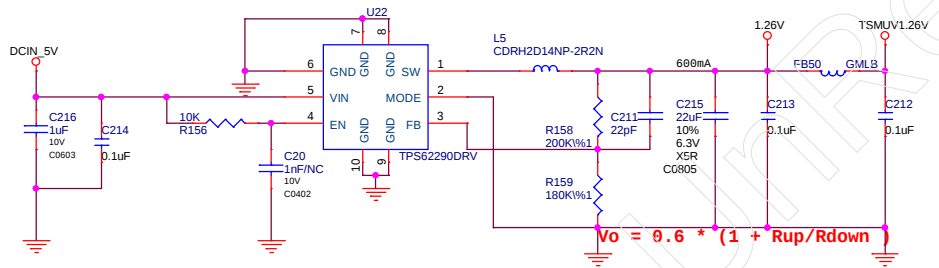
C

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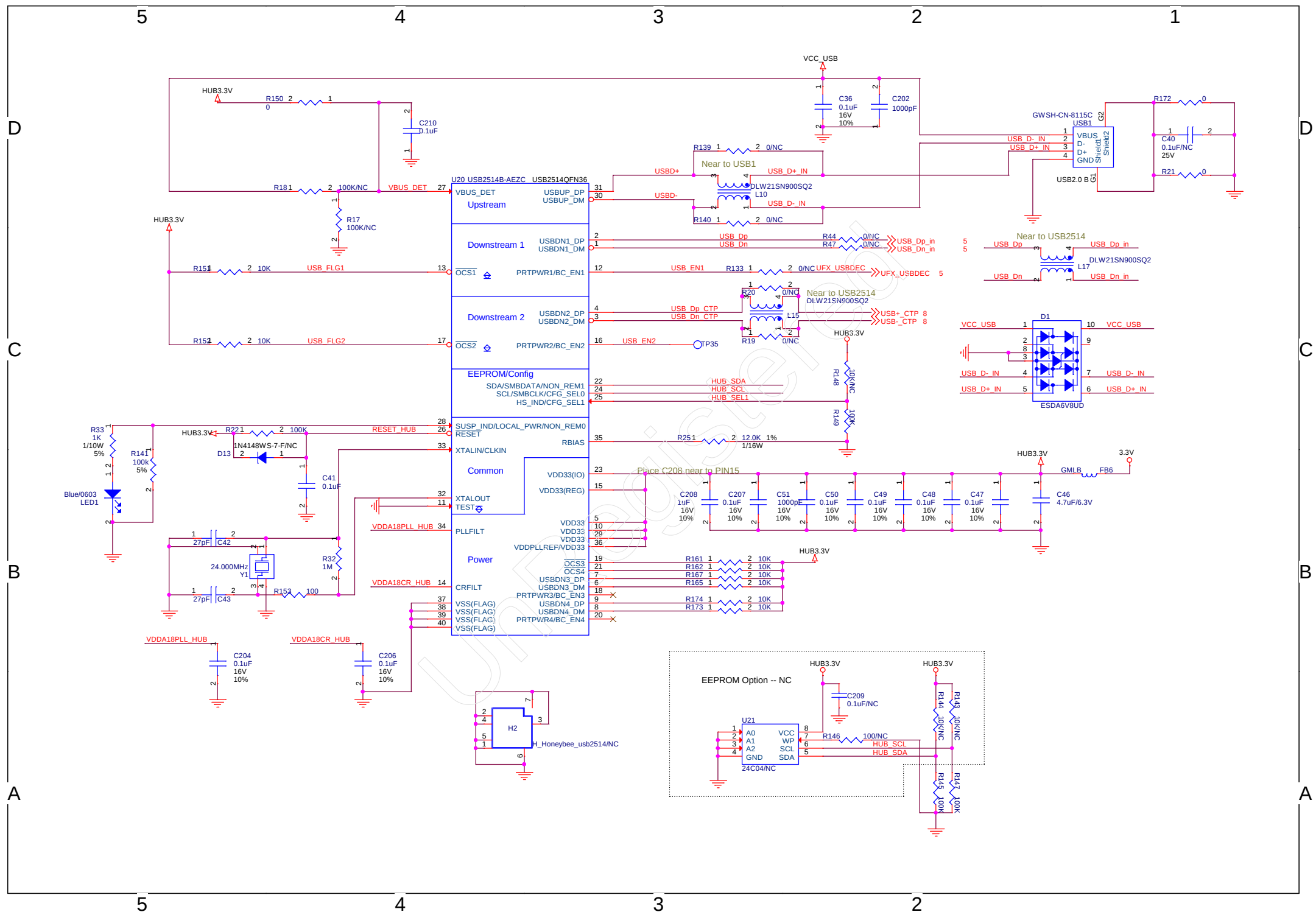
5

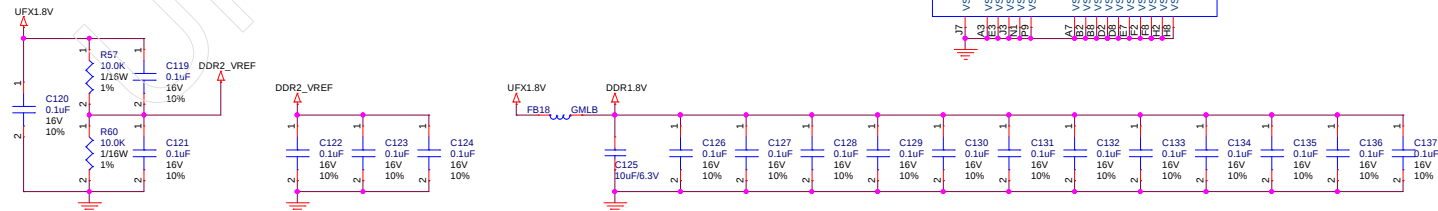
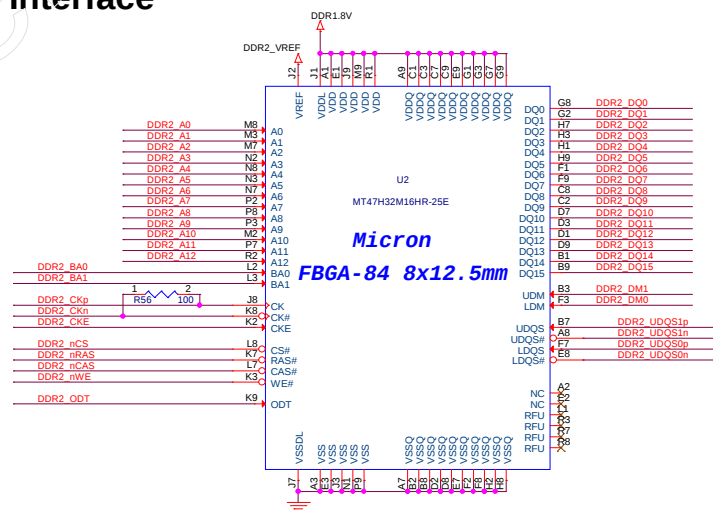
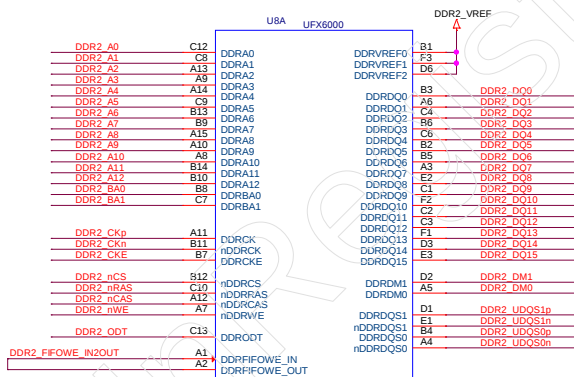
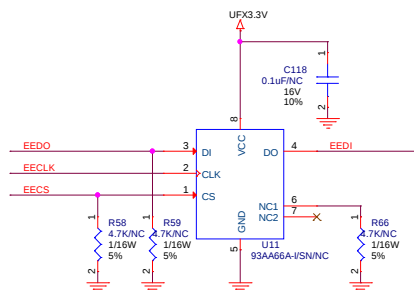
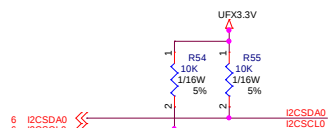
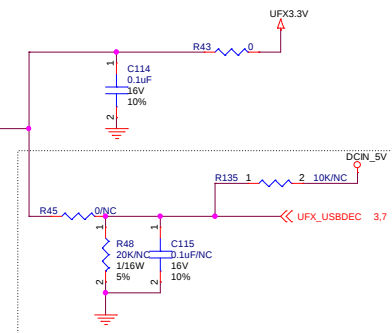
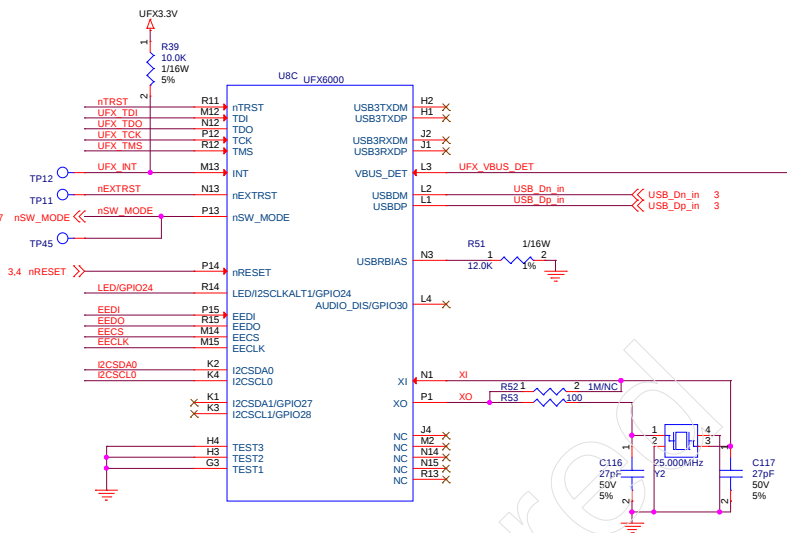
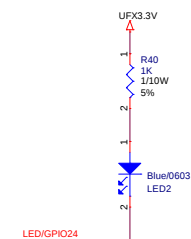
4

3

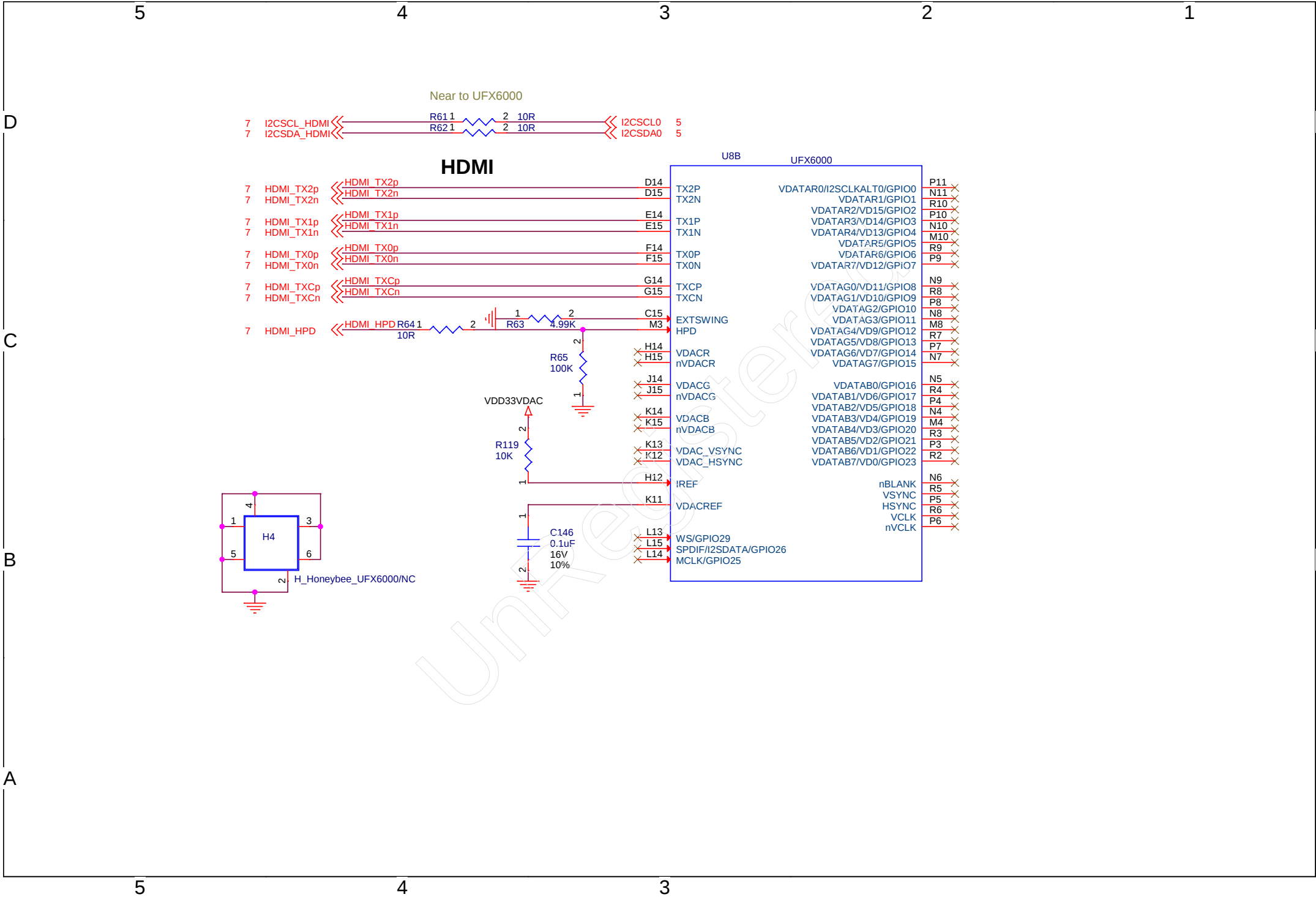
2

1

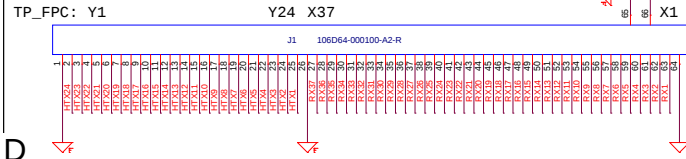




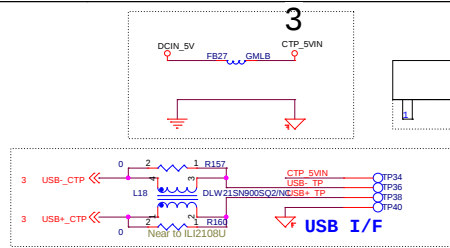
DDR2 Interface



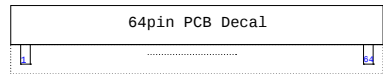
5 Sensor Connection



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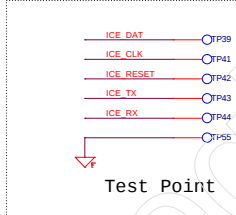
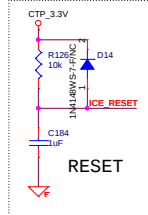
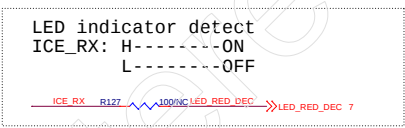
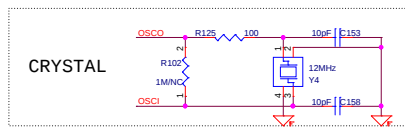
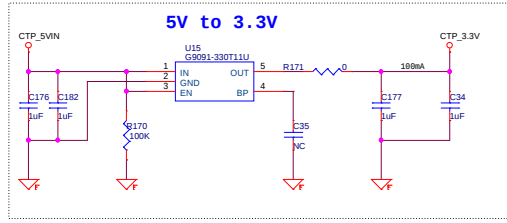
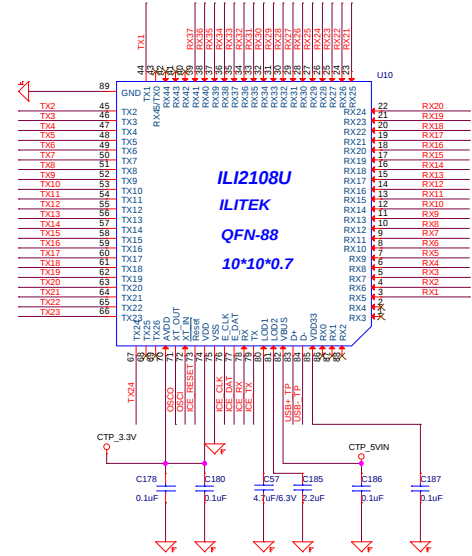
1

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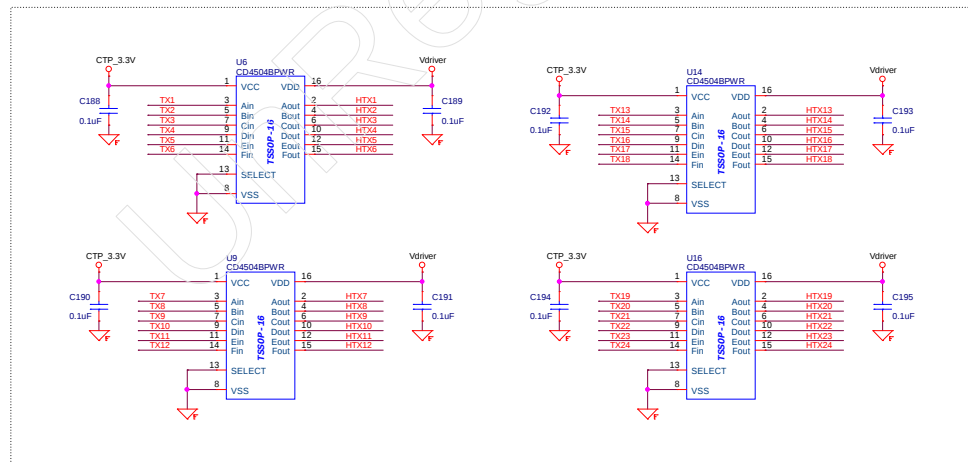
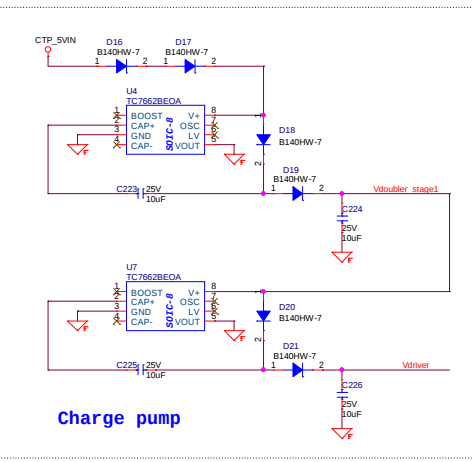
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