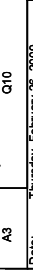
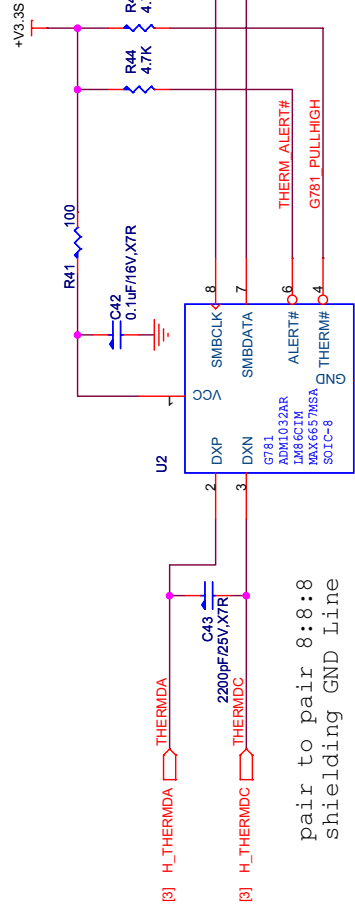


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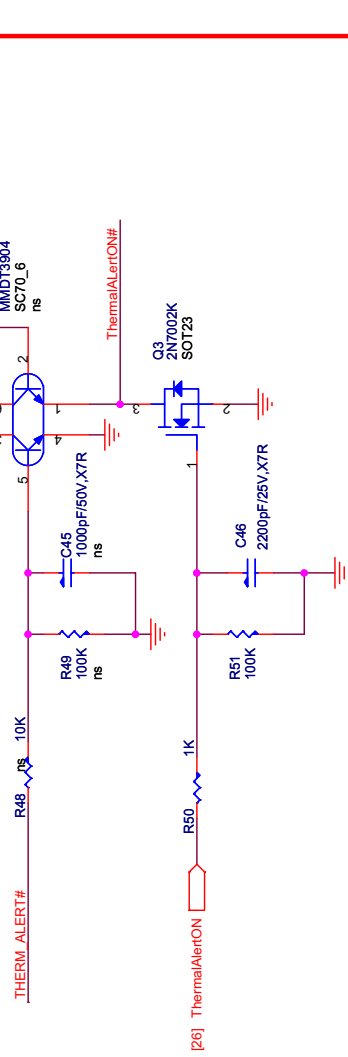


***CPU Thermal***

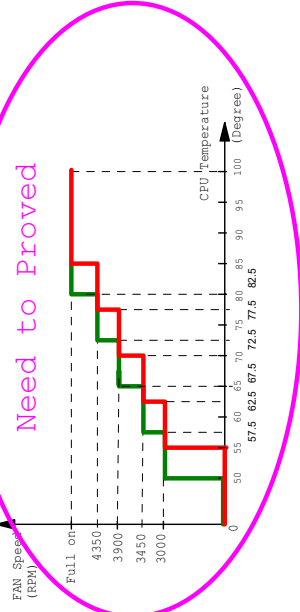


# System Thermal

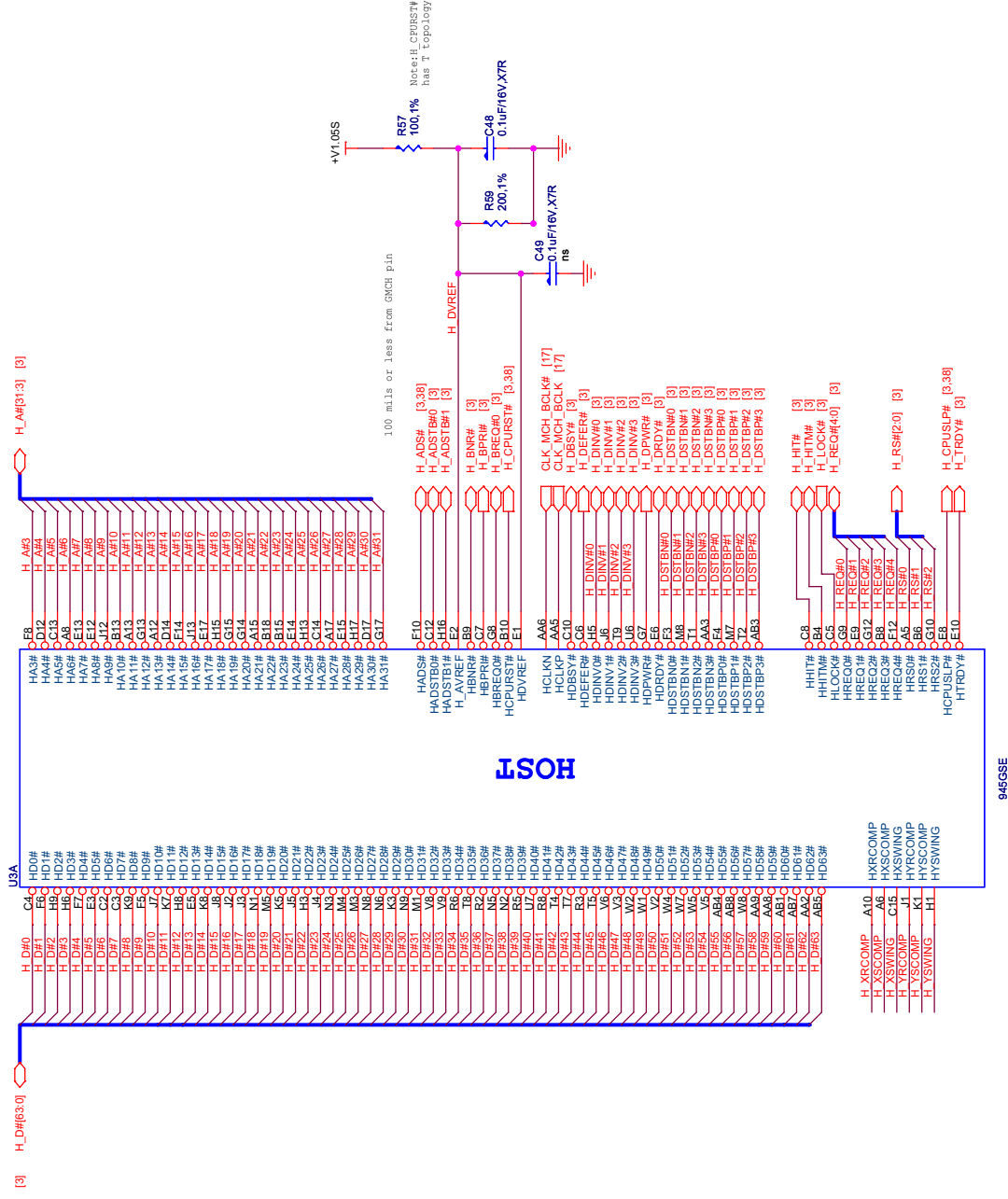
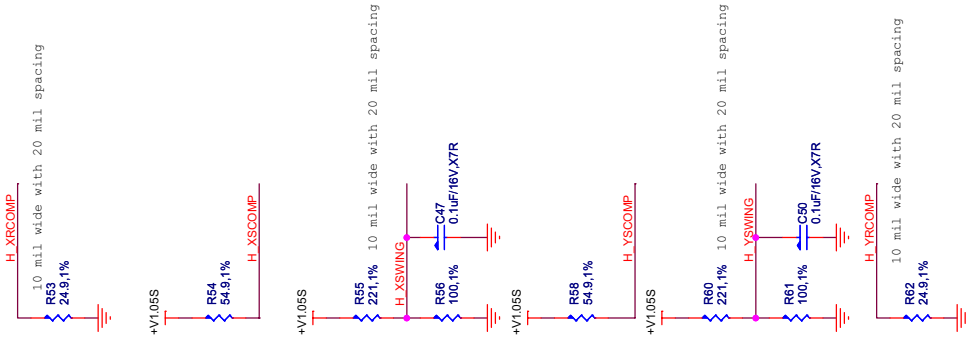
9,35]

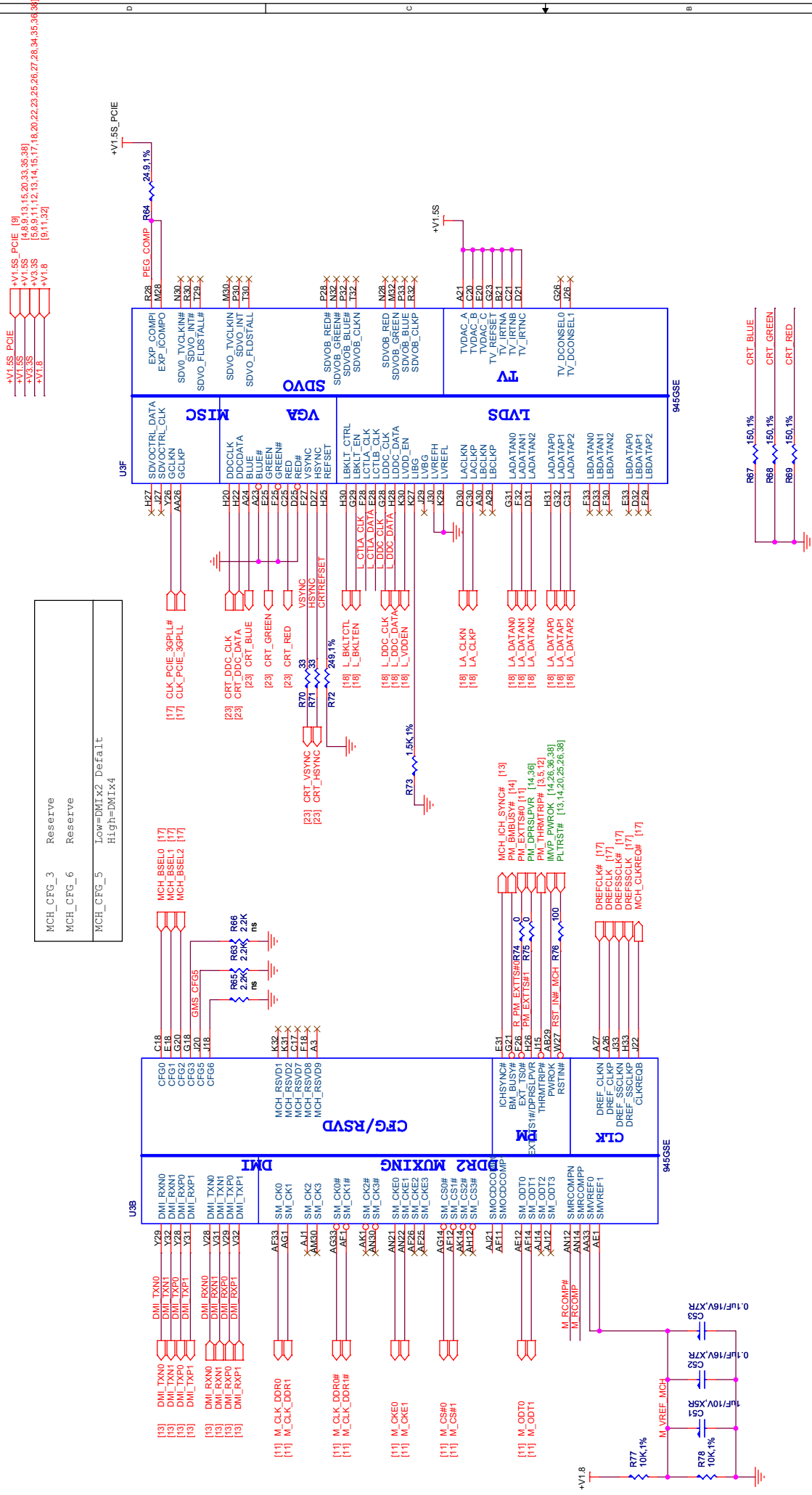


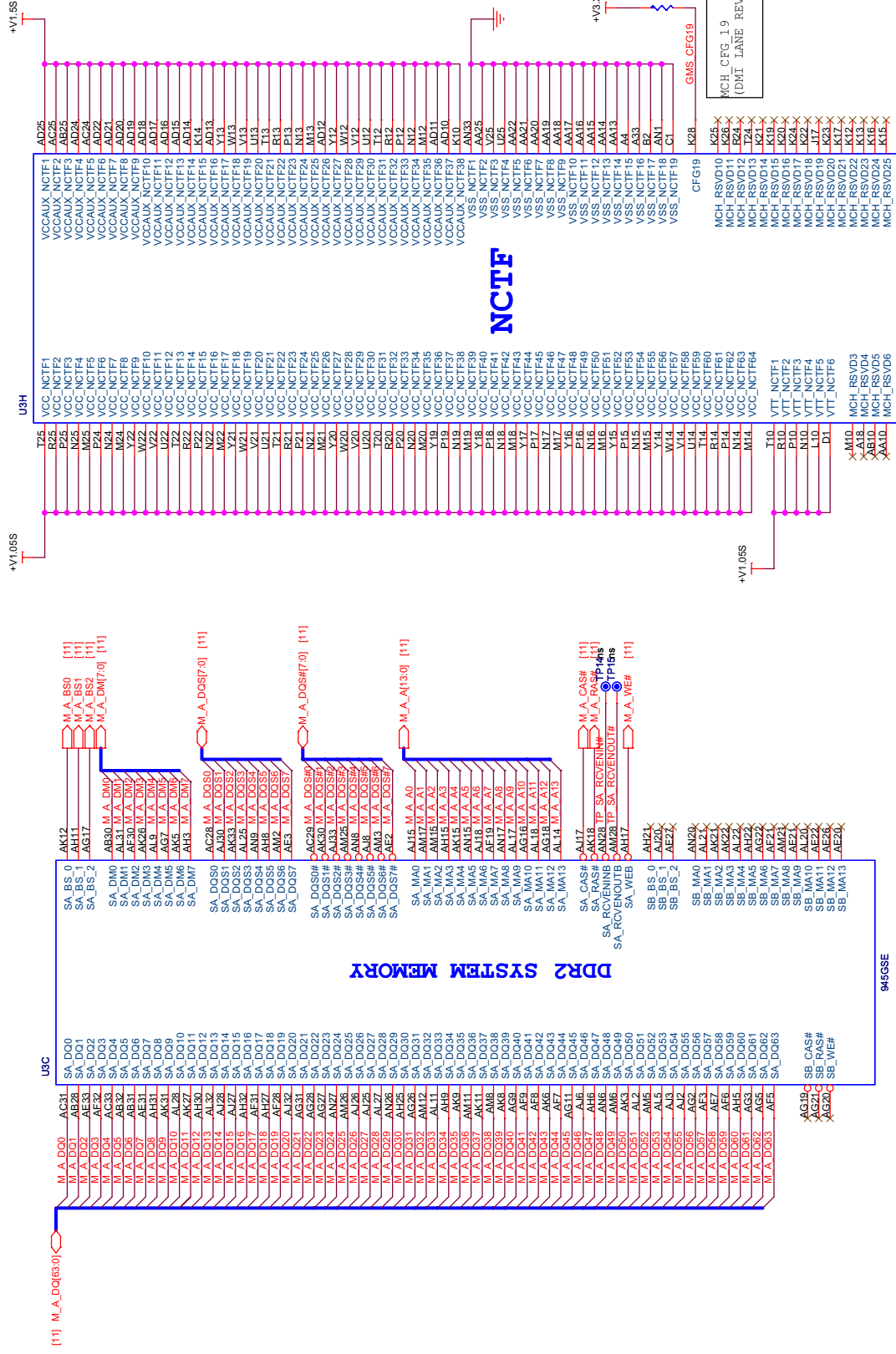
pair to pair 8:8:8  
shielding GND Line



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| Title              |                             |     |               |
| CPU THERMAL SENSOR |                             |     |               |
| Size               | Project Name                | Rev | B             |
| Custom             | Q10                         |     |               |
| Date:              | Thursday, February 26, 2009 |     | Sheet 5 of 48 |







945GSE

945GSE

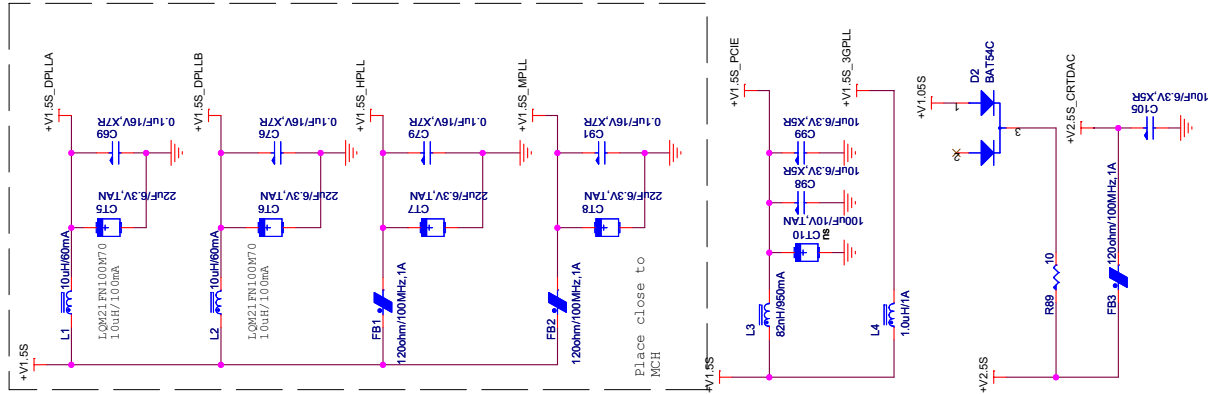
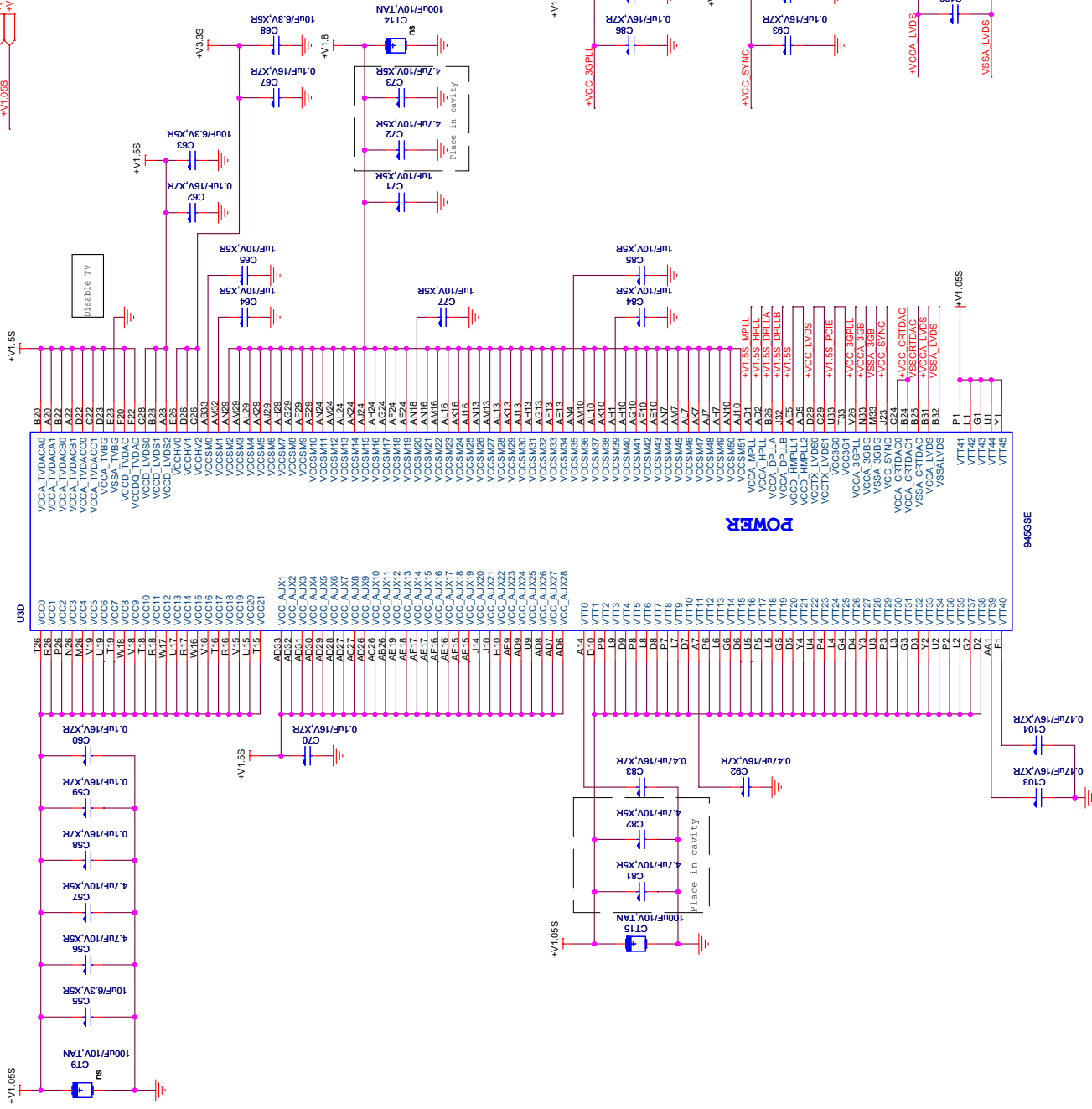
LOW= NORMAL  
HIGH= JAVES REVERSED

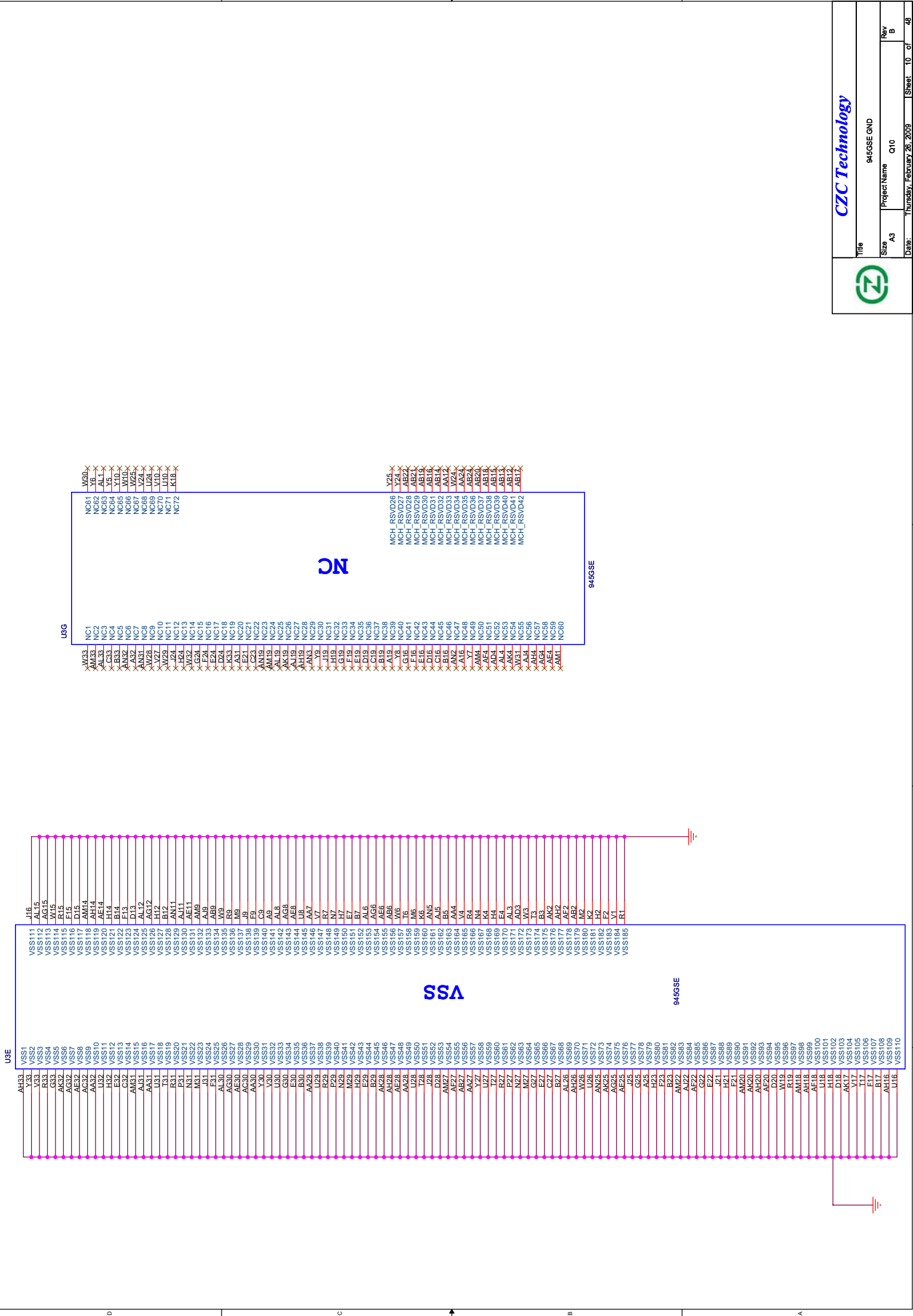


CZC Technology

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| Title |                             | 945GSE DDRII |         |
| Size  | A3                          | Project Name | Q10     |
| Date: | Thursday, February 26, 2003 | Sheet        | 8 of 49 |

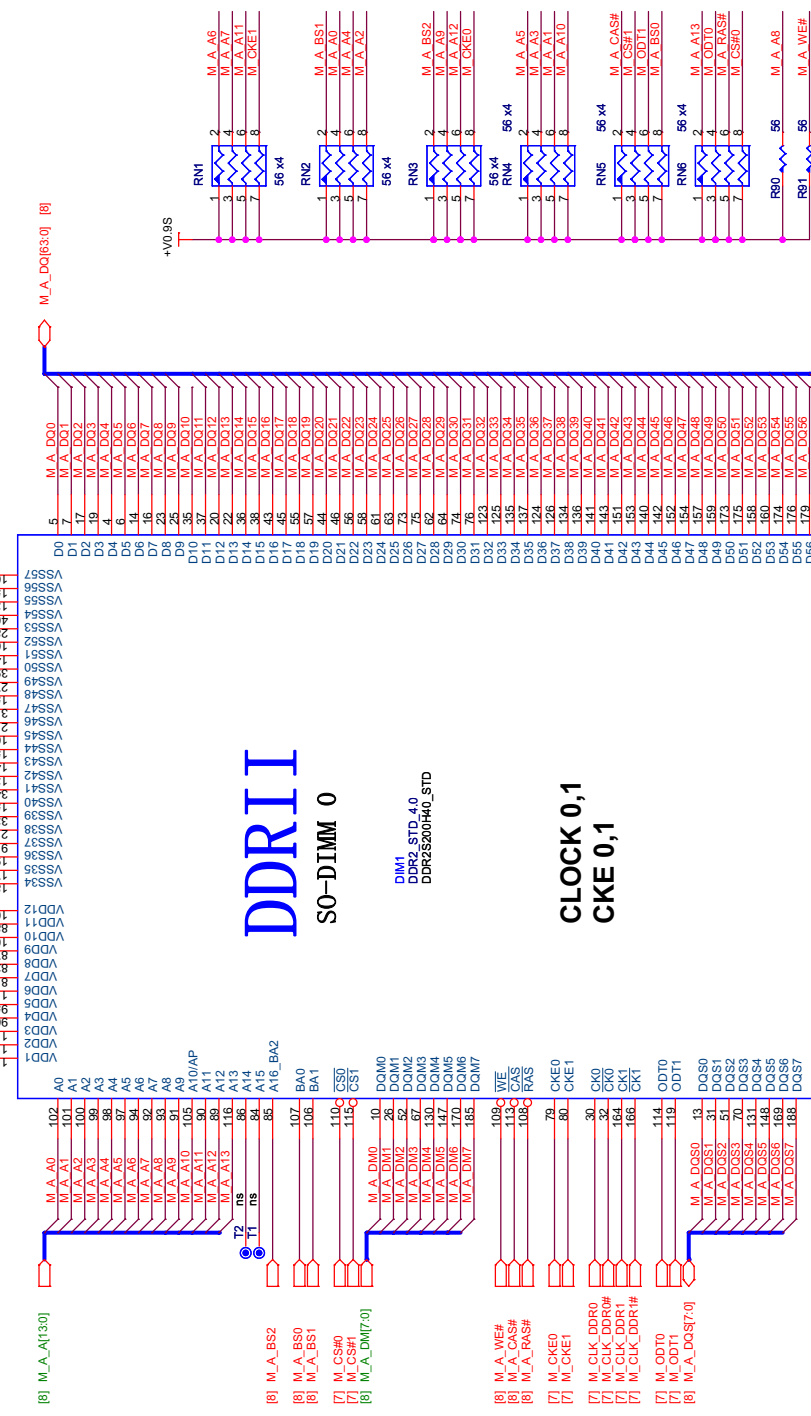
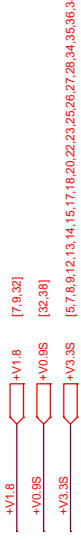
+V2.5S\_Pcie [9,38]  
+V1.5S\_Pcie [4,7,8,13,15,20,33,35,38]  
+V1.5S [5,7,8,11,12,13,14,15,17,18,20,22,23,25,26,27,28,34,35,36,38]  
+V2.5S [7,11,32]  
+V1.05S [9,4,5,6,8,12,15,17,26,33,35,38]





# CZC Technology

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| Title |                             |              | 945GSE GND |          |
| Size  | A3                          | Project Name | Q10        | Rev<br>B |
| Date: | Thursday, February 26, 2009 |              | Sheet:     | 10 of 48 |



Layout note: 电容靠近DDR slot VDD PIN

DDR2 SODIMM 0

DDR2 STD 4.0

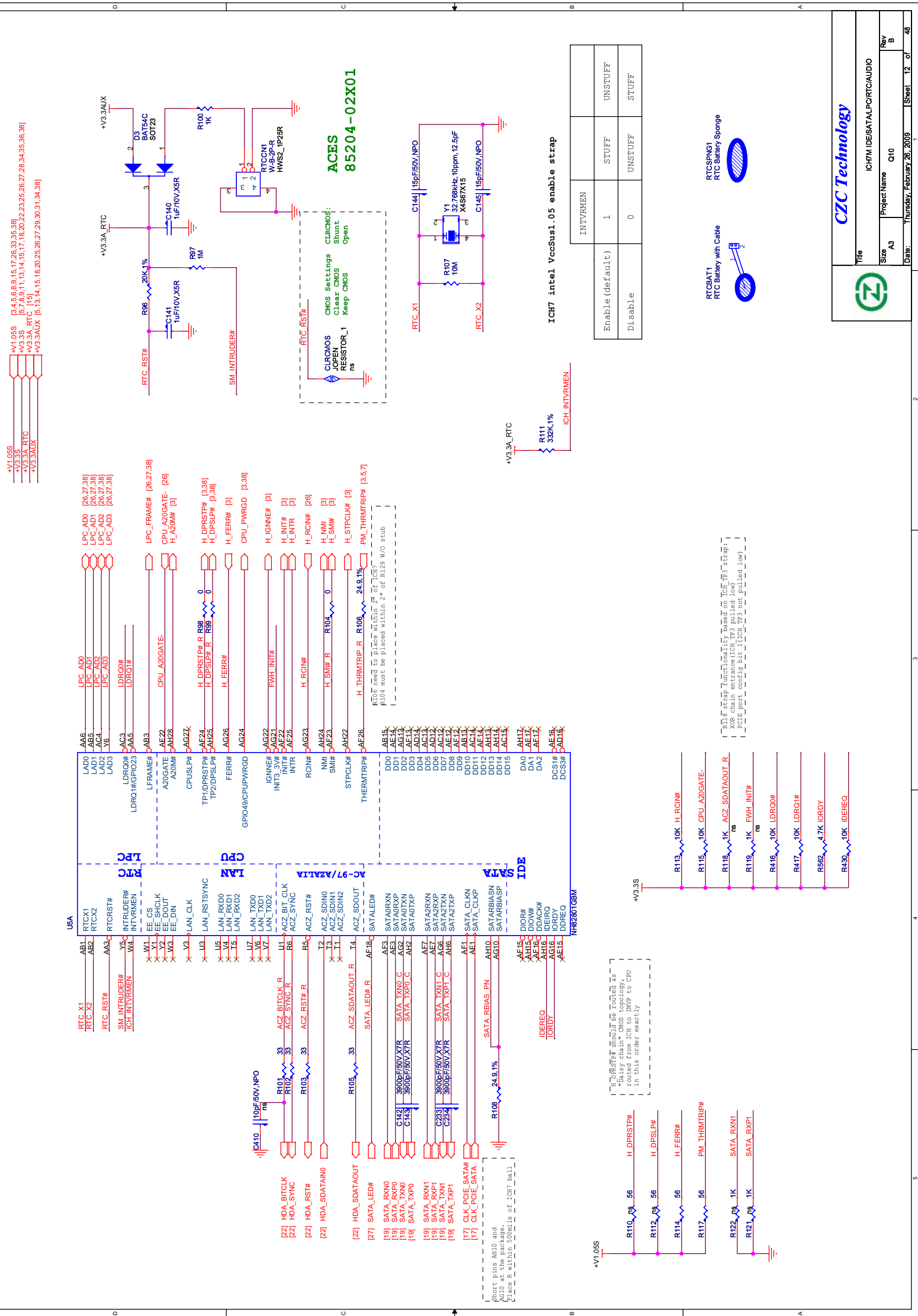
CLOCK 0,1  
CKE 0,1

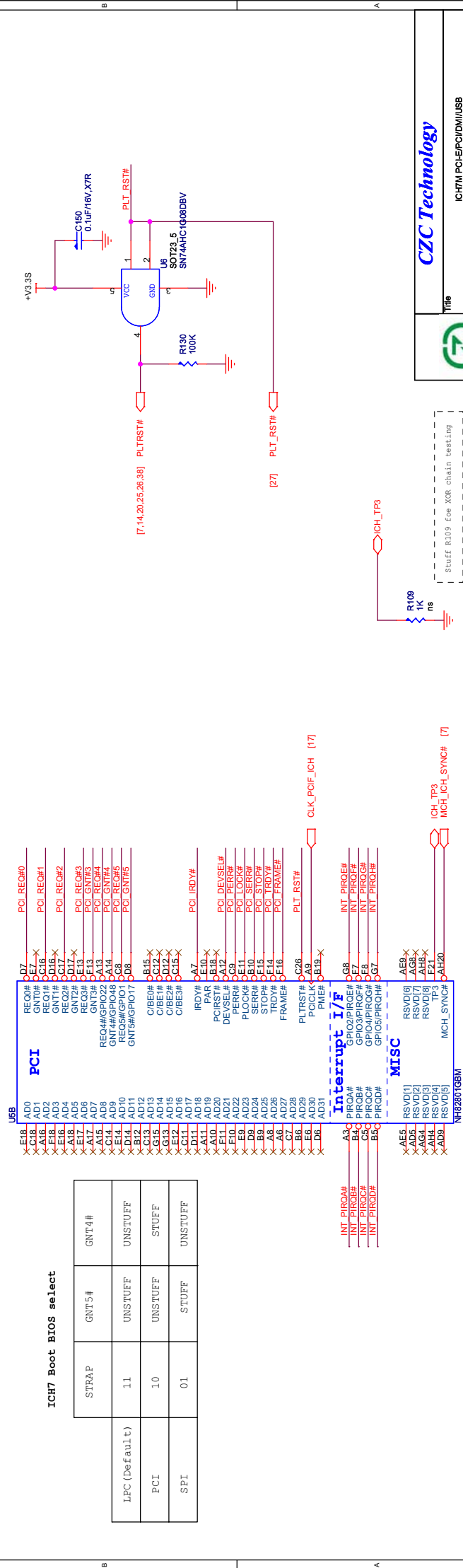
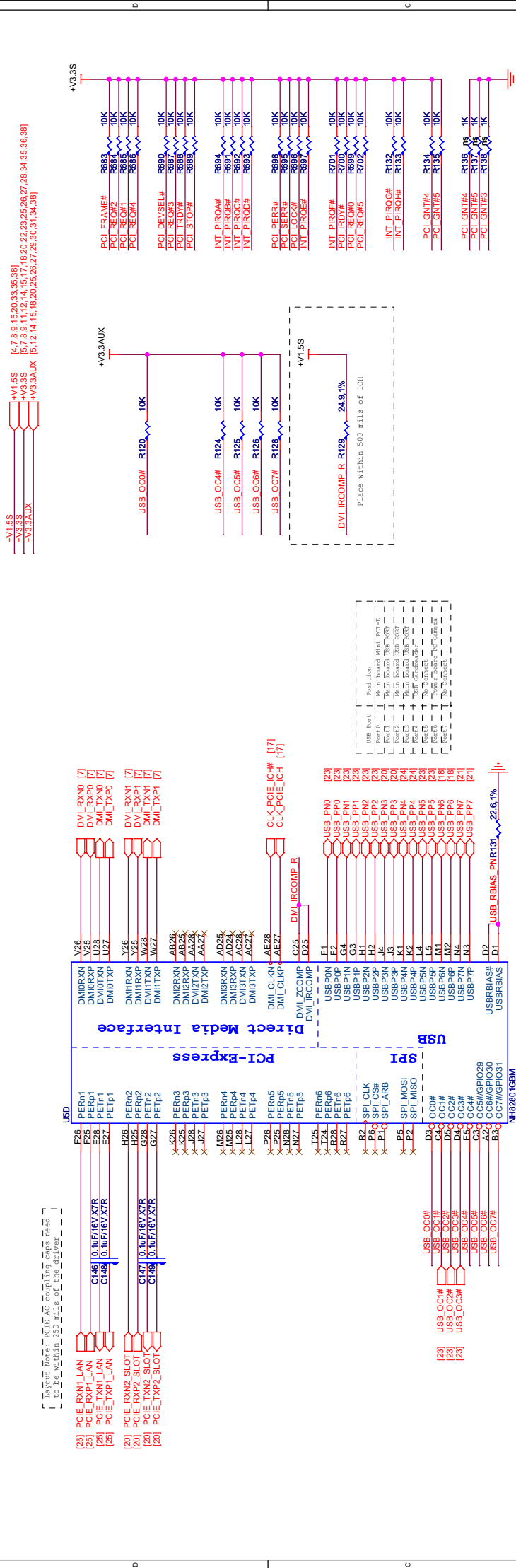
Address: 1010 000x=A0H



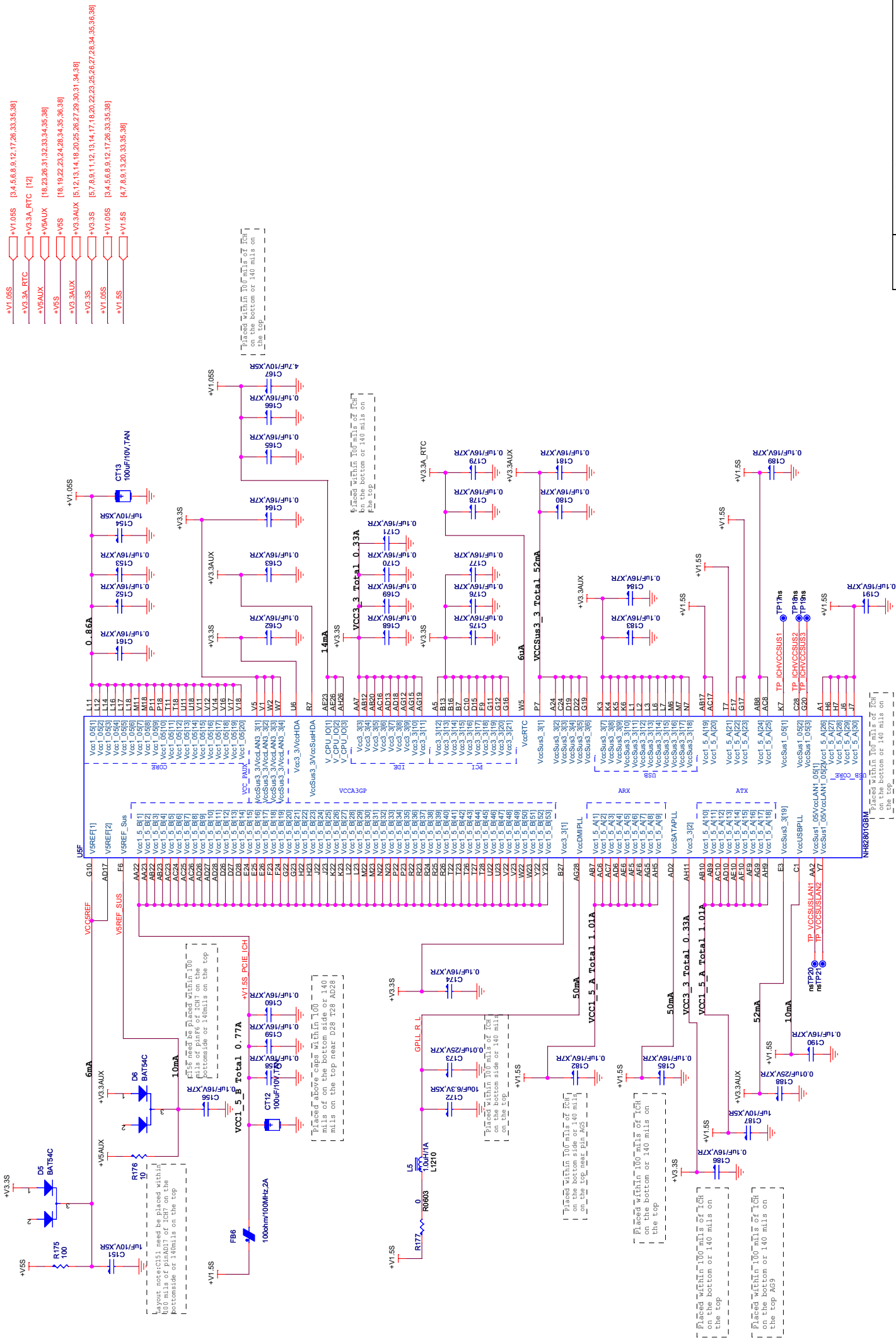
CZC Technology

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| Title |                             | DDR2 SODIMM  |          |
| Size  | A3                          | Project Name | Q10      |
| Date: | Thursday, February 26, 2003 | Sheet        | 11 of 48 |









|     |        |         |      |
|-----|--------|---------|------|
| U5E |        | P28     |      |
| A4  | VSS11  | VSS108  | R1   |
| A23 | VSS21  | VSS109  | R11  |
| B1  | VSS31  | VSS1100 | R12  |
| B4  | VSS44  | VSS101  | R13  |
| B11 | VSS55  | VSS102  | R14  |
| B17 | VSS66  | VSS103  | R15  |
| B20 | VSS77  | VSS104  | R16  |
| B26 | VSS88  | VSS105  | R17  |
| B38 | VSS99  | VSS106  | R18  |
| C2  | VSS101 | VSS107  | R19  |
| C27 | VSS111 | VSS108  | T1   |
| D10 | VSS121 | VSS109  | T13  |
| D13 | VSS131 | VSS110  | T14  |
| D18 | VSS145 | VSS111  | T15  |
| D21 | VSS156 | VSS112  | T16  |
| D41 | VSS167 | VSS113  | T17  |
| E1  | VSS178 | VSS114  | U1   |
| E2  | VSS189 | VSS115  | U12  |
| E4  | VSS191 | VSS116  | U13  |
| E8  | VSS201 | VSS117  | U14  |
| E15 | VSS211 | VSS118  | U15  |
| F3  | VSS221 | VSS119  | U16  |
| F4  | VSS232 | VSS120  | U17  |
| F5  | VSS244 | VSS121  | U18  |
| F12 | VSS255 | VSS122  | U19  |
| F27 | VSS266 | VSS123  | U25  |
| F28 | VSS278 | VSS124  | U26  |
| G1  | VSS288 | VSS125  | V2   |
| G5  | VSS299 | VSS126  | V13  |
| G6  | VSS301 | VSS127  | V15  |
| G8  | VSS311 | VSS128  | V16  |
| G18 | VSS321 | VSS129  | V27  |
| G21 | VSS331 | VSS130  | W6   |
| G25 | VSS341 | VSS131  | W24  |
| H3  | VSS351 | VSS132  | W25  |
| H4  | VSS361 | VSS133  | W26  |
| H5  | VSS371 | VSS134  | Y24  |
| H7  | VSS381 | VSS135  | Y27  |
| H26 | VSS391 | VSS136  | AA1  |
| I1  | VSS401 | VSS137  | AA24 |
| I2  | VSS411 | VSS138  | AA25 |
| J5  | VSS421 | VSS139  | AA26 |
| J6  | VSS431 | VSS140  | AB6  |
| J26 | VSS441 | VSS141  | AB11 |
| K27 | VSS451 | VSS142  | AB14 |
| L13 | VSS461 | VSS143  | AB16 |
| L24 | VSS471 | VSS144  | AB21 |
| L26 | VSS481 | VSS145  | AB24 |
| M3  | VSS491 | VSS146  | AB27 |
| M12 | VSS501 | VSS147  | AC2  |
| M13 | VSS511 | VSS148  | AC11 |
| M14 | VSS521 | VSS149  | AC9  |
| M15 | VSS531 | VSS150  | AD1  |
| M16 | VSS541 | VSS151  | AD3  |
| M17 | VSS551 | VSS152  | AD4  |
| M27 | VSS561 | VSS153  | AD7  |
| N1  | VSS571 | VSS154  | AD8  |
| N2  | VSS581 | VSS155  | AD11 |
| N5  | VSS591 | VSS156  | AD15 |
| N11 | VSS601 | VSS157  | AD19 |
| N12 | VSS611 | VSS158  | AD23 |
| N13 | VSS621 | VSS159  | AE2  |
| N14 | VSS631 | VSS160  | AE8  |
| N15 | VSS641 | VSS161  | AE11 |
| N16 | VSS651 | VSS162  | AE13 |
| N17 | VSS661 | VSS163  | AE18 |
| N18 | VSS671 | VSS164  | AE21 |
| N22 | VSS681 | VSS165  | AE25 |
| N23 | VSS691 | VSS166  | AF4  |
| N24 | VSS701 | VSS167  | AF8  |
| N25 | VSS711 | VSS168  | AG3  |
| N26 | VSS721 | VSS169  | AG7  |
| N27 | VSS731 | VSS170  | AG11 |
| N28 | VSS741 | VSS171  | AG14 |
| N29 | VSS751 | VSS172  | AG18 |
| N30 | VSS761 | VSS173  | AG20 |
| N31 | VSS771 | VSS174  | AG25 |
| N32 | VSS781 | VSS175  | AG26 |
| N33 | VSS791 | VSS176  | AG27 |
| N34 | VSS801 | VSS177  | AG28 |
| N35 | VSS811 | VSS178  | AG29 |
| N36 | VSS821 | VSS179  | AG30 |
| N37 | VSS831 | VSS180  | AG31 |
| N38 | VSS841 | VSS181  | AG32 |
| N39 | VSS851 | VSS182  | AG33 |
| N40 | VSS861 | VSS183  | AG34 |
| N41 | VSS871 | VSS184  | AG35 |
| N42 | VSS881 | VSS185  | AG36 |
| N43 | VSS891 | VSS186  | AG37 |
| N44 | VSS901 | VSS187  | AG38 |
| N45 | VSS911 | VSS188  | AG39 |
| N46 | VSS921 | VSS189  | AG40 |
| N47 | VSS931 | VSS190  | AG41 |
| N48 | VSS941 | VSS191  | AG42 |
| N49 | VSS951 | VSS192  | AG43 |
| N50 | VSS961 | VSS193  | AG44 |
| N51 | VSS971 | VSS194  | AG45 |

NH82801GBM

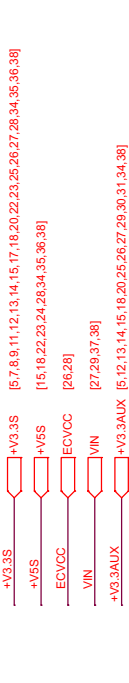


CZC Technology

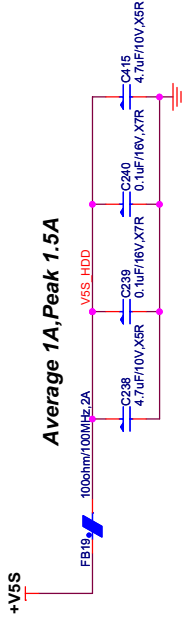
|       |                             |              |          |
|-------|-----------------------------|--------------|----------|
| Title |                             | ICHTM GND    |          |
| Size  | A3                          | Project Name | Q10      |
| Date: | Thursday, February 26, 2009 | Sheet        | 16 of 49 |





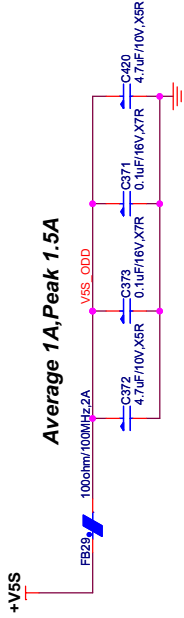


SATA HDD

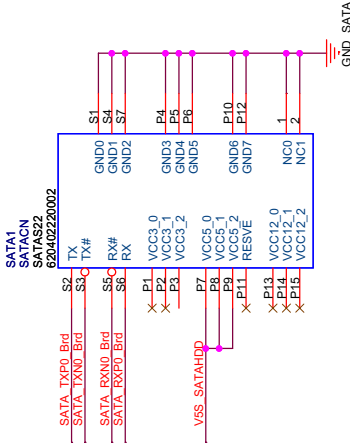


Average 1A, Peak 1.5A

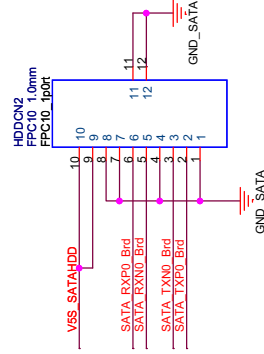
SATA ODD



Average 1A, Peak 1.5A



Alltop C16614-122A4-Y



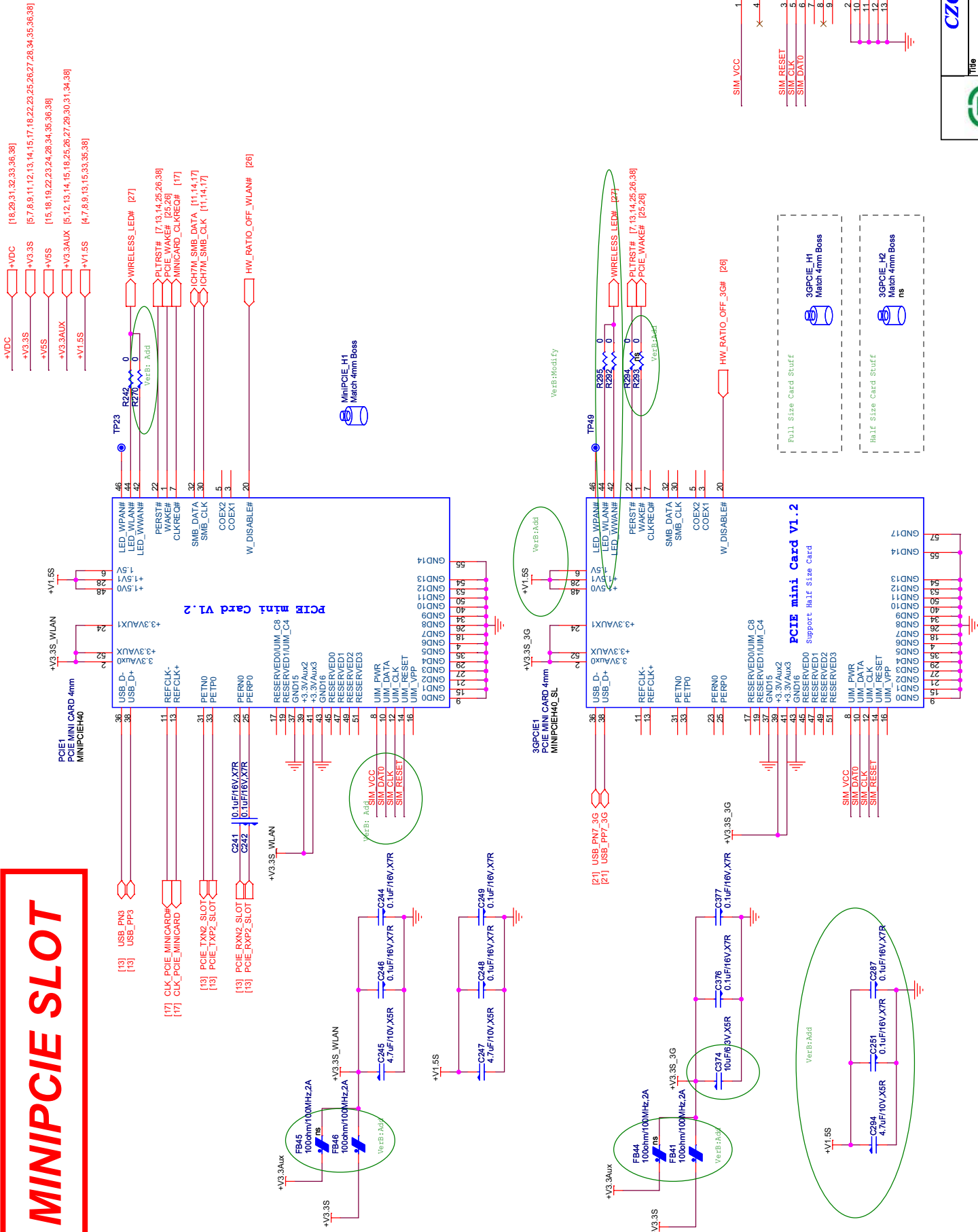
SATAC8L1  
SATA 10P 1mmPitch FFC cable XXXmmL

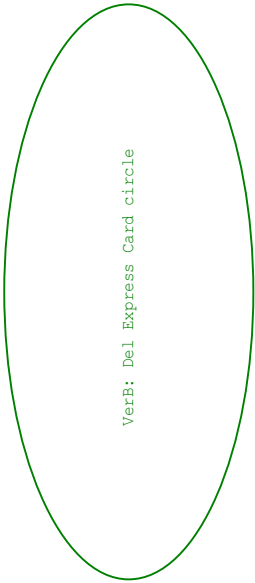
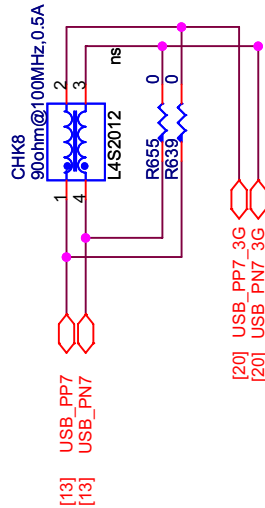


CZC Technology

|       |                             |              |          |
|-------|-----------------------------|--------------|----------|
| Title |                             | SATAMISC     |          |
| Size  | A3                          | Project Name | Q10      |
| Date: | Thursday, February 26, 2009 | Sheet        | 19 of 49 |

**MINIPCIE SLOT**





**CZC Technology**

Title  
BLOCK DIAGRAM

Size  
A4

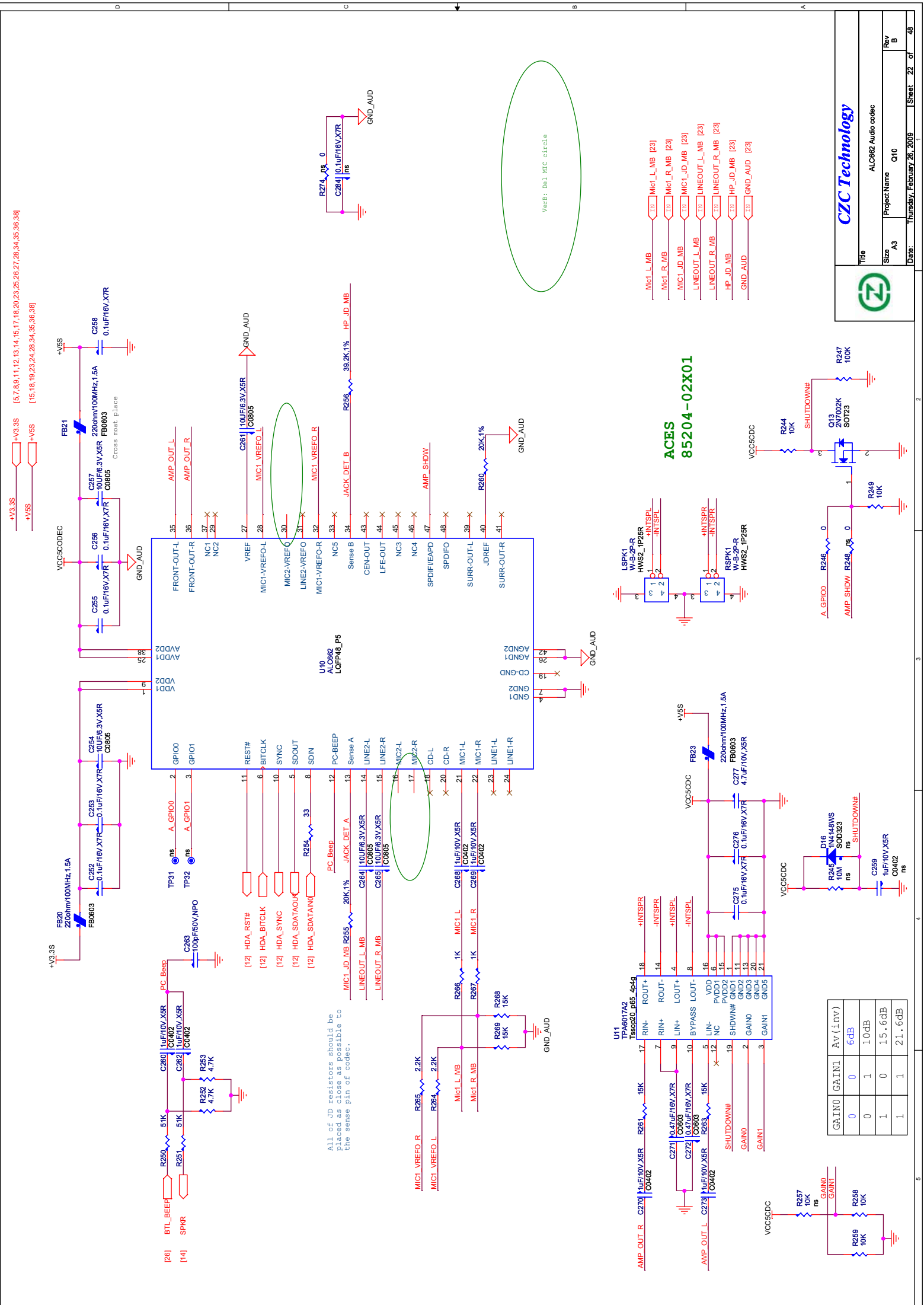
Project Name  
Q10

Rev  
B

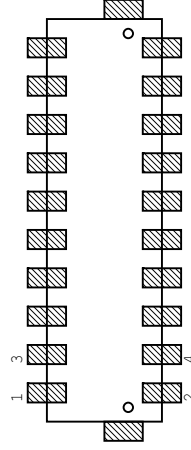
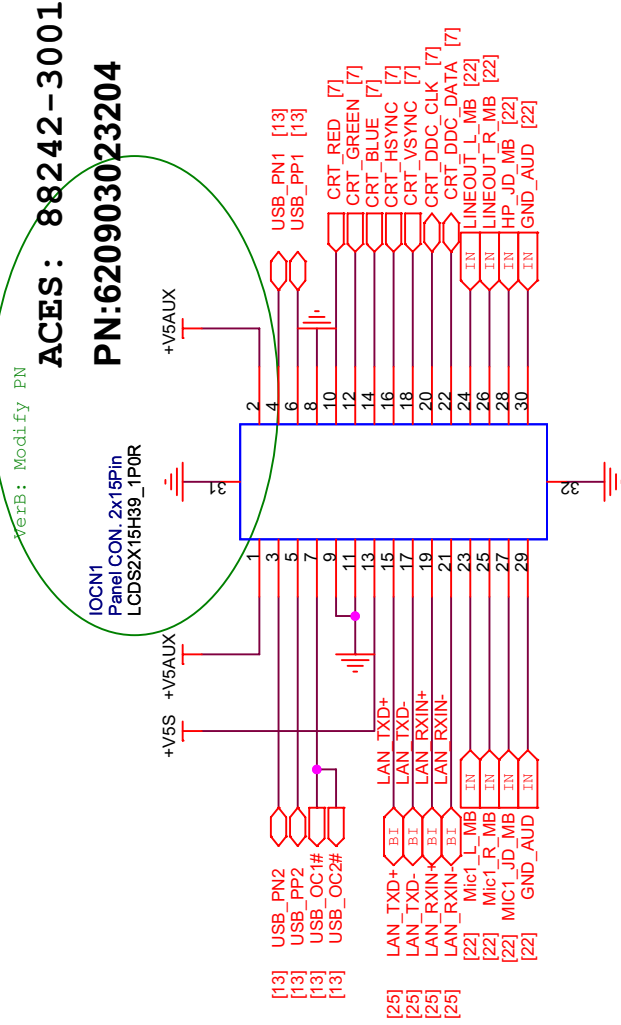
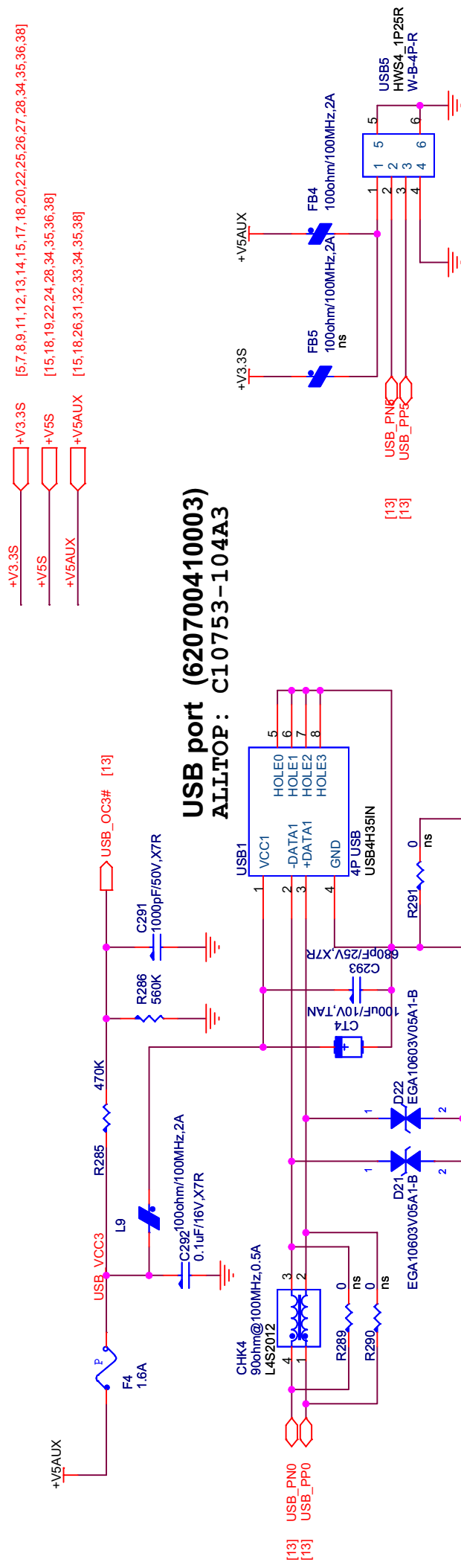
Date:  
Thursday, February 26, 2009

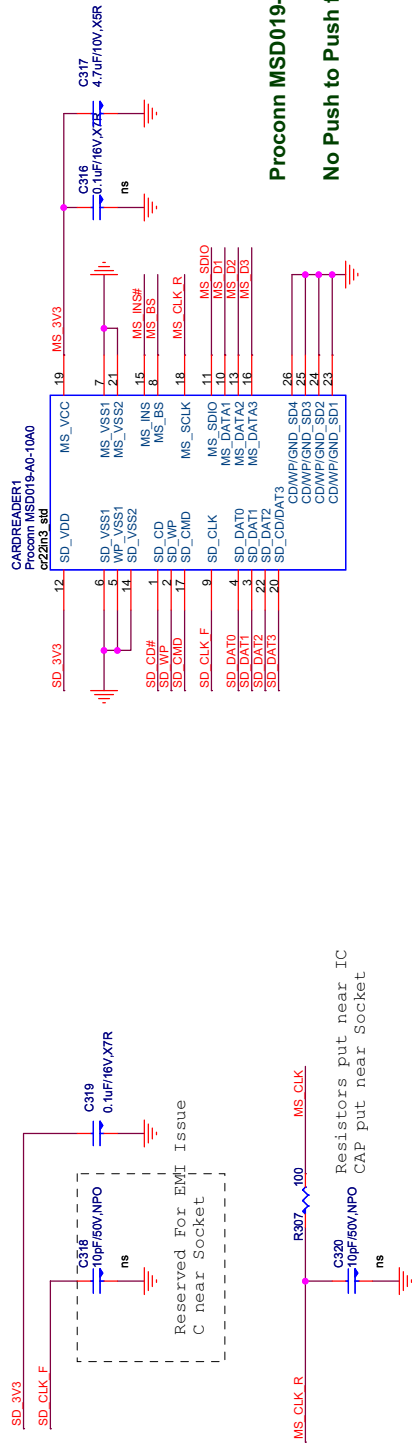
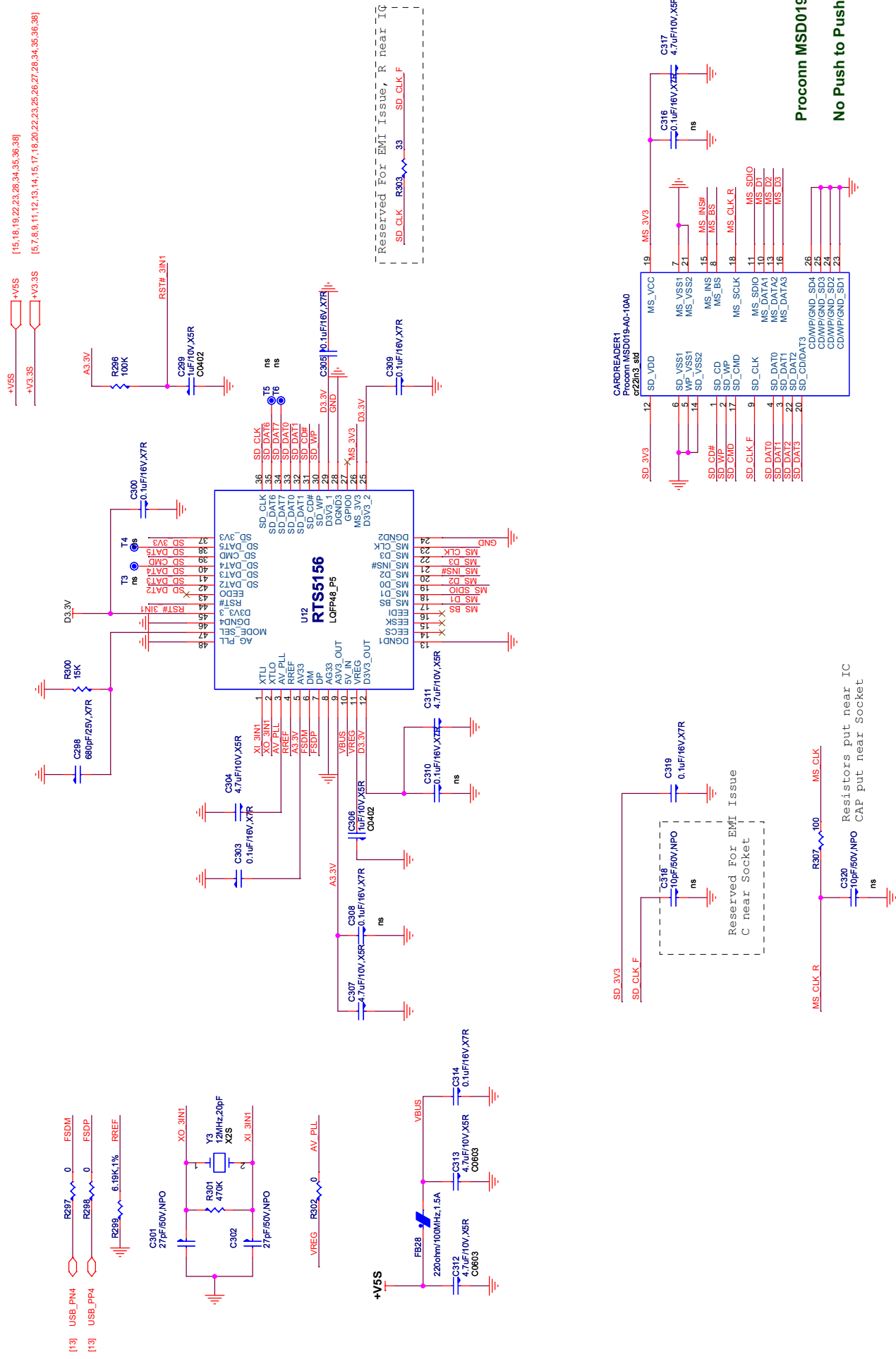
Sheet  
21

of  
48



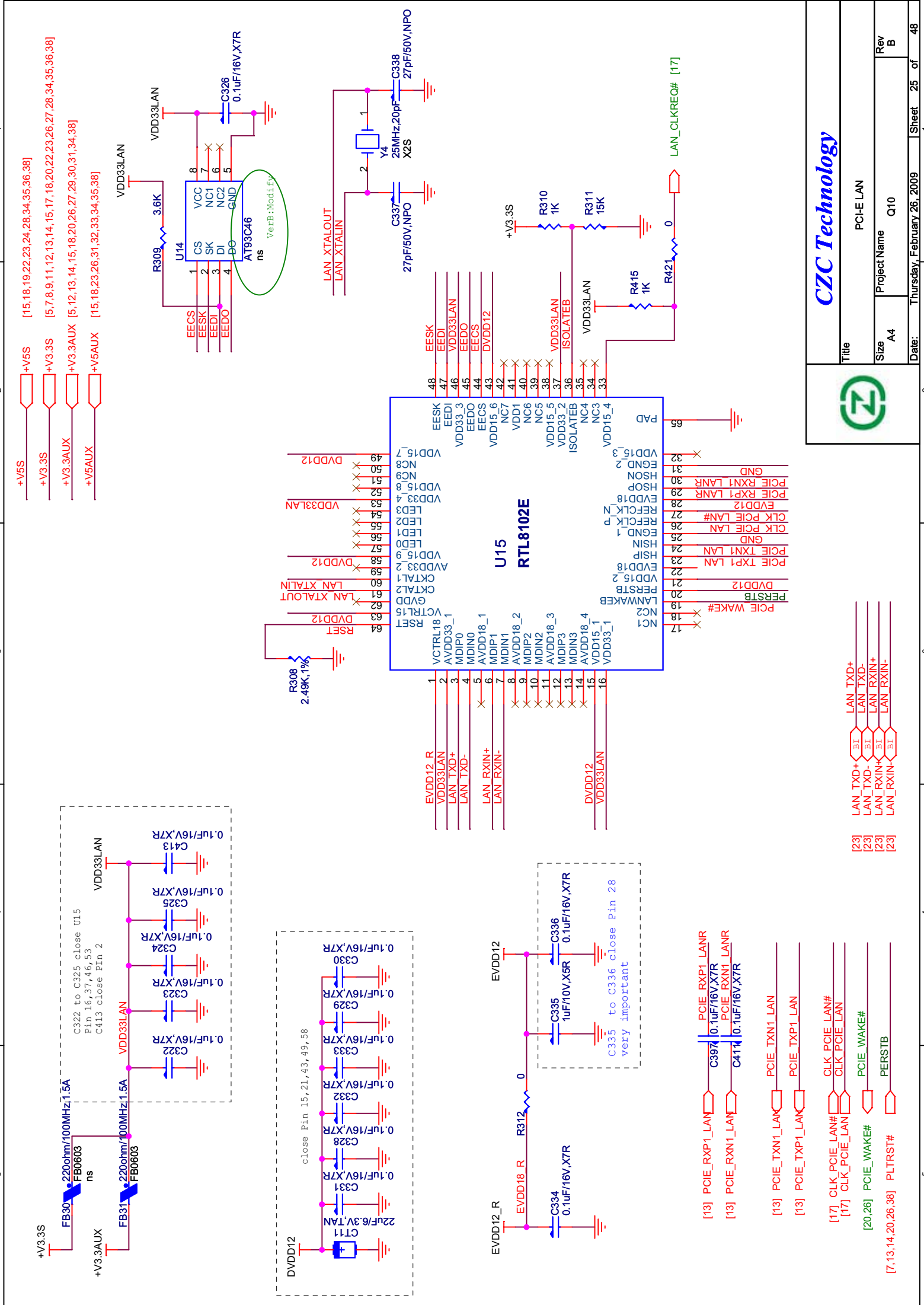
| Title |                             | ALC662 Audio codec |          |
|-------|-----------------------------|--------------------|----------|
| Size  | Project Name                | Q10                | Rev      |
| A3    |                             |                    | B        |
| Date: | Thursday, February 26, 2009 | Sheet              | 22 of 49 |

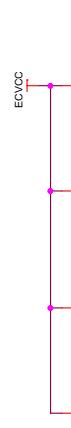
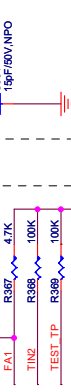




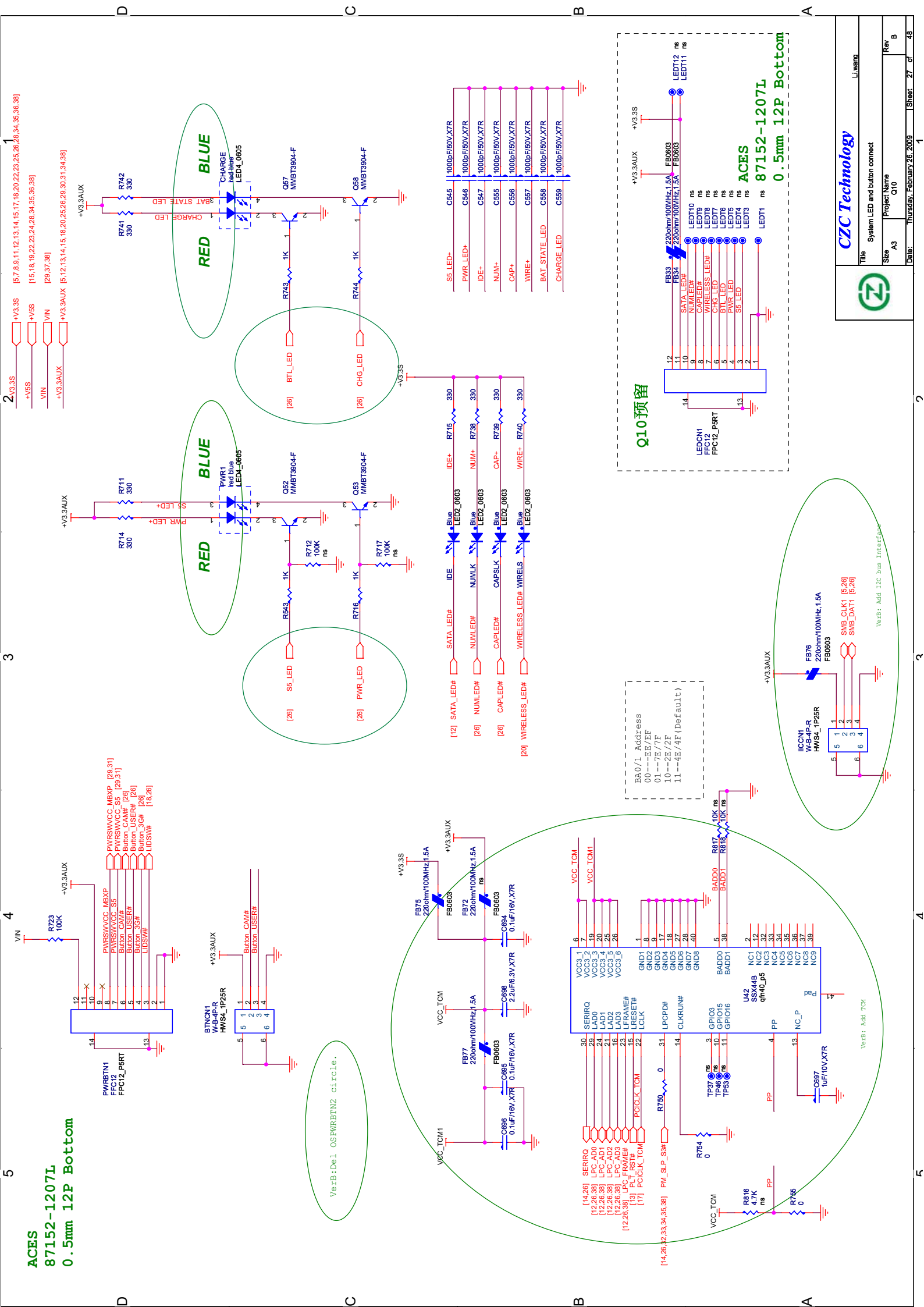
Proconn MSD019-A0-10A0

**No Push to Push function.**





ACES  
87152-1207L  
0.5mm 12P Bottom





011: Reserved



ACES



昇平



| Friction | P. M2 | P. M1 | P. M0 |
|----------|-------|-------|-------|
|----------|-------|-------|-------|

\*

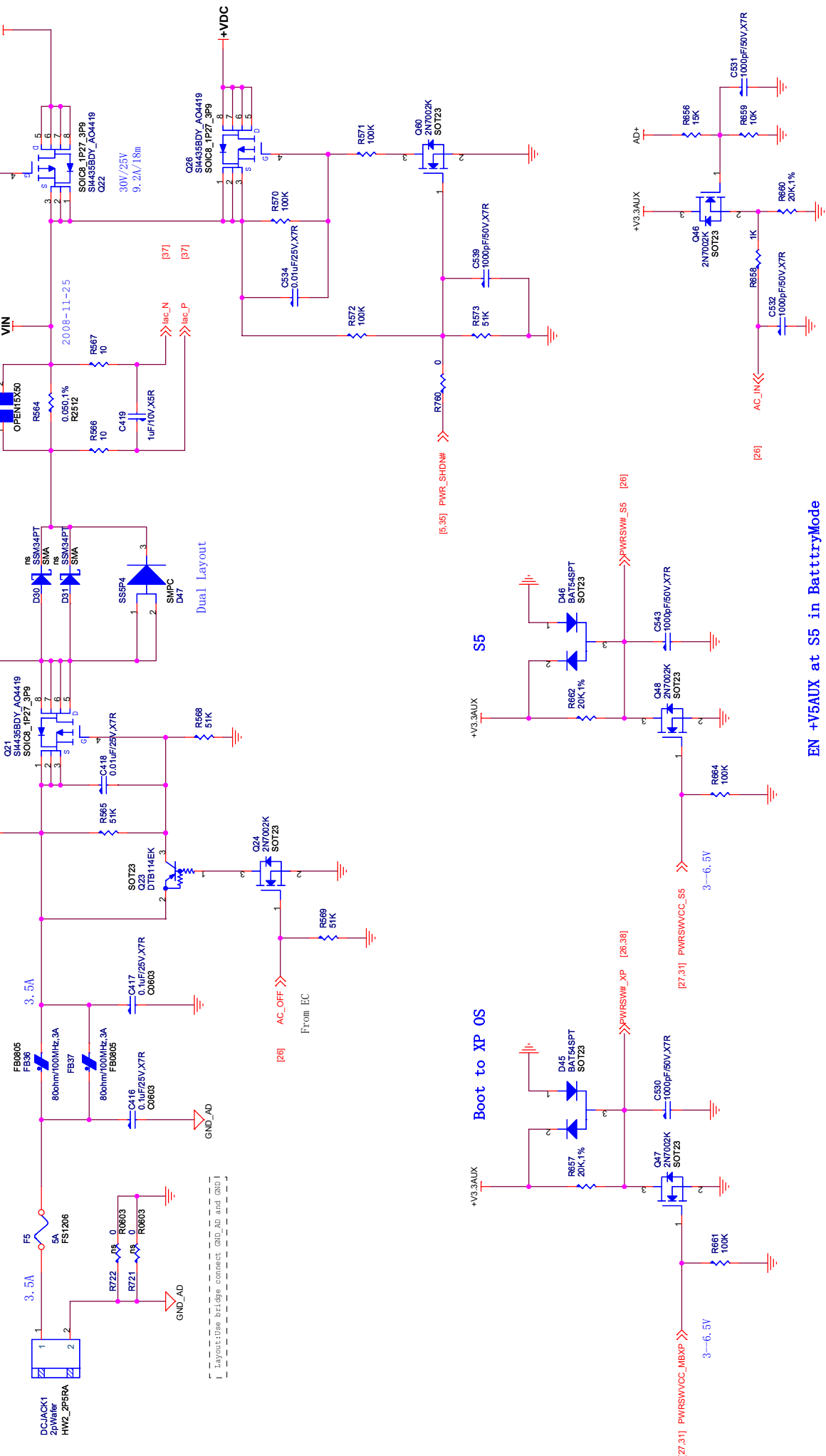


|         |      |      |
|---------|------|------|
| Fuction | P.M1 | P.M0 |
|---------|------|------|

\*

AD:65W 3.25A  
30mohm

与T10 VerB信号相同, 请特别注意



EN +V5AUX at S5 in BatteryMode

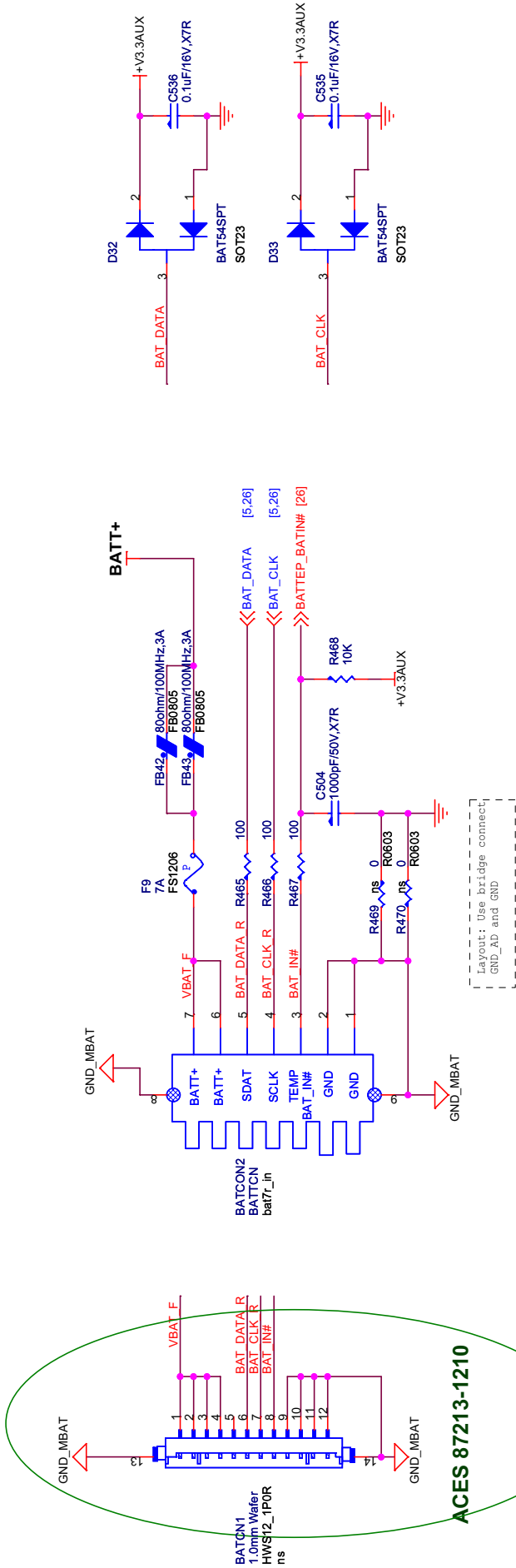
- +VDC [18,31,32,33,36,38]
- +V3.3AUX [5,12,13,14,15,18,20,25,26,27,30,31,34,38]
- VIN [27,37,38]
- BATT+ [30,37,38]
- lac\_P [37]
- lac\_N [37]



CZC Technology

|       |                             |                  |     |       |    |
|-------|-----------------------------|------------------|-----|-------|----|
| Title |                             | Power Adapter IN |     | Quarf |    |
| Size  | A3                          | Project Name     | Q10 | Rev   | B  |
| Date: | Thursday, February 26, 2009 | Sheet            | 29  | of    | 49 |

BATT+ [29,37,38]  
+V3.3AUX [5,12,13,14,15,18,20,25,26,27,29,31,34,38]



ACES 87213-1210

| BAT_TEM        |              |
|----------------|--------------|
| 0.5-3.0V       | Battery OK   |
| <0.5V or >3.0V | Battery Fail |

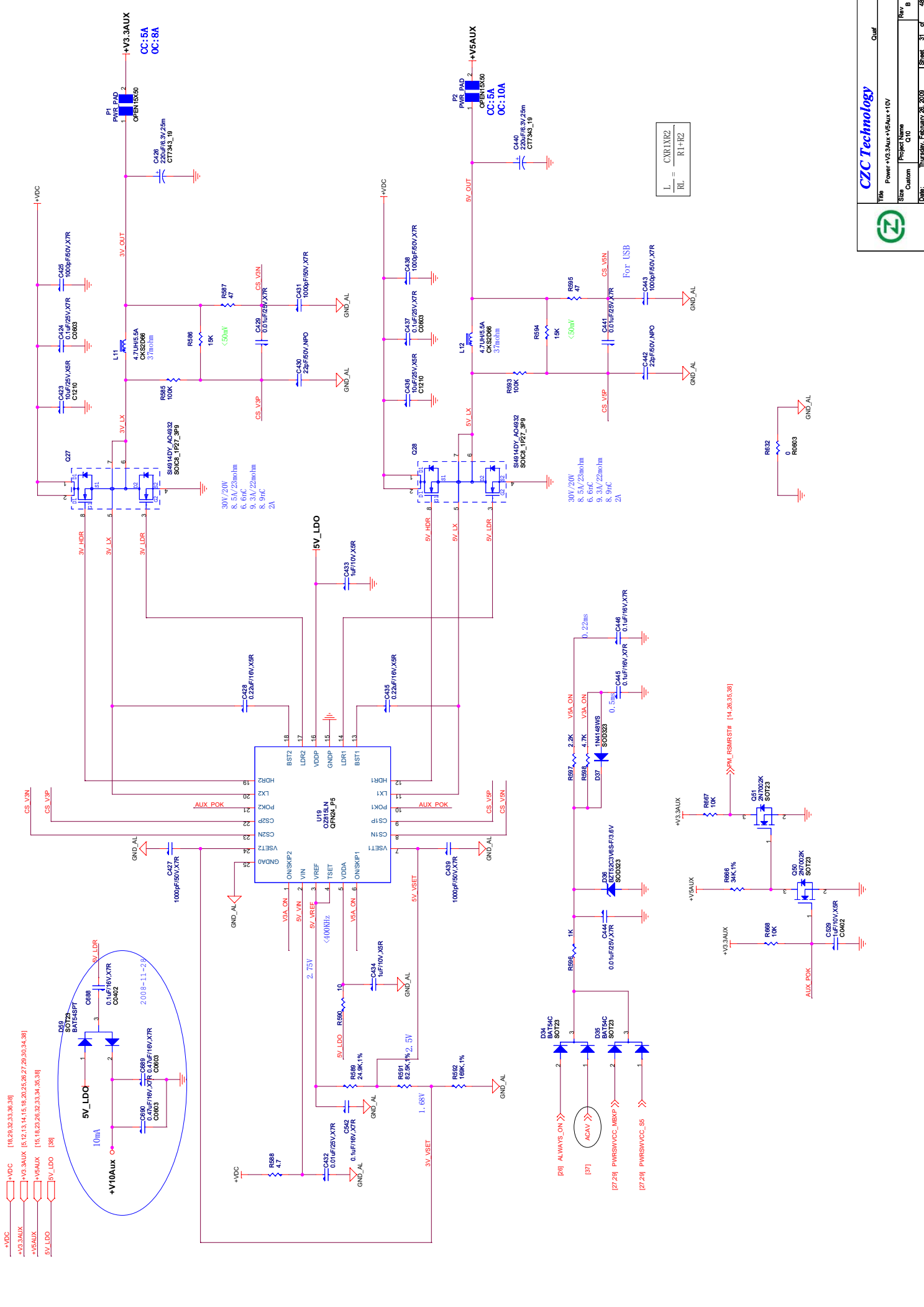
| BATT_IN# |            |
|----------|------------|
| 3.3V     | No Battery |
| 0V       | Battery In |

|                       |         |
|-----------------------|---------|
| RTC MF52 D 103 F 3950 |         |
| Temperature           | Voltage |
| -15                   | 3.0     |
| 70                    | 0.5     |
|                       | RT      |
|                       | 74.18K  |
|                       | 1.740K  |



CZC Technology

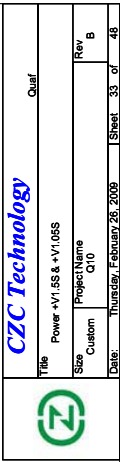
| Title            |                             | Quaf  |          |
|------------------|-----------------------------|-------|----------|
| Power Battery IN |                             |       |          |
| Size             | Project Name                | Rev   |          |
| Custom           | Q10                         | B     |          |
| Date:            | Thursday, February 26, 2009 | Sheet | 30 of 48 |

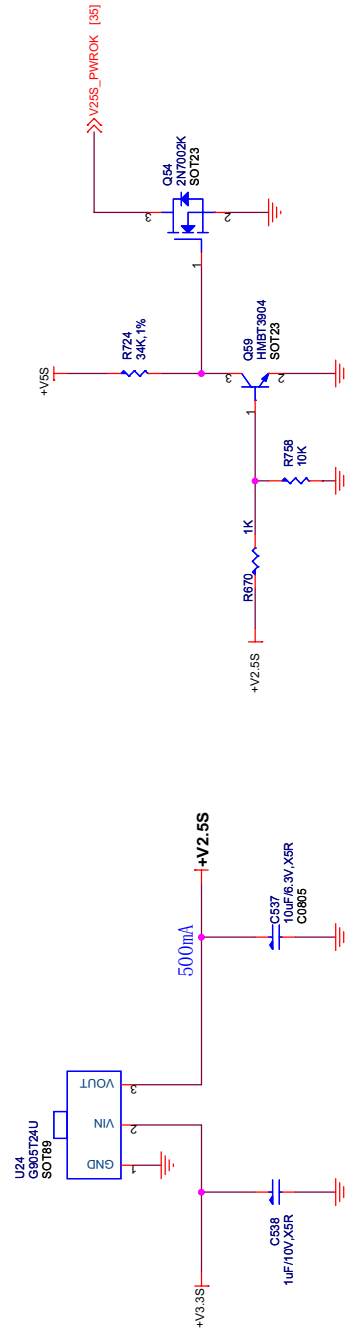
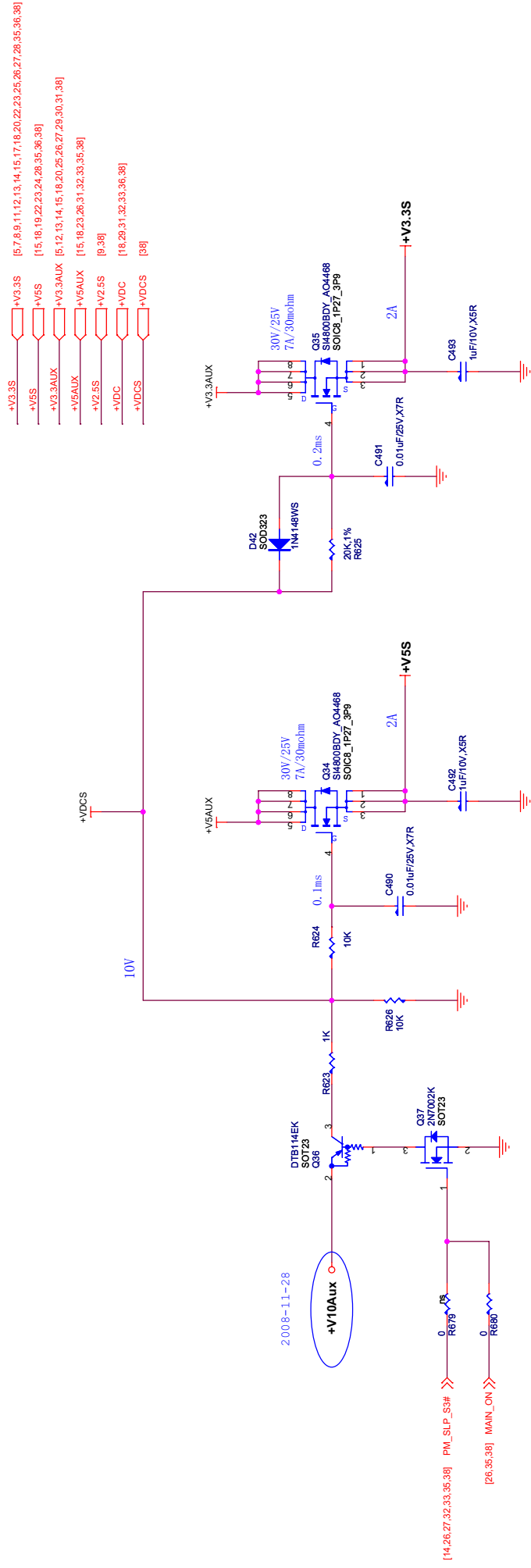


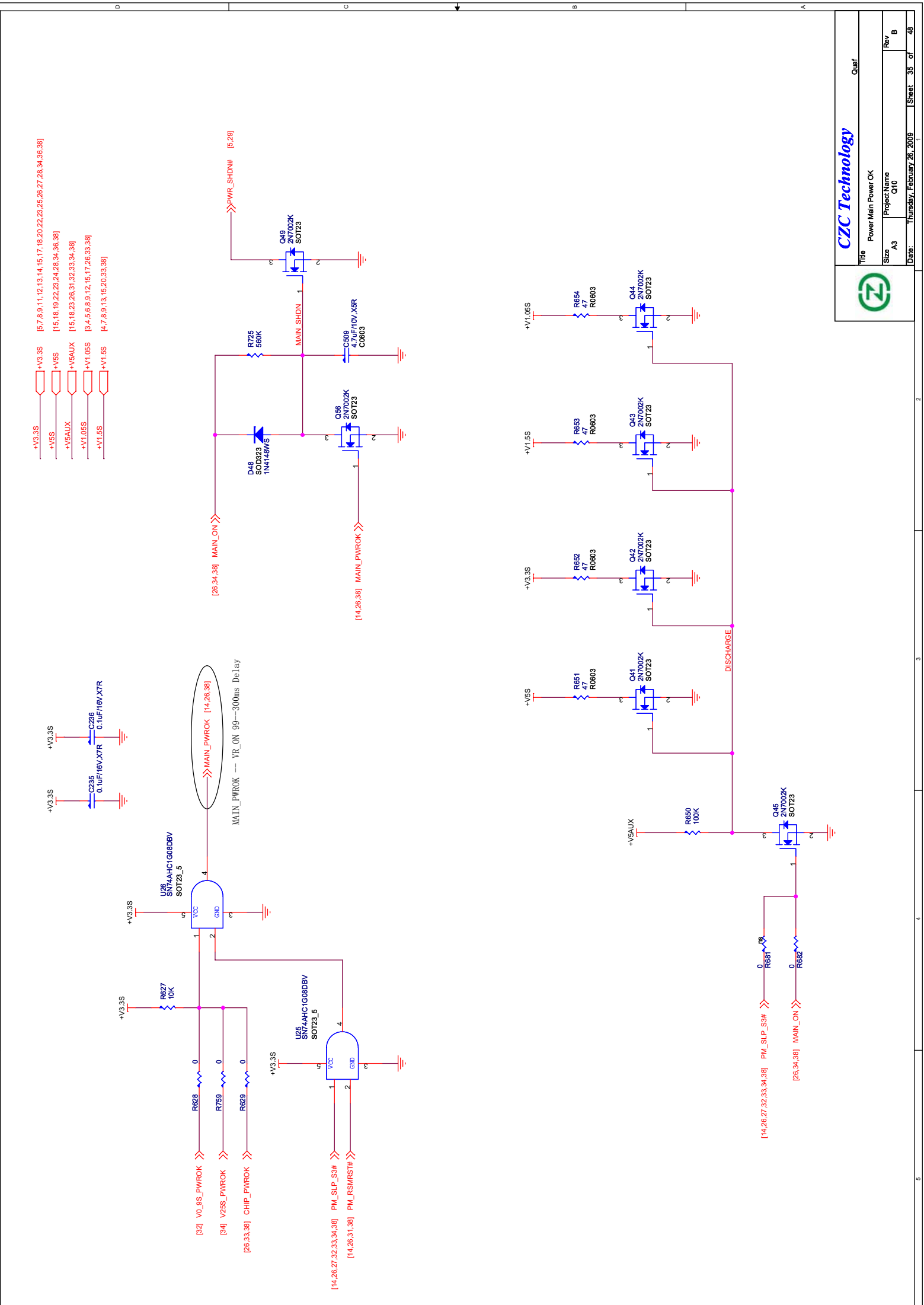
$$\frac{L}{RL} = \frac{CRLXR2}{R1R2}$$

|                |                             |       |          |
|----------------|-----------------------------|-------|----------|
| CZC Technology |                             | Qual  |          |
| Title          | Power+V3.3Aux+V5Aux+10V     | Rev   | B        |
| Size           | Project Name                | Rev   | B        |
| Client         | C10                         | Sheet | 31 of 48 |
| Date           | Thursday, February 28, 2008 | Sheet | 31 of 48 |

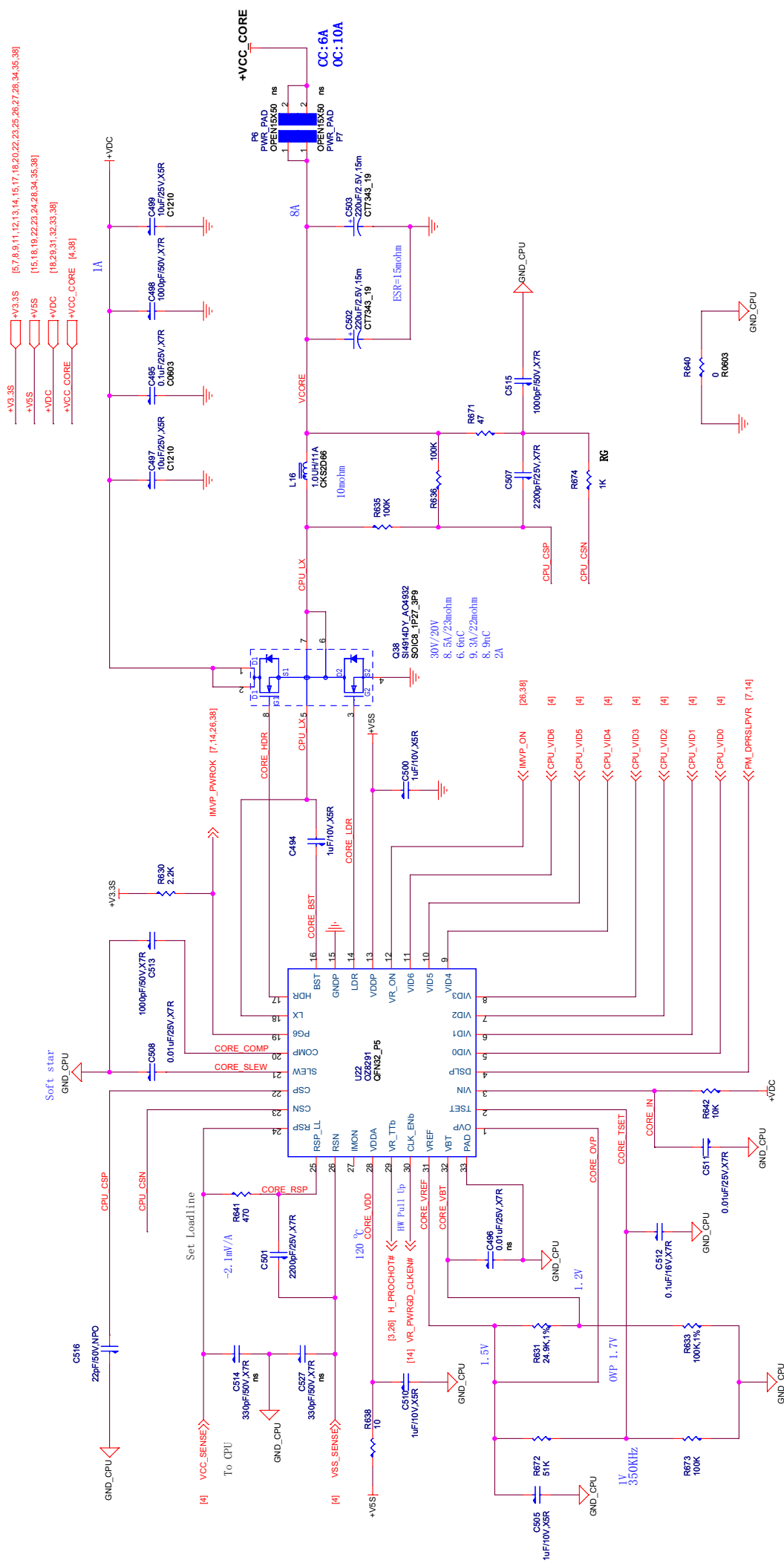








|       |                             |                     |     |       |    |
|-------|-----------------------------|---------------------|-----|-------|----|
| Title |                             | Power Main Power OK |     | Quarf |    |
| Size  | A3                          | Project Name        | Q10 | Rev   | B  |
| Date: | Thursday, February 26, 2009 | Sheet               | 35  | of    | 49 |

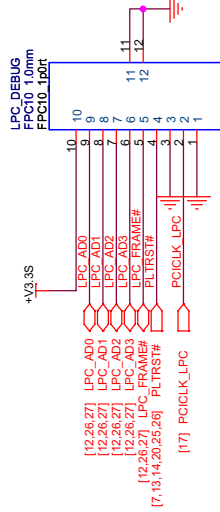
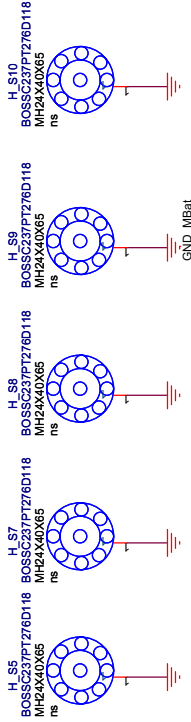
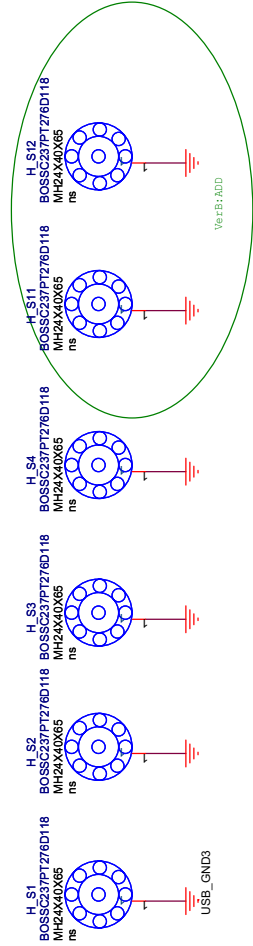


## CPU Core Debug

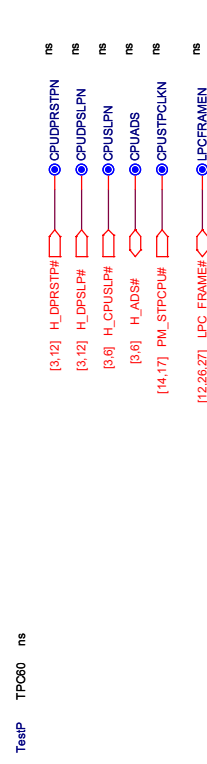
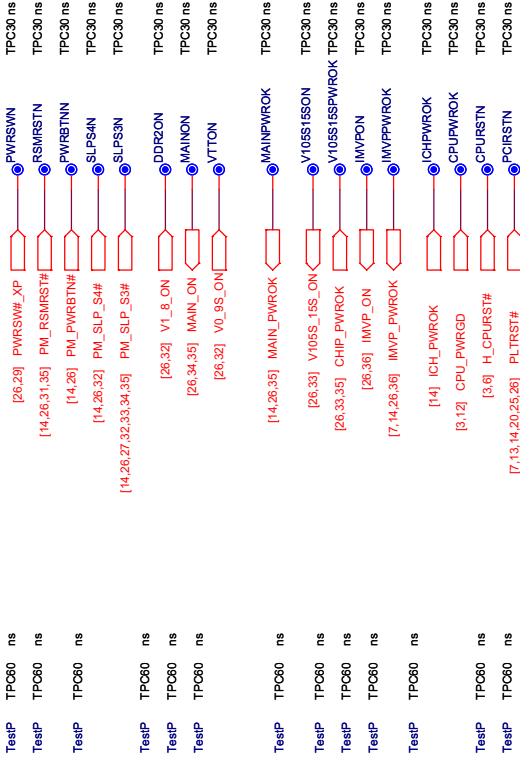
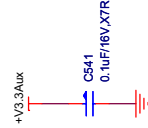
| VID6 | VID5 | VID4 | VID3 | VID2 | VID1 | VID0 | Vout    |
|------|------|------|------|------|------|------|---------|
| 0    | 0    | 1    | 1    | 0    | 0    | 0    | 1.2000V |
| 0    | 1    | 1    | 0    | 0    | 0    | 0    | 0.9000V |

|                                                                                   |                                                                     |                             |              |       |          |
|-----------------------------------------------------------------------------------|---------------------------------------------------------------------|-----------------------------|--------------|-------|----------|
|  | <h2 style="text-align: center; color: #000080;">CZC Technology</h2> |                             |              |       | Quarf    |
|                                                                                   | Title    Power CPU                                                  |                             |              |       |          |
|                                                                                   | Size                                                                | Custom                      | Project Name | Rev   |          |
|                                                                                   |                                                                     |                             | Q10          | B     |          |
| Date:                                                                             |                                                                     | Thursday, February 26, 2009 |              | Sheet | 36 of 48 |





MB pin10---DB pin1



Layout note:  
This Test point must be place same side and follow CPU side.



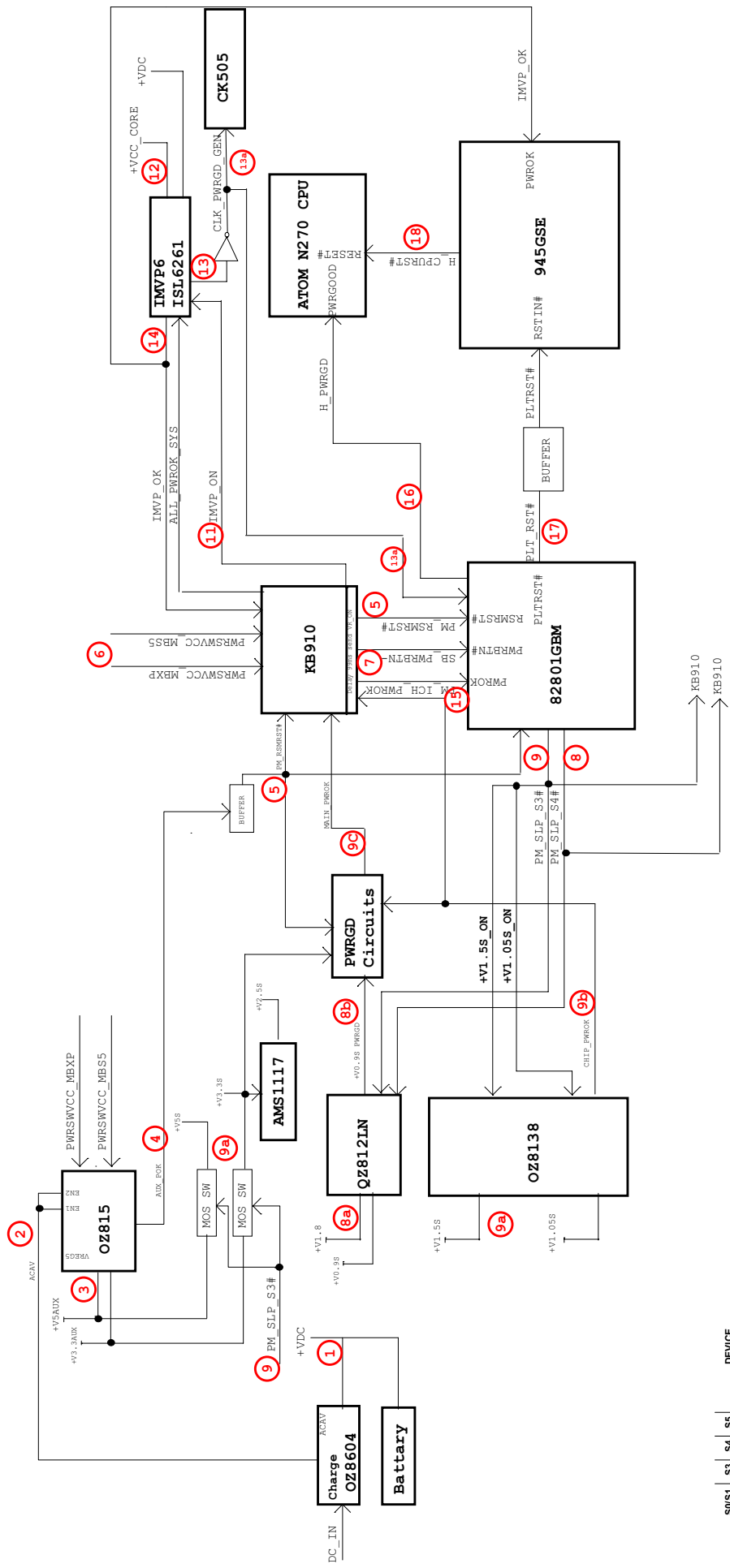
CZC Technology

Title SCREW HOLE

Size A3 Project Name Q10

Date: Thursday, February 26, 2009 Sheet 38 of 49

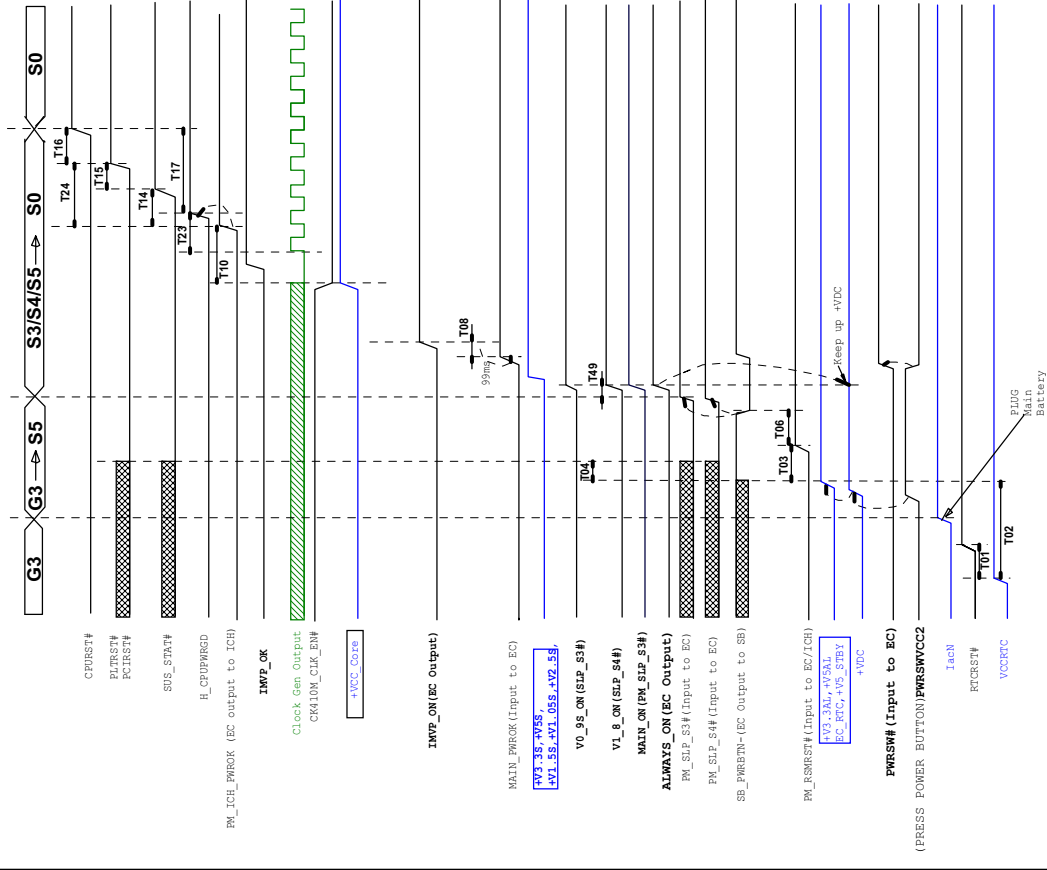




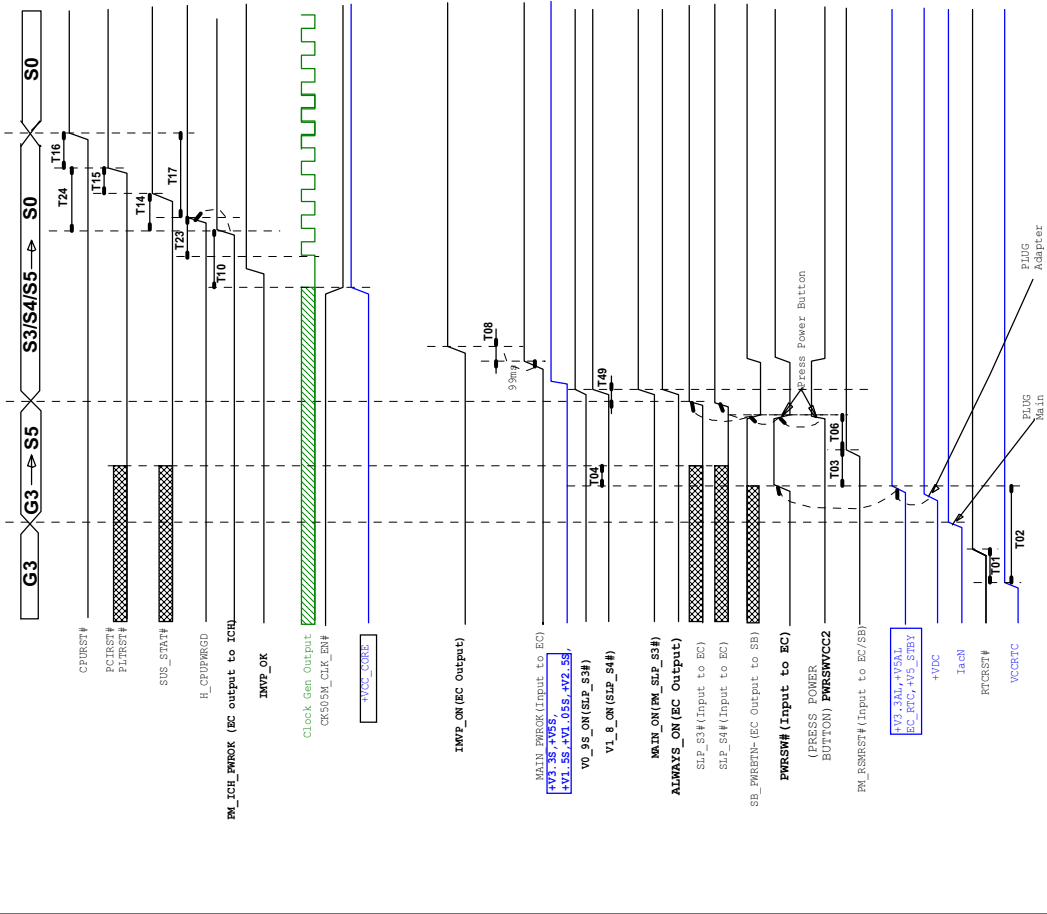
| S0/S1 | S3 | S4 | S5 | DEVICE         |
|-------|----|----|----|----------------|
| X     |    |    |    | V CORE         |
| X     |    |    |    | +V1.05S        |
| X     | X  | X  | X  | 5VAUX(AC)      |
| X     | X  | X  | X  | 5VAUX(BATT)    |
| X     | X  | X  | X  | 3.3VAUX(AC)    |
| X     | X  | X  | X  | 3.3VAUX(BATT)  |
| X     | X  | X  | X  | ECVCC(AC)      |
| X     | X  | X  | X  | ECVCC(BATT)    |
| X     |    |    |    | 5V             |
| X     |    |    |    | 3.3V           |
| X     |    |    |    | 1.5V           |
| X     | X  |    |    | 1.8VDIMM       |
| X     | X  |    |    | 0.9V           |
| X     | X  |    |    | VCCA           |
| X     | X  |    |    | 2.5V           |
| X     |    |    |    | LCDVDD, LEDVDD |

5. Power on Sequence:

Power On Sequence(Battery mode)



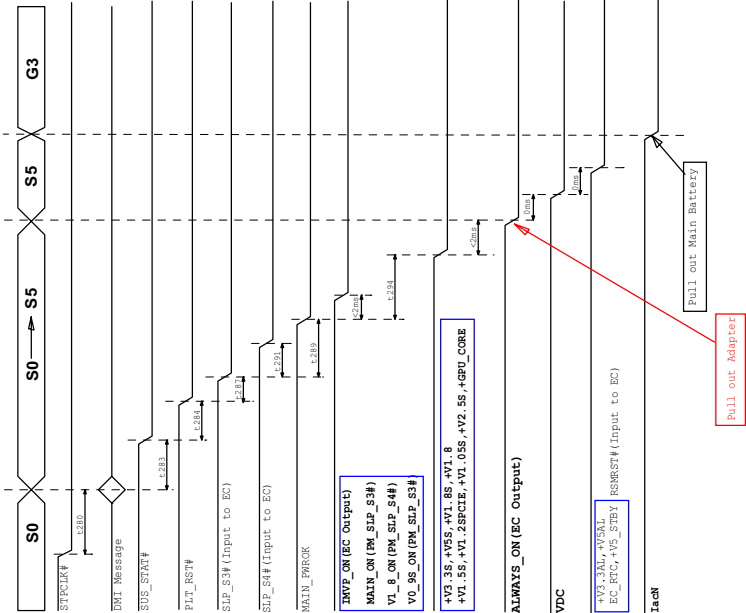
Power On Sequence(Adapter mode)



CZC Technology

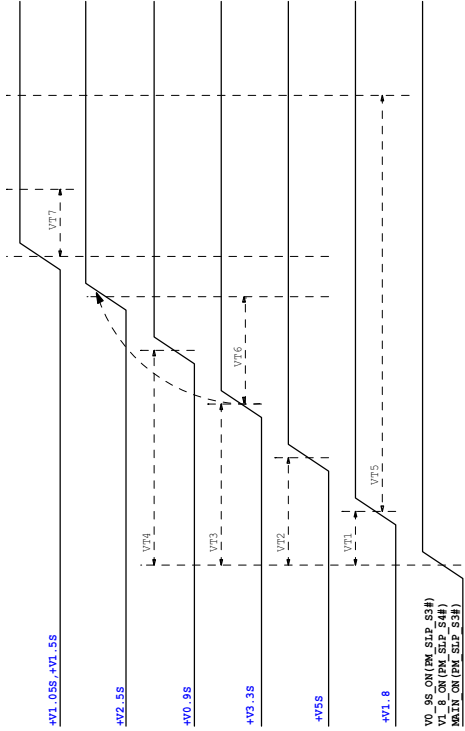
|       |                             |                   |     |         |    |
|-------|-----------------------------|-------------------|-----|---------|----|
| Title |                             | Power ON Sequence |     | LJ.wang |    |
| Size  | A3                          | Project Name      | Q10 | Rev     | B  |
| Date: | Thursday, February 26, 2009 | Sheet             | 41  | of      | 49 |

5. Power on & off Sequence;



Detail of VCC Power on

+V3\_3S,+V5S,+V1\_8S,+V1\_8  
+V1\_5S,+V1\_25PCIE,+V1\_05S,+V2\_5S,+GPU\_CORE



CZC Technology

Li.wang

Title Power OFF Sequence

Size A3

Project Name Q10

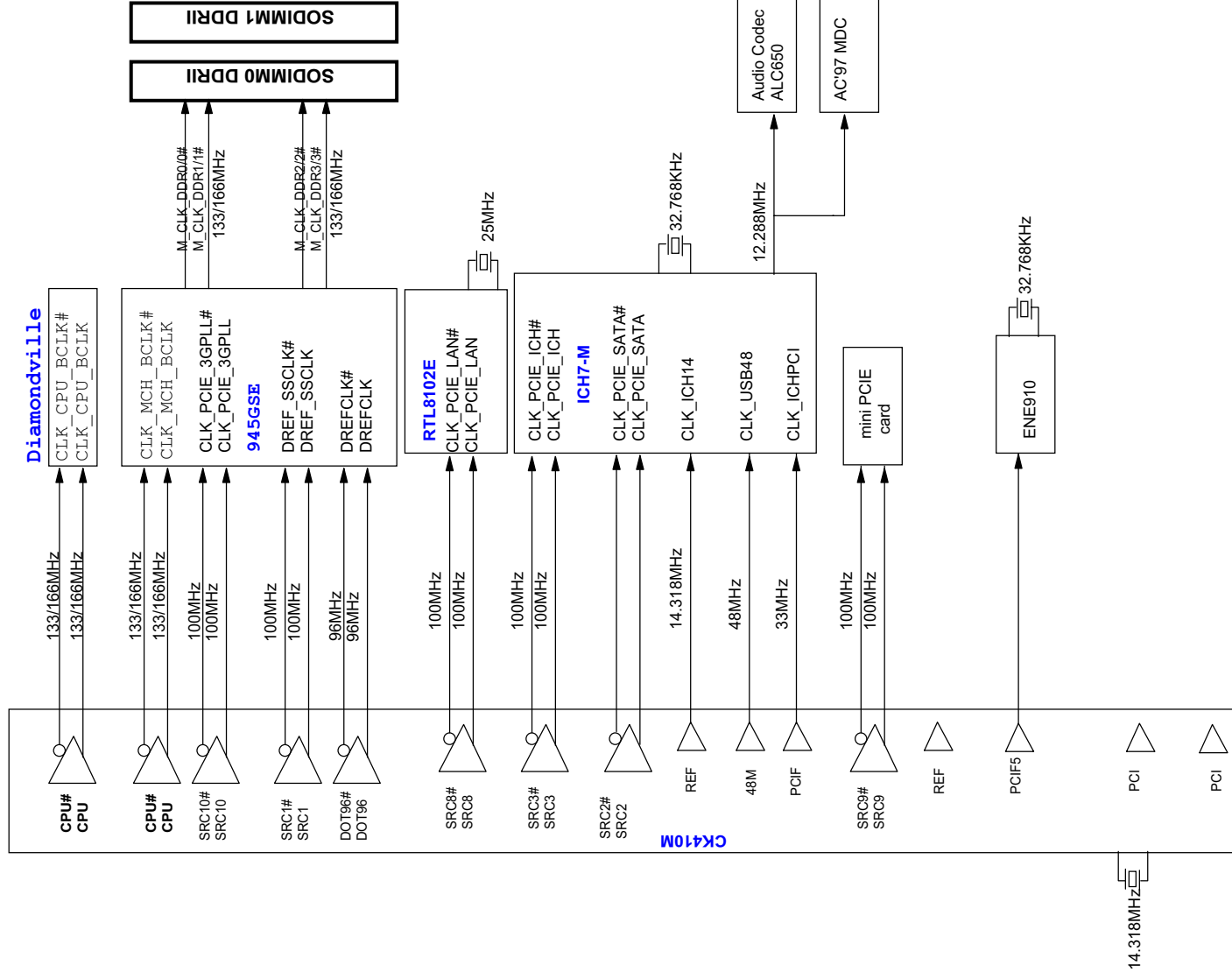
Date: Thursday, February 26, 2009

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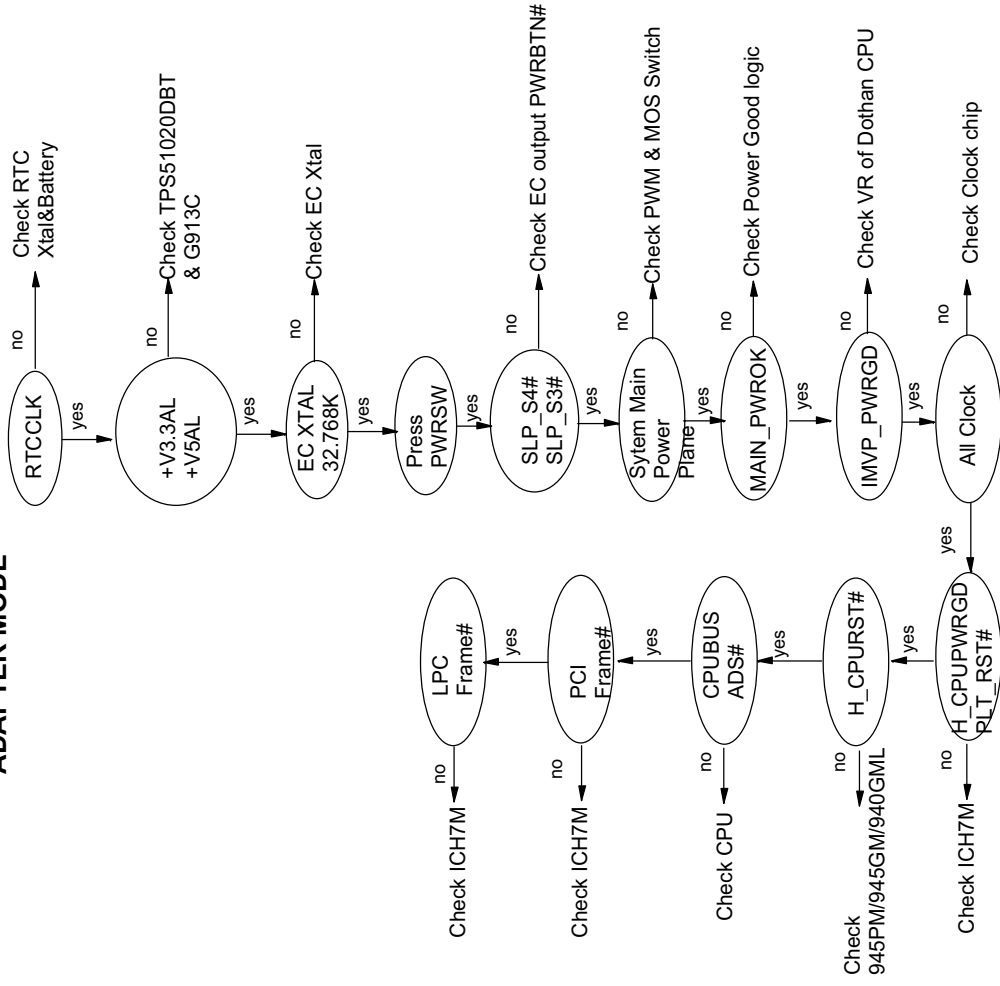
Rev B



CLOCK Distribution:



ADAPTER MODE



AD-0000

Project Name

Size

Page

Date

Thursday, February 28, 2009

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Sheet

of



Li wang

## CZC Technology

## Notes and Annotations

| Size | Project Name | Rev |
|------|--------------|-----|
| A4   | Q10          |     |

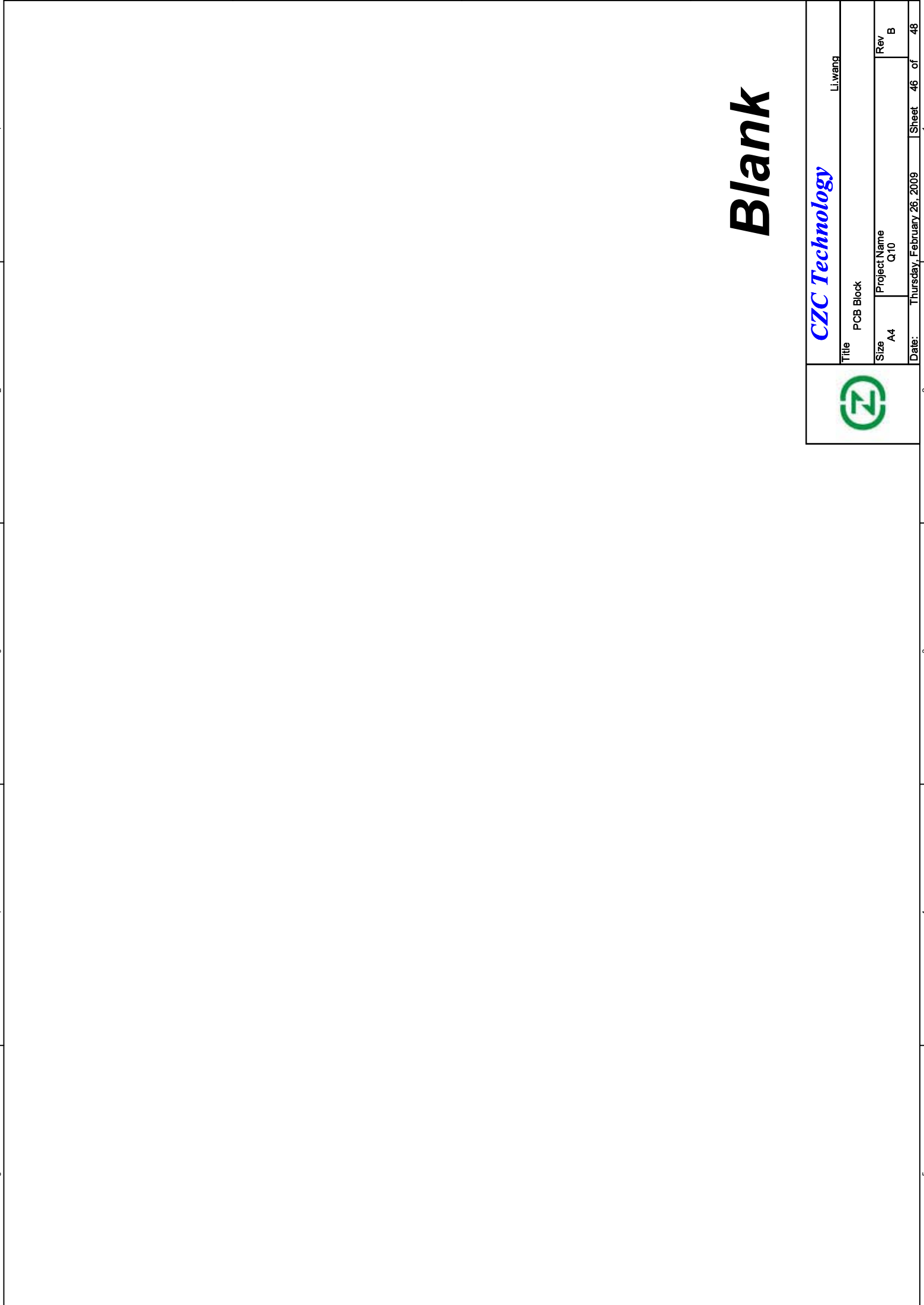
|    |                     |
|----|---------------------|
| A4 | Project Name<br>Q10 |
|----|---------------------|

|       |                           |                |
|-------|---------------------------|----------------|
| Date: | Thursday February 26 2009 | Sheet 45 of 48 |
|-------|---------------------------|----------------|

Thursday, February 26, 2009

Date:

# Blank



Blank



CZC Technology

|           |                             |         |          |
|-----------|-----------------------------|---------|----------|
| Title     |                             | Li.wang |          |
| PCB Block |                             |         |          |
| Size      | Project Name                | Rev     |          |
| A4        | Q10                         | B       |          |
| Date:     | Thursday, February 26, 2009 | Sheet   | 46 of 48 |

VerA To VerB List:

- 1. MiniPCIE 4.0mm高度的BOSS铜柱PCB板孔径要加大，不加大无法打上螺丝，参考T11
- 2. 需要兼容四种10.1的Panel 1024X576: HSD101PFW1,CLAA101NA0ACG亮面；CLAA101NA0ACN雾面； B101AW01 V3
- 3. CLK部分调FSB的需要增加上拉为+V1.05S
- 4. 增加EC I2Cbus输出;
- 5. Q10原理图和BOM中的VGA P/N改为DSUB03151005D: 620501510002
- 6. Match 4.0mm的MiniPCIE的Boss图纸要更新，孔径太大了。
- 7.I/O连接器上的30Pin应该用88742-3001；料号为620903023204
- 8. 3GPCIE1的连接器为5.6mm的MiniPCIE SLOT, Alltop C15702
- 9. LAN的93C46不上
- 10. EC要换成KB910Q C1版本的
- 11.KB要改成下接触的，同时PCB信号要同VERA垂直镜像。
- 12. PWRBTN1连接器考虑增加一个按钮信号。
- 13. 删去OSPWRBTN2相关电路
- 14. 删去PWROKLED相关电路
- 15. PCB版本改为B版: R228装上，R231不装；
- 16. 支持半卡的3G卡，参考MiniPCIE Ver1.2
- 17. 预留支持TCM功能
- 18. 将电源部分的电容默认上Sanyo POSCAP，MOSFET上AOS;
- 19. 将PANEL连接器更换成40Pin的，满足支持3种Panel的要求。

20. (2009.2.17) 除了SATAHDD板之外，将其它小板做成单独的拼板。



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