Operation description

When machine not coonect to power, machine is in G3 status,there is VCCRTC. When plug adapter, there is +VDC and V3.3AUX and V5AUX. Y1 produces 3.768KHz frequence. When push power button, U5 produces SLP_S4#,SLP_S3#, adapter starts to work and provides 1.8V,0.9V,1.5V,1.05V. When these power OK, Main_PWROK signal is sent to U17. U22 produces +VCC_CORE and send clock enable signal to U7. U7 generates clock signals. U22 send IMVP_OK signal to U17, U17 send power good signal to SB, SB send power good signal to CPU. SB send Reset signal to NB, NB send Reset signal to CPU. CPU starts to work.

CPU first send signal to NB through FSB533, NB send signal to SB through X2 DMI interface, SB send signal to U17 through LPC, U17 send signal to BIOS ROM through ISA. BIOS ROM send signals to CPU through U17, SB, NB.

WiFi module
Frequency range: 2.4GHz ISM Bands
Module : 802.11g: OFDM
802.11b: CCK(11,5.5Mbps), DQPSK(2Mbps), DBPSK(1Mbps)

Battery:
3 cell 11.1V 2200mAh

Power supply
Push power botton, Adapter starts to provide 19V, 19V convert to +VDC through power circuit.
+VDC convert to +3.3V_AUX and +5V_AUX through U19, +VDC convert to +V1.8 and +V0.9S through U20, +VDC convert to +V1.5S and +V1.05S through U21, +VDC convert to +VCC_core through U22.

Clock distribution
CPU: 133/166MHz
945GSE: 133/166MHz, 100MHz, 96MHz
DDRII: 133/166MHz
LAN: 100MHz,25MHz
ICH7-M: 100MHz,14.318MHz,48MHz,33MHz,32.768KHz,
miniPCIE: 100MHz
Audio codec: 12.288MHz
ENE910: 32.768KHz