

The diagram illustrates the internal architecture of the 8910 BASE radio receiver. It features a central CPU (IC4, TCC1508C11-C0C8) that manages the system. Key components include:

- RF Section:** An AM ANT and FM ANT feed into input loops. The AM path includes an RF Amplifier (Q1), a 2475MHz filter, and an RF VCO (IC2, KA8825B). The FM path includes an RF Amplifier, a 2403MHz filter, and an FM VCO (IC2, KA8825B).
- Mixing and Demodulation:** Both paths use mixers (Q2, Q3) and demodulators (U2, U3) to extract audio signals. The FM path also includes an FM demodulator (U4).
- Control and Power:** The CPU (IC4) is connected to a PLL VCO CONTROL (IC2, KA8825B) and a TX VCO. It also manages the power amplifier (Q5, Q7) and the speaker. The system is powered by a 3.579545MHz crystal (X2) and a 11.50MHz crystal (X1).
- Interface:** The radio includes a LINE JACK, a speaker, and a display (LCD).

The diagram is a detailed block diagram showing the interconnections between various integrated circuits (ICs), transistors (Q1-Q9), capacitors (C1-C10), and other components. It also includes a revision record table at the bottom left and a title block at the bottom right.

