

USB DONGLE Block Diagram Description

The USB DONGLE use CSR BC03-ROM as its core chip .The chip is mostly include DSP、 MOD、 DEMOD、 LNA、 PA、 Base band and logic、 Micro controller and RF synthesiser. The external crystal that provide clock for the core chip is 16MHz .The dongle is USB interface that connected to PC. The PC's USB port voltage is processed by LDO(AME8801AME), that provided to BC03-ROM chip operating voltage. The RF signal is transmitted and received from the external Balun 、 filter and ANT

TX block diagram description: The control signals and data in the BC03-ROM is modulated and processed and pass the PA in the chip. They output from the chip and through the Balun and Filter. Last they will be transmitted from ANT to another Bluetooth device.

RX block diagram description: The RF signal from other Bluetooth device is received via ANT. And they through filter and balun into the BC03-ROM chip. They are magnified by internal LNA. the signal is transmitted to PC via USB port after demodulated.