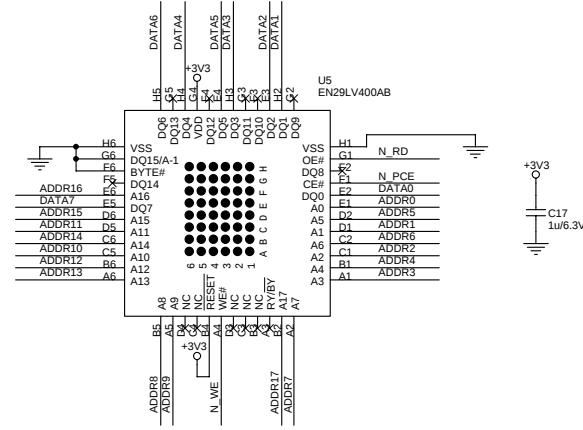
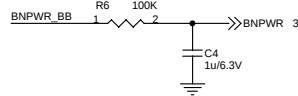


Working current: 1mA
Standby current:
0.5uA

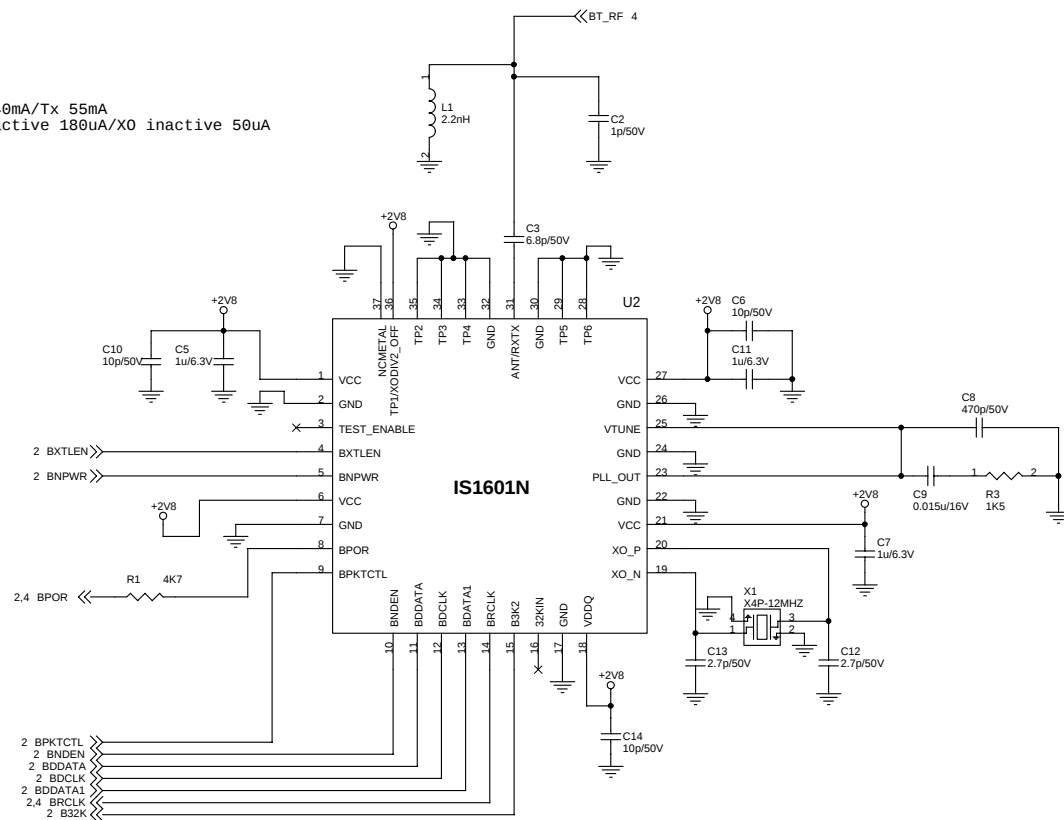


4 P0[0..7] <<<
4 P2[0..7] <<<

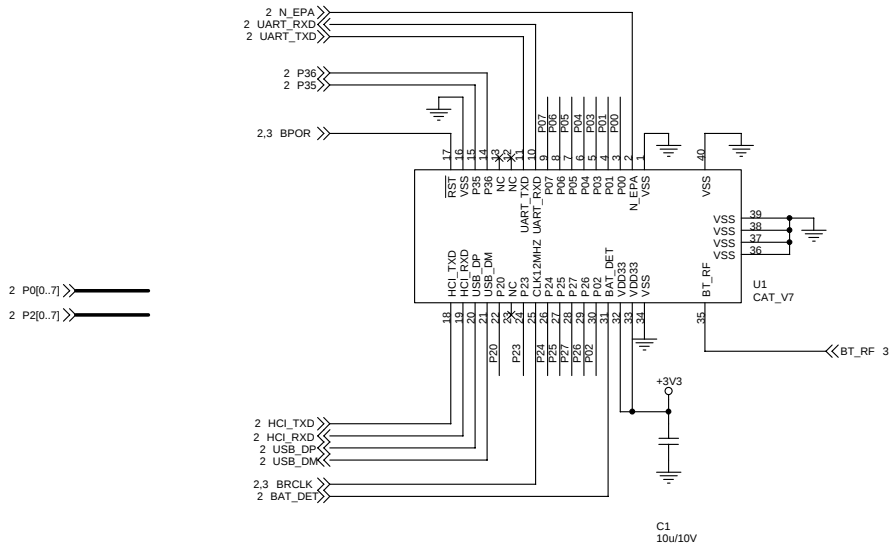


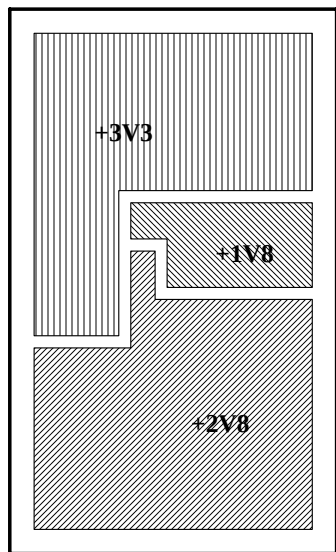
 ISSC 新竹科學園區工業東一路2-1號3樓 Tel: 03 - 5778385 Designer: Ben Shen		
Board Name		P/N
Bluetooth GPS Receiver / BT-R900		9315
Size	Title	Rev
B	IS1005-BGA81	7.0
Date:	Thursday, January 05, 2006	Sheet 2 of 7

Working current: Rx 40mA/Tx 55mA
Standby current: X0 active 180uA/X0 inactive 50uA

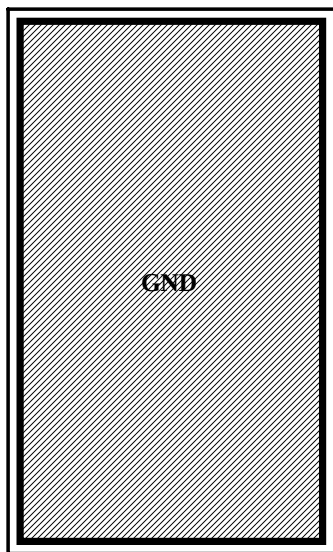


		ISSC	
新竹科學園區工業東一路2-1號3樓		Tel: 03 - 5778385	
Designer: Ben Shen			
Board Name		Bluetooth GPS Receiver / BT-R900	P/N 9315
Size B	Title	RF IS1601N	Rev 7.
Date: Thursday, January 05, 2006		Sheet 3 of 7	1





POWER PLAN



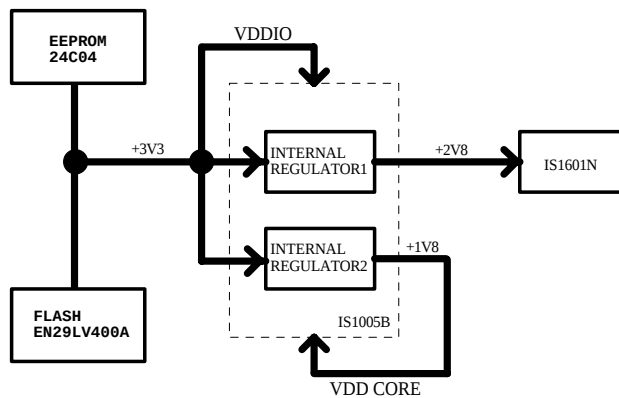
GROUND PLAN

Power estimation		
	WORKING MODE	STANDBY MODE
ST M24C02W	1mA	0.5uA
SST 39VF020	read 20mA program, erase 30mA	15uA
ISSC IS1601N	Rx 40mA Tx 55mA	X0 active 180uA X0 inactive 50uA

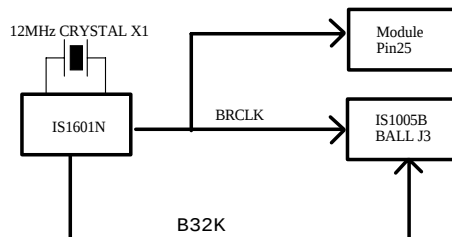


ISSC
新竹科學園區工業東一路2-1號3樓
Tel: 03 - 5778385
Designer: Ben Shen

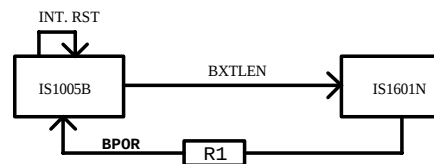
Board Name		P/N
Bluetooth GPS Receiver / BT-R90C		9315
Size	Title	Rev
B	POWER PLAN	7.0
Date:	Thursday, January 05, 2006	Sheet 5 of 7



POWER TREE



CLOCK TREE



RESET TREE

 ISSC 新竹科學園區工業東一路2-1號3樓 Tel: 03 - 5778385 Designer: Ben Shen		
Board Name Bluetooth GPS Receiver / BT-R900		P/N 9315
Size B	Title Power/Reset Tree	Rev 7.0
Date: Thursday, January 05, 2006	Sheet 1	6 of 7

CAT v5 is modify from CAT v4, the difference is as the listed below:

1. The flash CE# is controlled by IS1005B N_PCE, not N_RD, and R1 is deleted.
2. The module outline pin23 is changed to be NC pin, no signal is connected to this pin
3. V5 logo on top side next to the flash as the indication

CAT v6 is modify from CAT v5, the difference is as the listed below:

1. Change the flash to 400 series, not pin compatible with 040 series.
2. Add the 32KHz clock from RF IS1601N to CAT outline pin 23th.
3. Add a resistor R1 (4.7K ohm) at the BPOR path
4. Add R6 (100K) and C4 (1uF) to solve the cold start fail issue.

CAT v7 is modify from CAT v6, the difference is as the listed below:

- 1.Modify RF PLL layout
- 2.Connect B32K to baseband 32KHz_I pin directly



ISSC
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Tel: 03 - 5778385
Designer: Ben Shen

Board Name		P/N
BT-R900 / BT-R900		9315
Size	Title	Rev
B	History	6.0
Date:	Thursday, January 05, 2006	Sheet 7 of 7