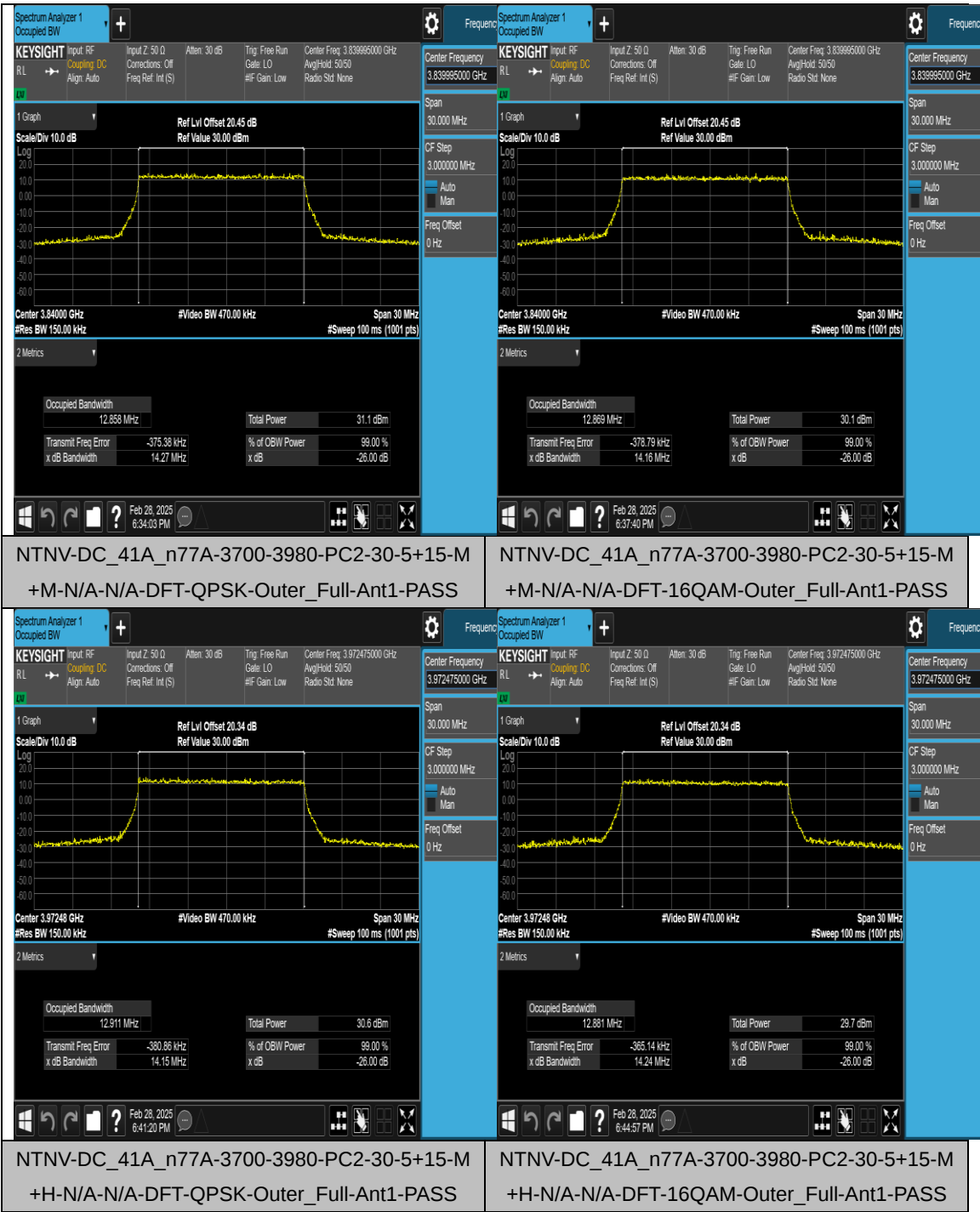
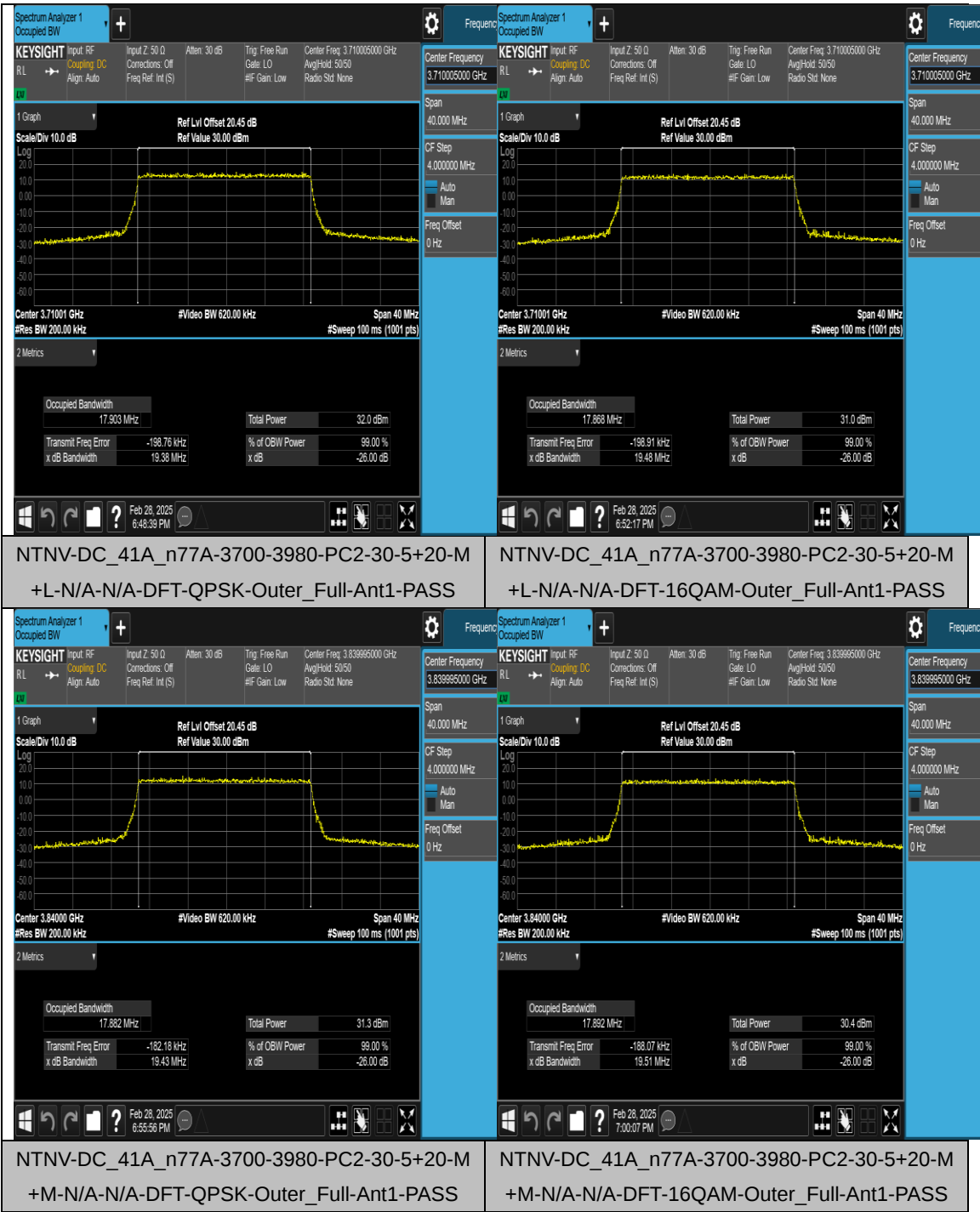
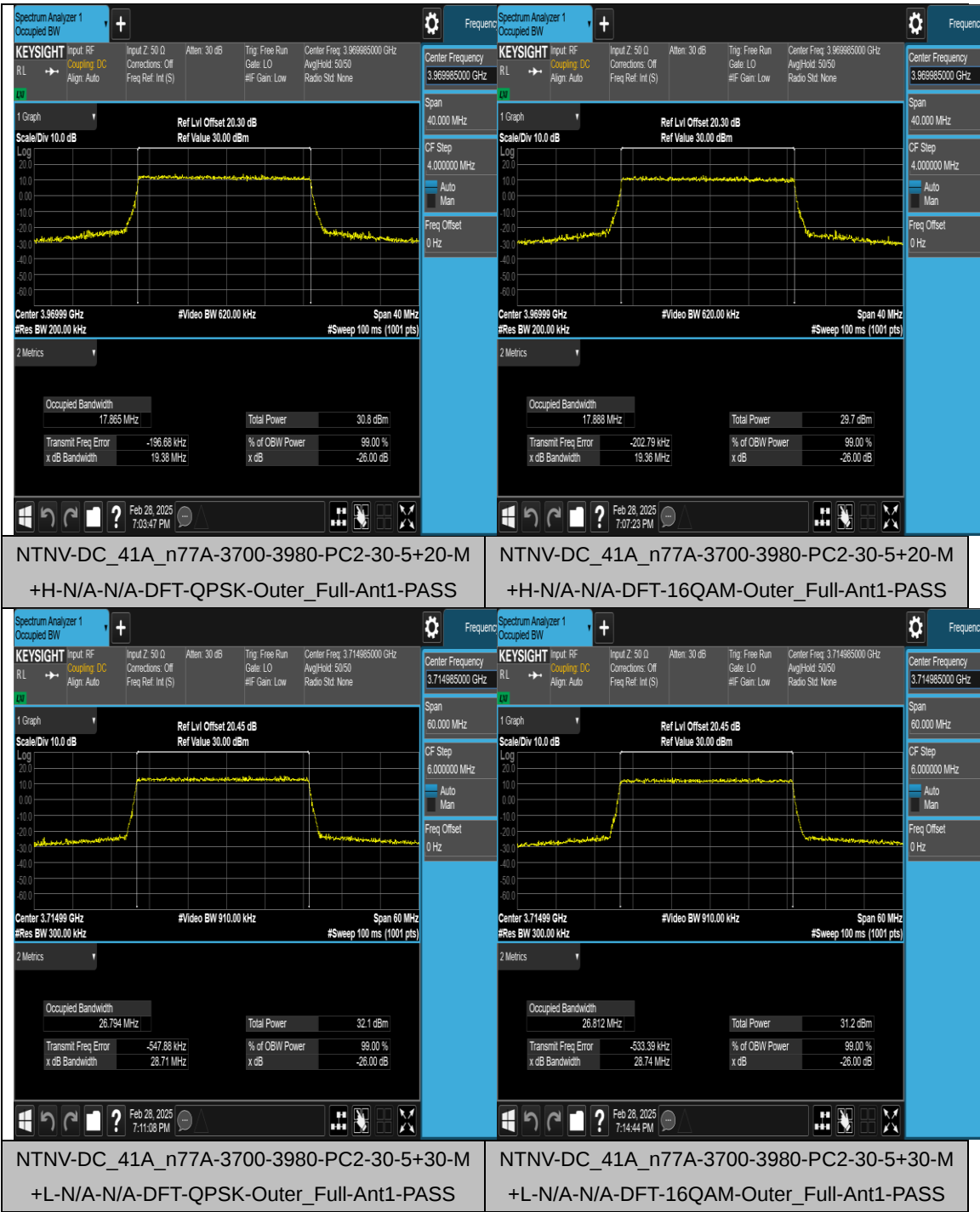
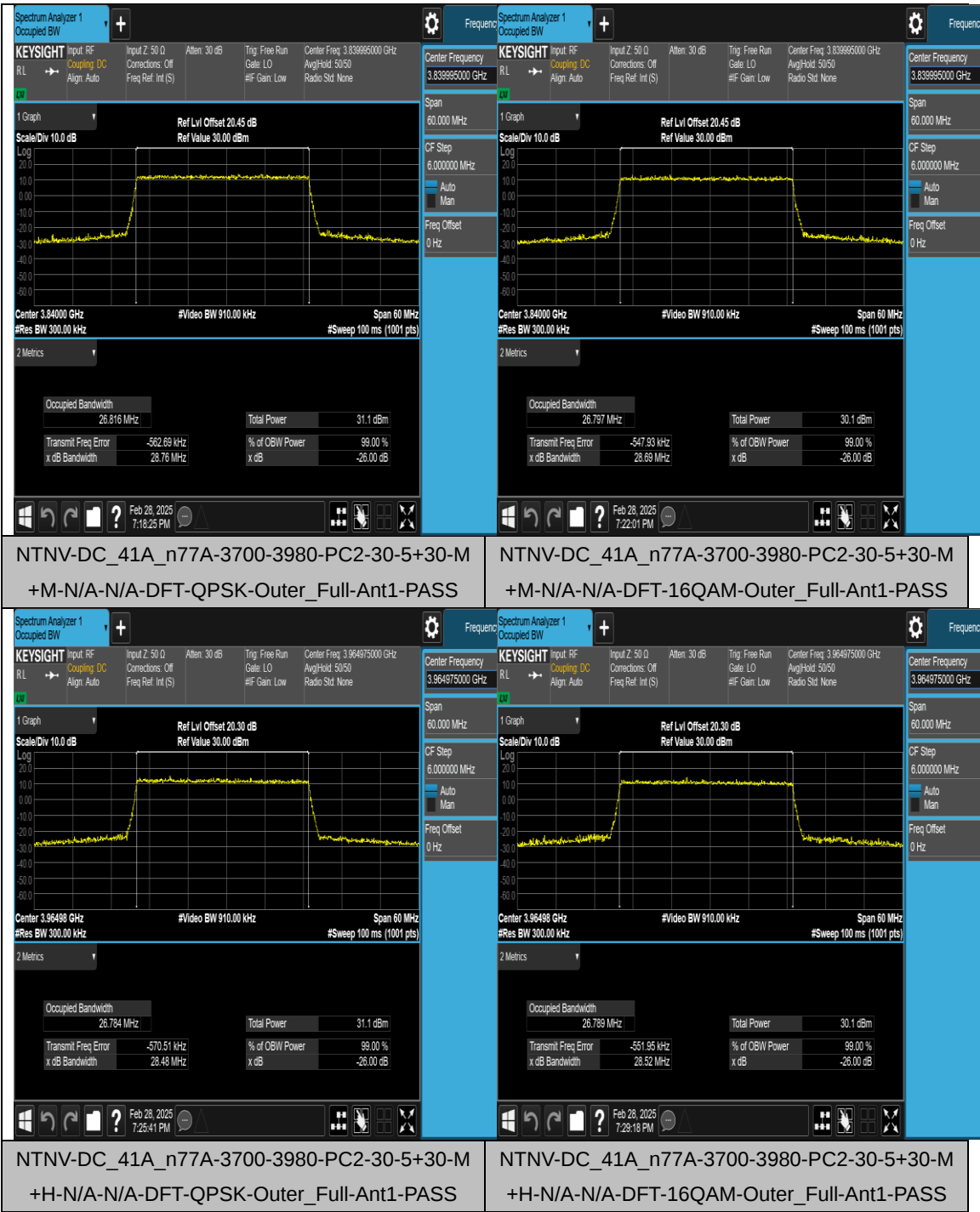
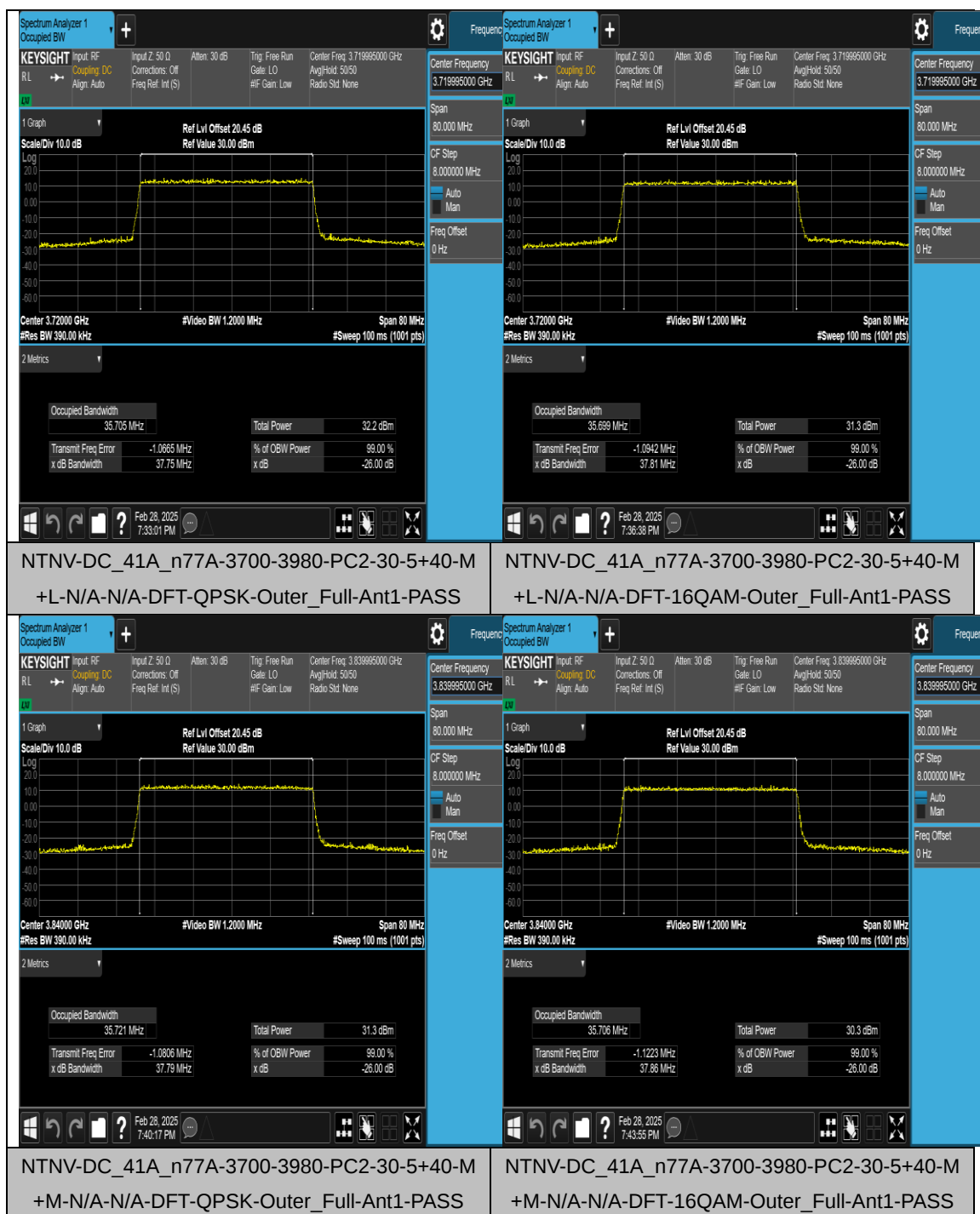


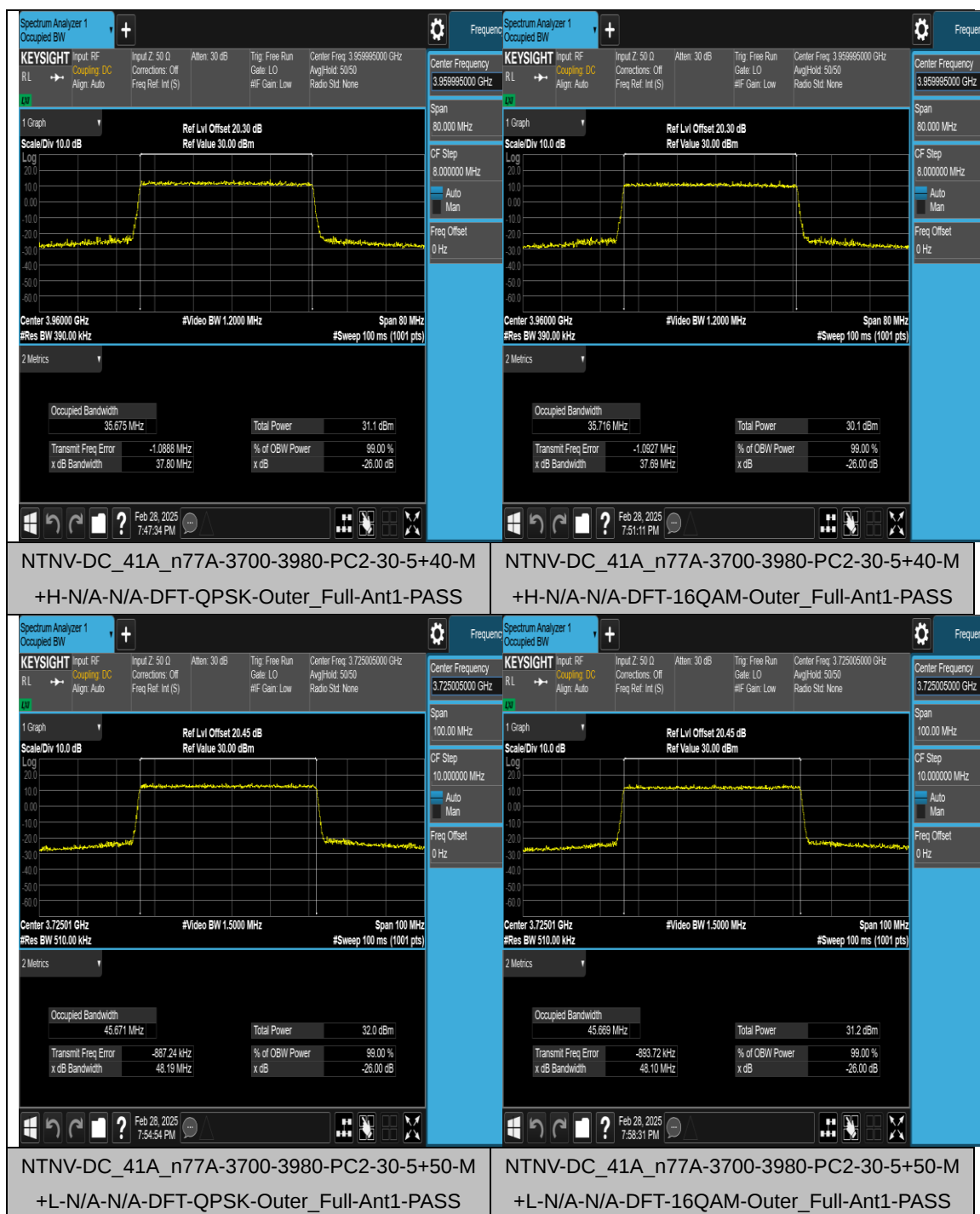


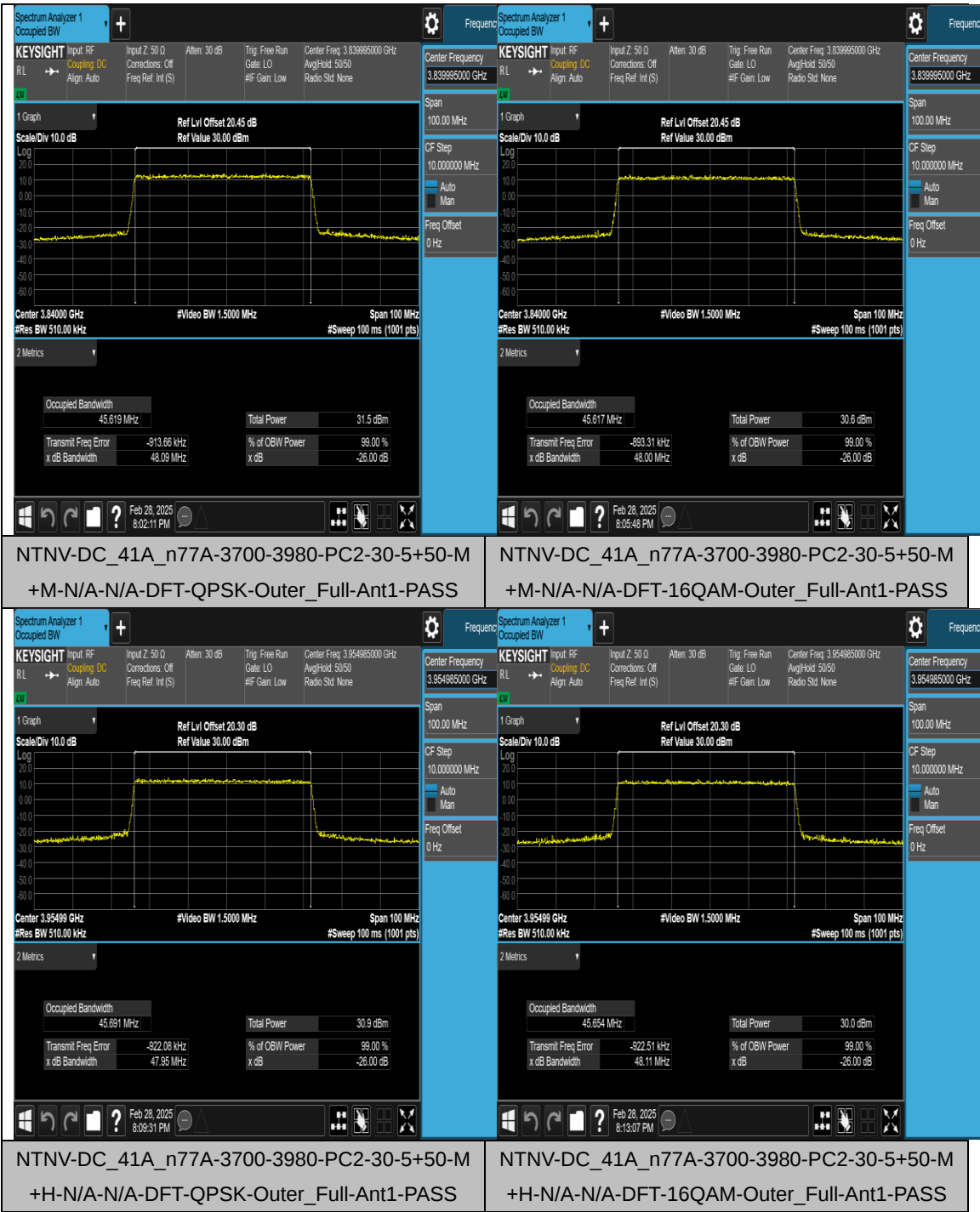


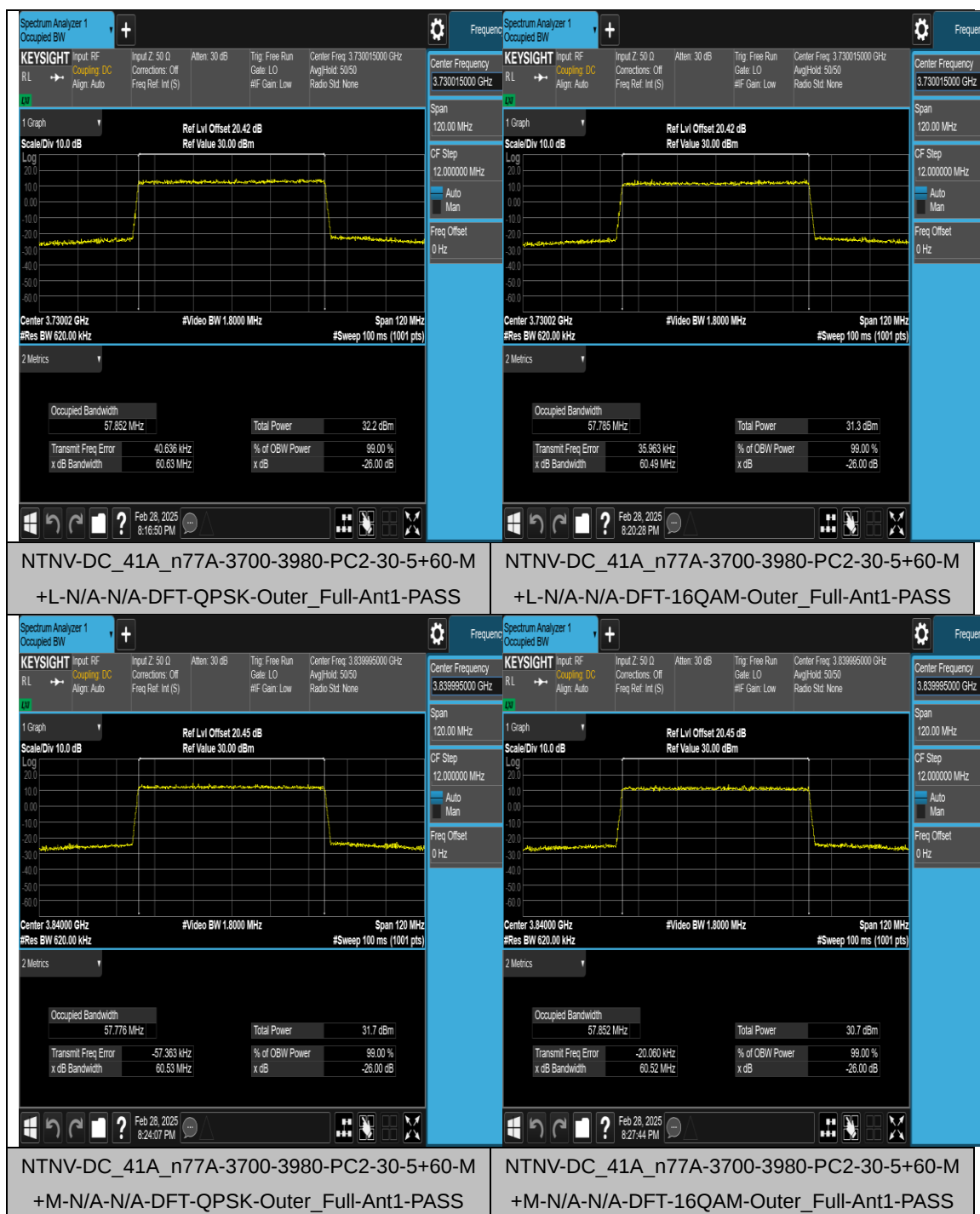


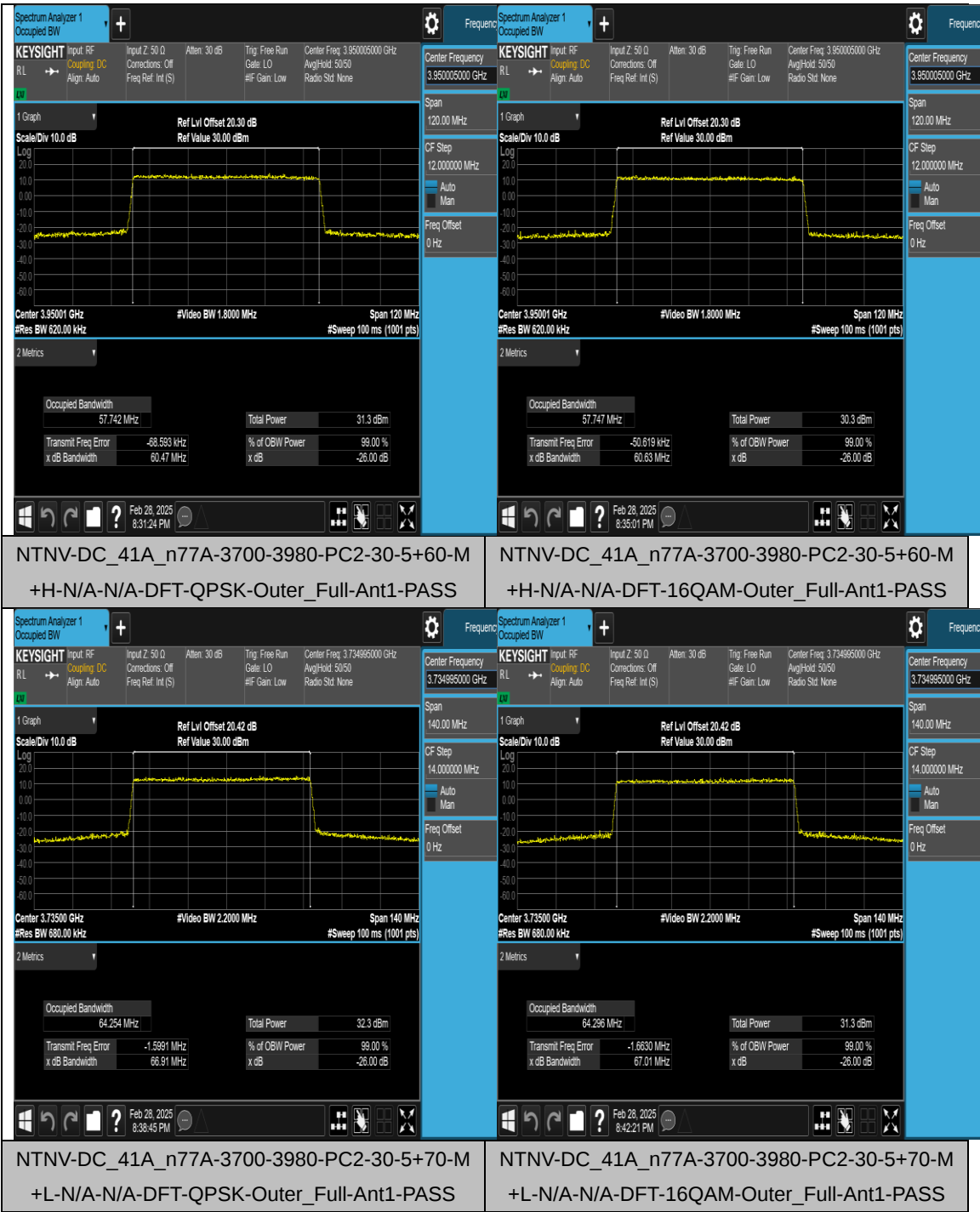


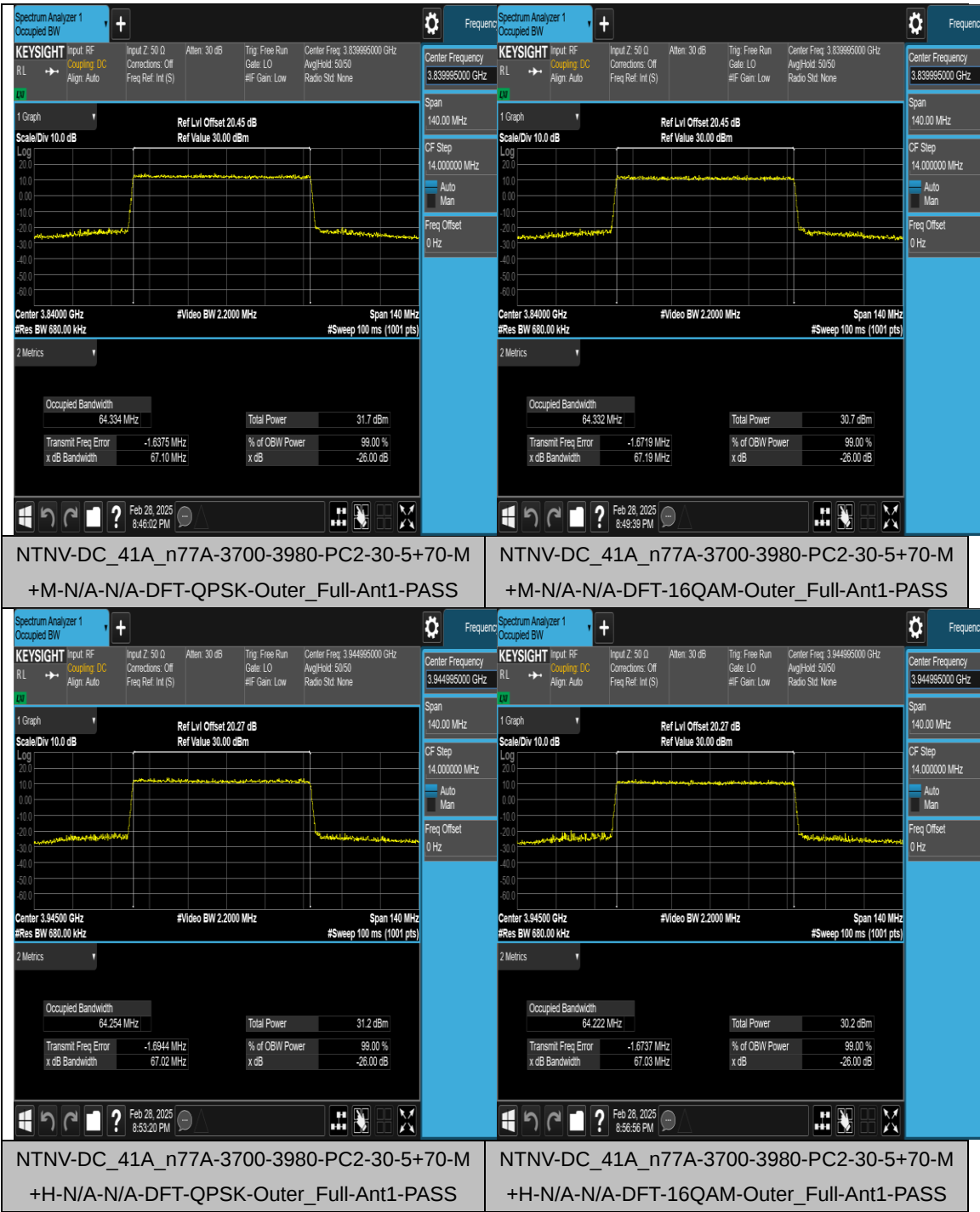


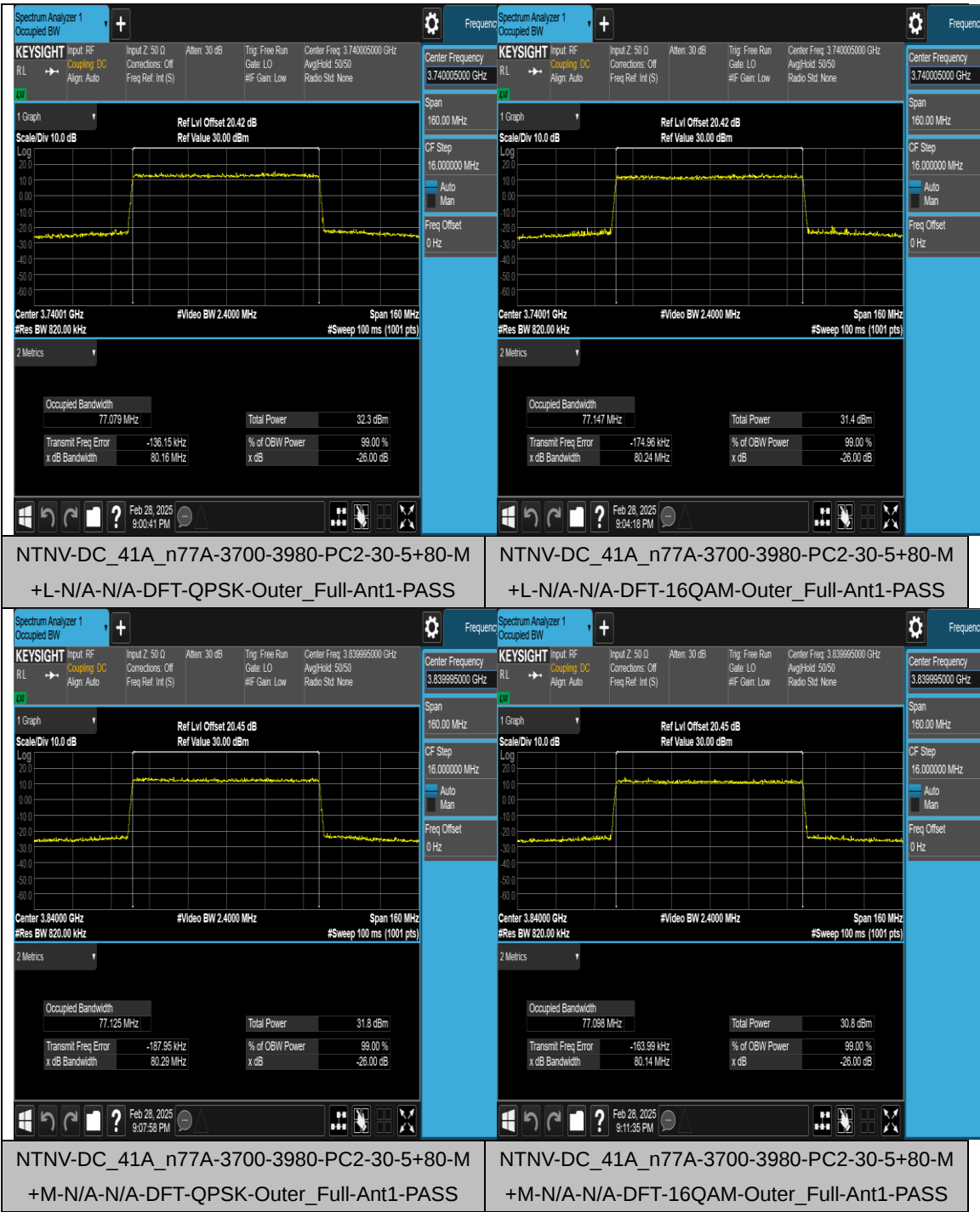


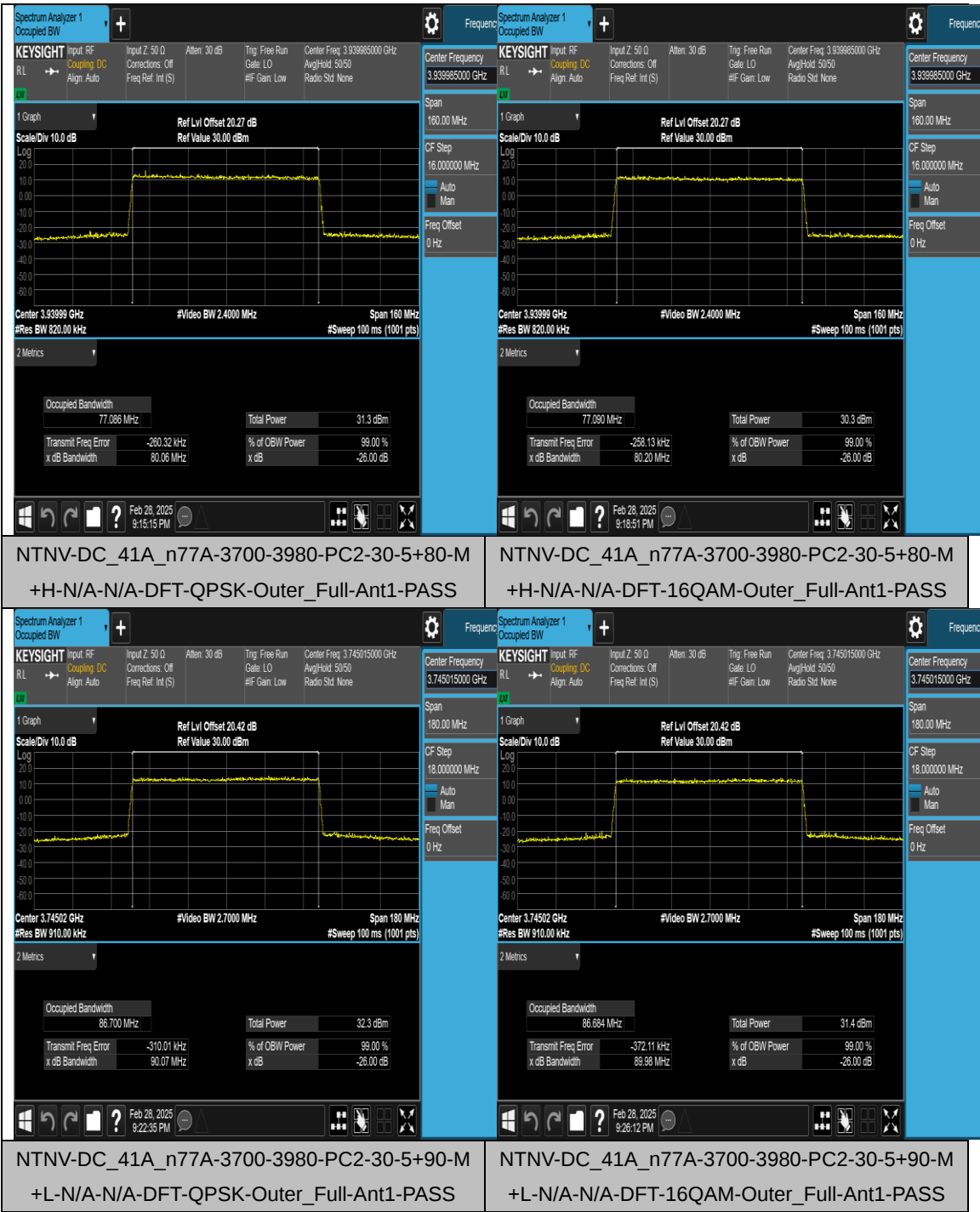


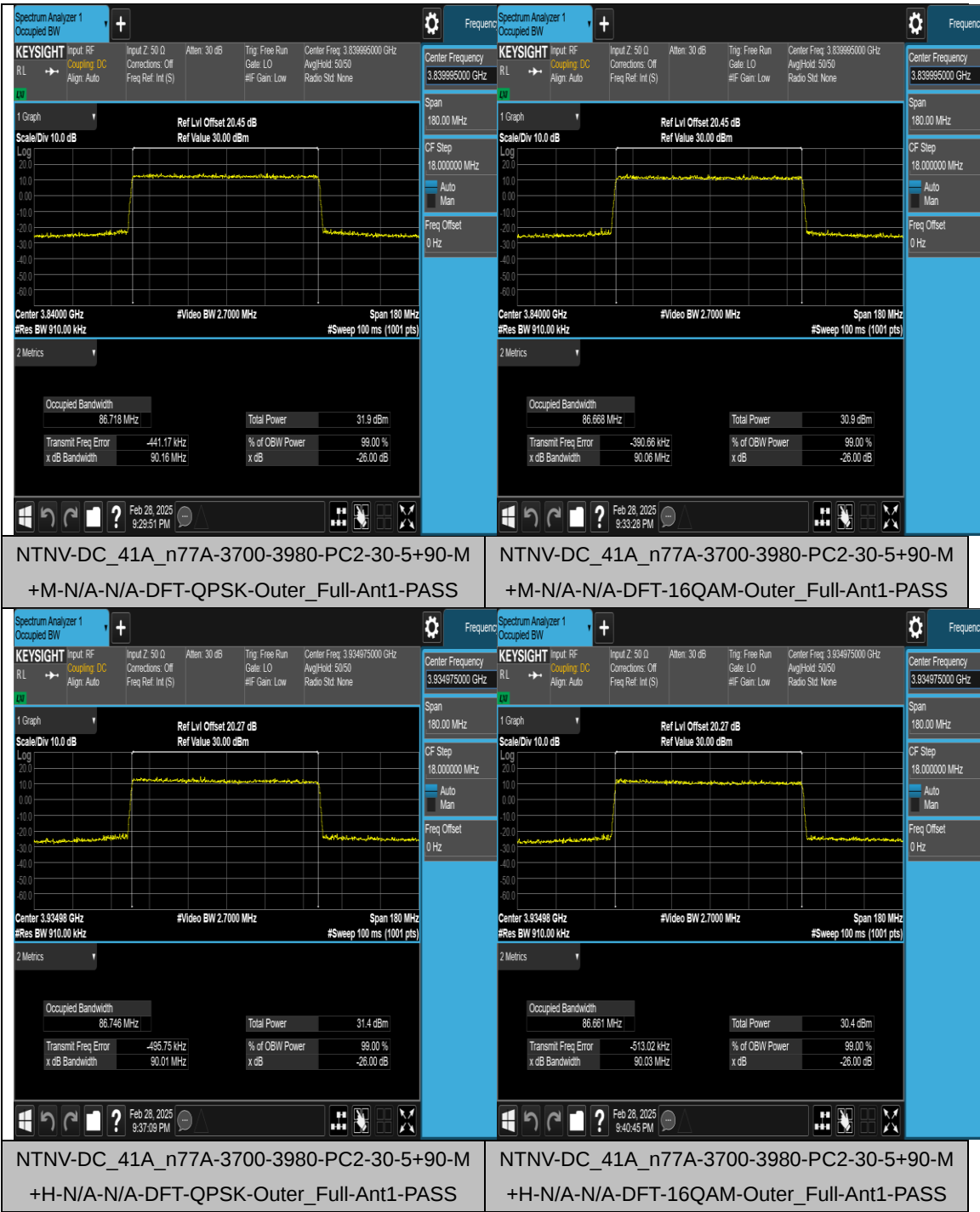


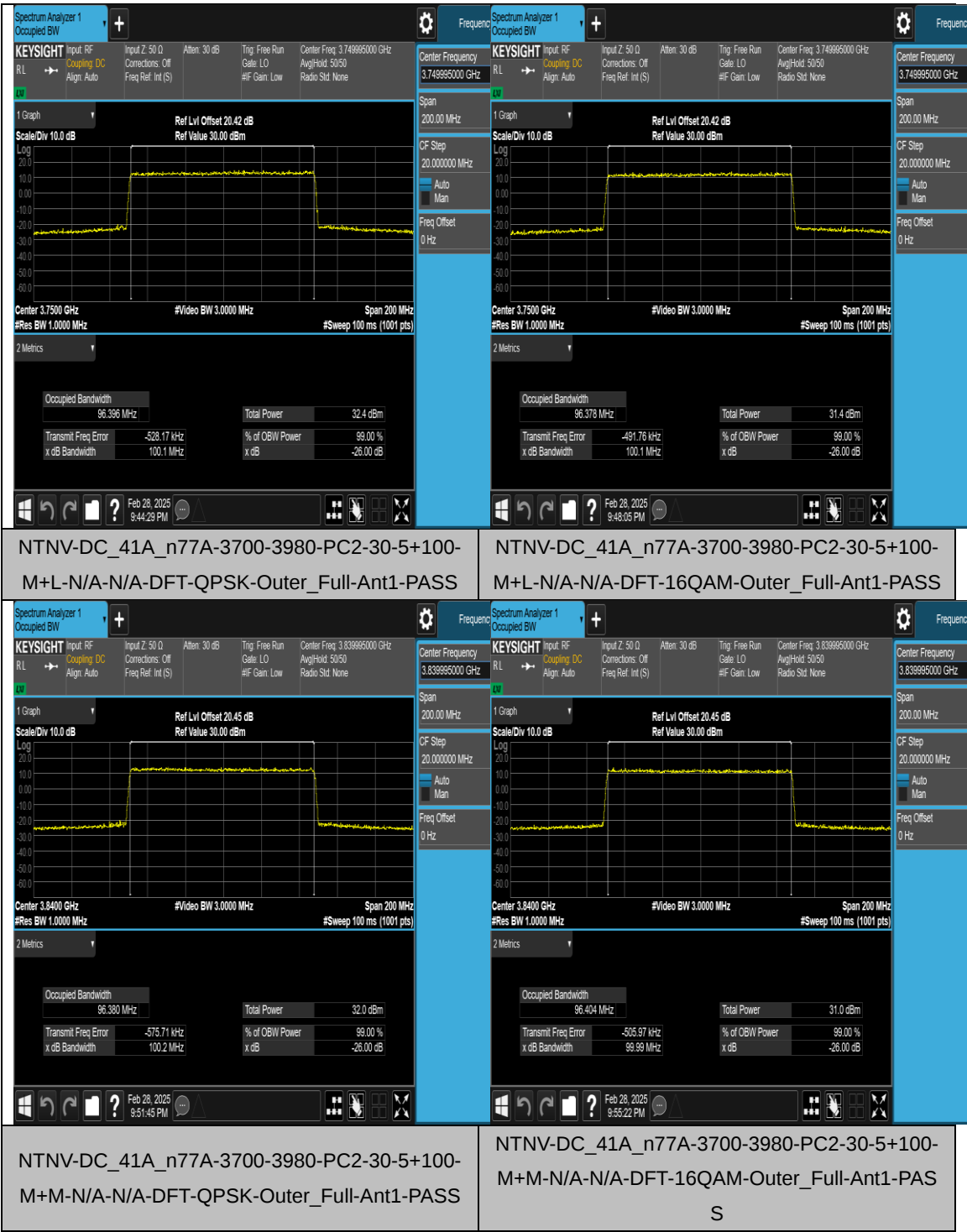


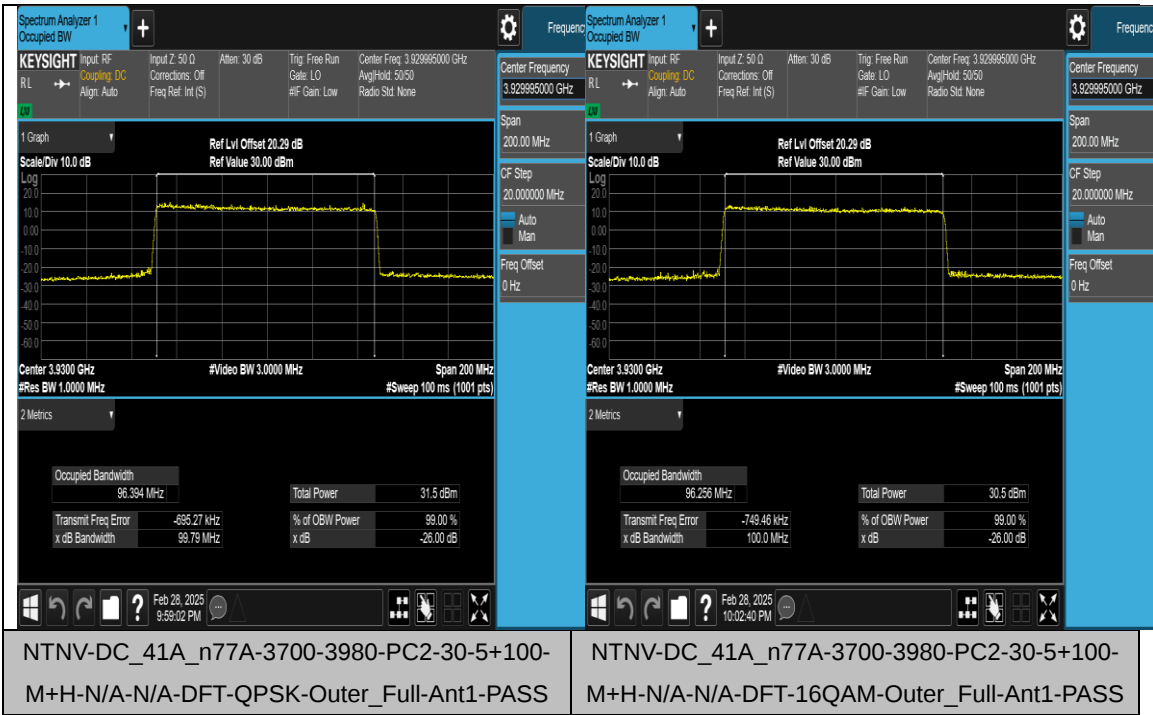












Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	-31.09	18.09	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-34.13	21.13	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	-32.24	19.24	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-30.22	17.22	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	-32.15	19.15	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-30.10	17.10	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	-33.38	20.38	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-33.56	20.56	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	-32.86	19.86	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-32.89	19.89	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	-33.92	20.92	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-35.49	22.49	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	-32.89	19.89	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	-32.65	19.65	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	-34.32	21.32	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	-36.51	23.51	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	-34.03	21.03	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	-32.45	19.45	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	-35.02	22.02	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	-36.02	23.02	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	-34.14	21.14	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	-36.94	23.94	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	-35.50	22.50	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	-32.12	19.12	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	-34.79	21.79	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	-34.63	21.63	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	-35.40	22.40	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	-36.05	23.05	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	-34.25	21.25	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	-35.85	22.85	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	-36.30	23.30	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	-36.45	23.45	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	-35.46	22.46	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	-36.67	23.67	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	-36.65	23.65	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	-35.81	22.81	PASS

DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	-34.71	21.71	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	-36.15	23.15	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	-36.42	23.42	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	-32.83	19.83	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	-35.61	22.61	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	-34.07	21.07	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	-36.66	23.66	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	-32.88	19.88	PASS

Test Graphs

