Appendix for 38A_n78A(3450-3550MHz)

Product Name: TITAN 5400
Model No: G5G-X62O

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	24.52	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	25.50	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	24.59	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	24.50	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	23.51	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	24.55	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	23.36	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	23.90	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	25.17	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	26.08	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	25.09	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	25.15	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	24.15	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	25.08	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.79	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	24.55	PC2	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	25.11	PC2	PASS
DC_38A_n78A-3	30	5+10	DFT-QPSK	M+H	Inner_Full	26.10	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	24.85	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	25.14	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	24.10	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	25.20	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	24.15	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	23.91	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	24.84	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	25.81	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	24.82	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	25.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	23.75	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	24.78	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	23.70	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	23.88	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	25.33	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	26.32	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	25.32	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	25.52	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	24.31	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	25.29	PC2	PASS
DC_38A_n78A-3	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	24.30	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	24.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	25.19	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	26.22	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	25.41	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	25.11	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	24.21	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	25.20	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	24.09	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	24.69	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	24.85	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	25.76	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	24.45	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	25.11	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	23.76	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	24.73	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	23.28	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.76	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	25.31	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	26.30	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	25.10	PC2	PASS
DC_38A_n78A-3	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	25.25	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	24.27	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	25.24	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	24.45	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	24.46	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	25.22	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	26.20	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	25.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	25.39	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	24.17	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	25.26	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	24.23	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	24.14	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	24.95	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	25.94	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	24.68	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	25.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	23.99	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	25.00	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.72	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	24.42	PC2	PASS
DC_38A_n78A-3	30	5+30	DFT-QPSK	M+M	Outer_Full	25.38	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	26.28	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	25.22	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	25.40	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	24.36	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	25.30	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	24.46	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	24.68	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	25.23	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	26.19	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	25.58	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	25.30	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	24.23	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	25.11	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	24.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	23.96	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	25.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	26.03	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	24.78	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	25.22	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	24.01	PC2	PASS
DC_38A_n78A-3	30	5+40	DFT-16QAM	M+L	Inner_Full	25.04	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.93	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.75	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	25.30	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	26.32	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	25.34	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	25.40	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	24.39	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	25.32	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	24.57	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	24.17	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	25.33	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	26.20	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	25.43	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	25.42	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	24.23	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	25.26	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	24.17	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	24.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	24.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	26.07	PC2	PASS
DC_38A_n78A-3	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	24.55	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.85	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	23.94	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	25.07	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	23.56	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.92	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	25.16	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	26.11	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	25.25	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	25.06	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	24.12	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	25.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	24.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.98	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	25.00	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	26.03	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	25.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	25.02	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	24.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	25.06	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	24.05	PC2	PASS
DC_38A_n78A-3	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	23.97	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	25.03	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	26.15	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	24.50	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	24.95	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	24.03	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	25.12	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.58	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	24.17	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	25.20	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	26.14	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	25.09	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	24.95	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	24.20	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	25.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.85	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.84	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	25.10	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	26.10	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	25.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	25.01	PC2	PASS
DC_38A_n78A-3	30	5+60	DFT-16QAM	M+H	Outer_Full	24.12	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	25.12	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	24.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	24.07	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	24.90	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	26.02	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	24.51	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	24.96	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	23.90	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	25.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	23.48	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.93	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	25.07	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	26.10	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.76	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	25.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	24.16	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	25.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.62	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.94	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	25.12	PC2	PASS
DC_38A_n78A-3	30	5+70	DFT-QPSK	M+H	Inner_Full	26.11	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	25.06	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	25.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	24.12	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	25.07	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.80	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	23.89	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	24.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	26.07	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	24.47	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.82	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	23.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	25.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.46	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.94	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	25.06	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	26.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	24.57	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.87	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	24.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	25.15	PC2	PASS
DC_38A_n78A-3	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.57	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.70	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	25.02	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	26.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.98	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	25.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	23.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	25.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	24.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.86	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	24.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	26.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	24.38	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.90	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	23.94	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	25.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.54	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.96	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	25.01	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	26.10	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.52	PC2	PASS
DC_38A_n78A-3	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	25.04	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	24.08	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	25.11	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.57	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	24.11	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	24.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	26.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.55	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	25.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	23.97	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	25.05	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.63	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	24.04	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	25.02	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	26.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	24.22	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	24.85	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	23.99	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	25.12	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	23.21	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	24.02	PC2	PASS
DC_38A_n78A-3	30	5+100	DFT-QPSK	M+M	Outer_Full	25.03	PC2	PASS

450-3550								
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	26.14	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	24.29	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.95	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	24.03	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	25.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	23.09	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.75	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	25.02	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	26.14	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	24.23	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	24.86	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	24.00	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	25.13	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	23.15	PC2	PASS
DC_38A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	23.88	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	5.46	≤13	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	6.25	≤13	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	5.25	≤13	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	6.22	≤13	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	5.29	≤13	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	6.06	≤13	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	5.31	≤13	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	6.16	≤13	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	5.47	≤13	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	6.27	≤13	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	5.15	≤13	PASS
DC_38A_n	30	5+15	DFT-16QAM	M+H	Outer_Full	6.03	≤13	PASS

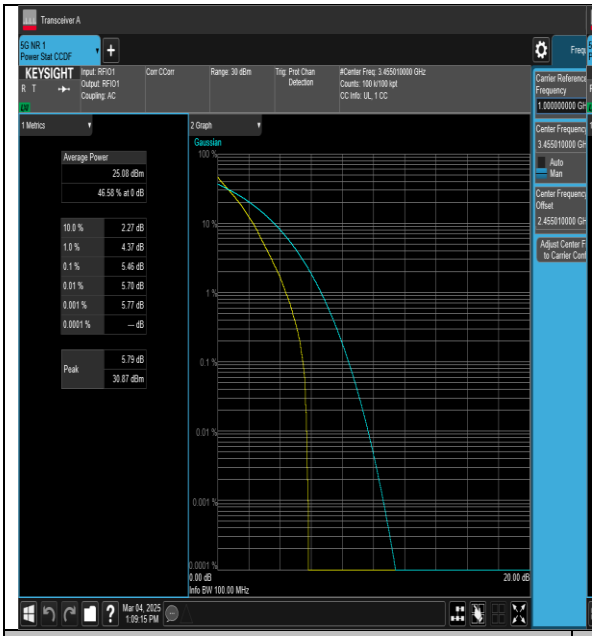
78A-3450-3550								
DC_38A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	6.10	≤13	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	5.51	≤13	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	6.32	≤13	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	5.46	≤13	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	6.17	≤13	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	5.40	≤13	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	6.15	≤13	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	5.48	≤13	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	6.24	≤13	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	5.46	≤13	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	6.08	≤13	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	5.50	≤13	PASS
DC_38A_n	30	5+40	DFT-16QAM	M+L	Outer_Full	6.21	≤13	PASS

78A-3450-3550								
DC_38A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	5.67	≤13	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	6.33	≤13	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	5.44	≤13	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	6.18	≤13	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	5.17	≤13	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	6.02	≤13	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	5.46	≤13	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6.19	≤13	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	5.51	≤13	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	6.28	≤13	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	5.31	≤13	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	6.15	≤13	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	5.50	≤13	PASS
DC_38A_n	30	5+60	DFT-16QAM	M+M	Outer_Full	6.27	≤13	PASS

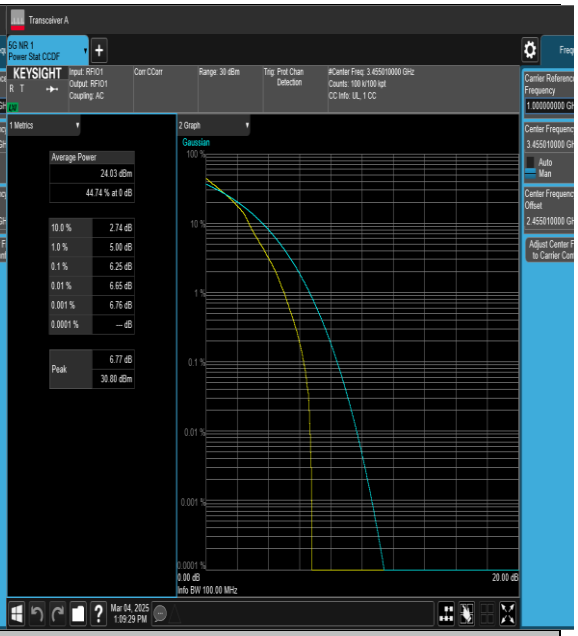
78A-3450-3550								
DC_38A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	5.13	≤13	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	6.06	≤13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	5.52	≤13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	6.27	≤13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	5.61	≤13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	6.32	≤13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	5.46	≤13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	6.21	≤13	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	5.26	≤13	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	6.19	≤13	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	5.45	≤13	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6.31	≤13	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	5.44	≤13	PASS
DC_38A_n	30	5+80	DFT-16QAM	M+H	Outer_Full	6.31	≤13	PASS

78A-3450-3550								
DC_38A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	5.54	≤13	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	6.33	≤13	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	5.38	≤13	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6.25	≤13	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	5.45	≤13	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	6.24	≤13	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	5.34	≤13	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	6.25	≤13	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	5.42	≤13	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	6.18	≤13	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	5.44	≤13	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	6.26	≤13	PASS

Test Graphs



NTNV-DC_38A_n78A-3450-3550-PC2-30-5+10-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS



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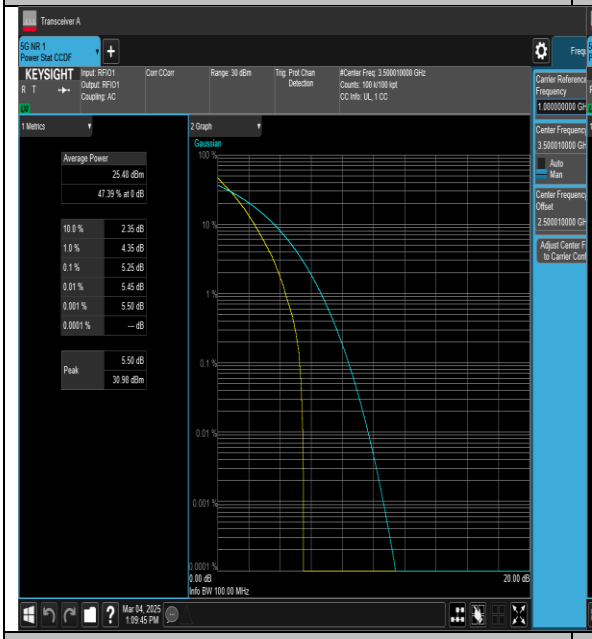

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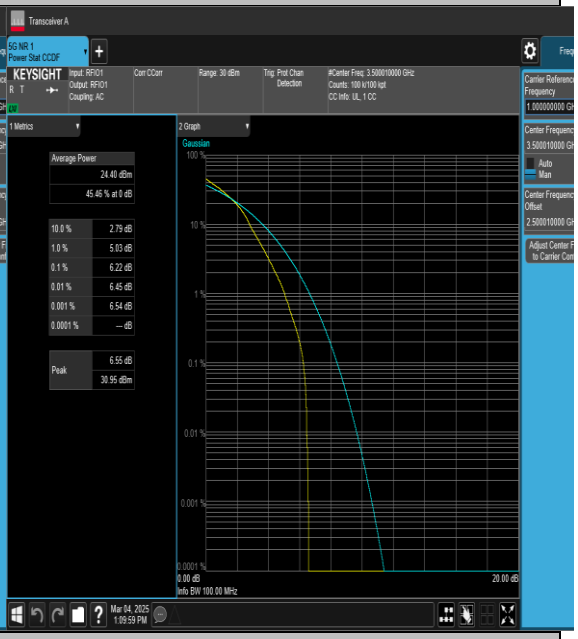

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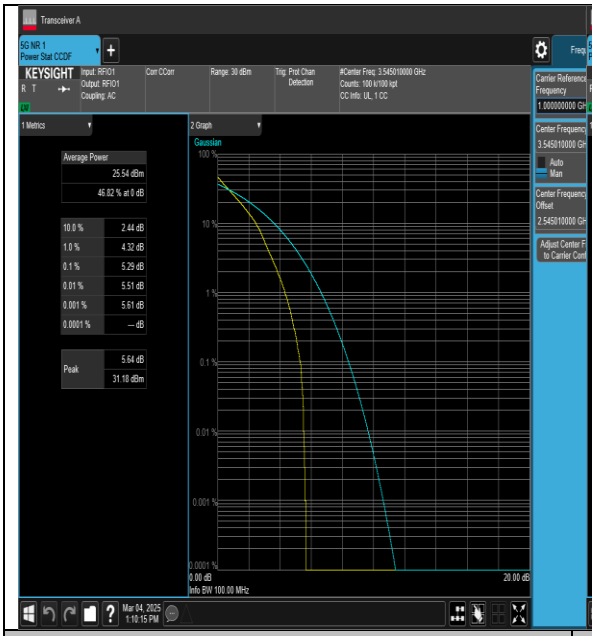

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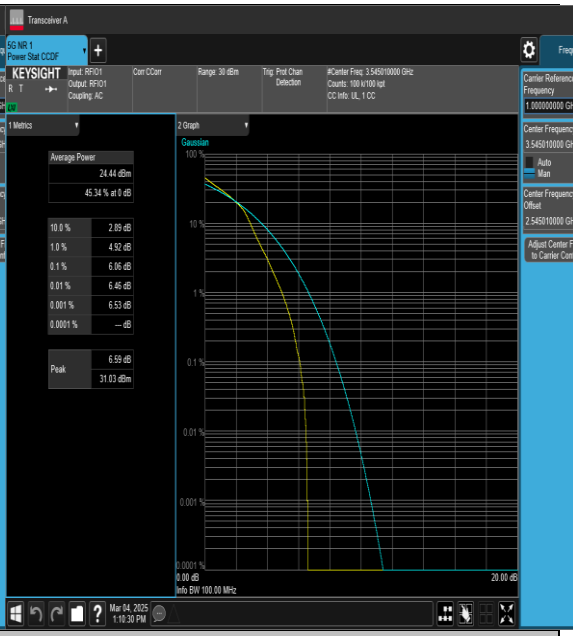

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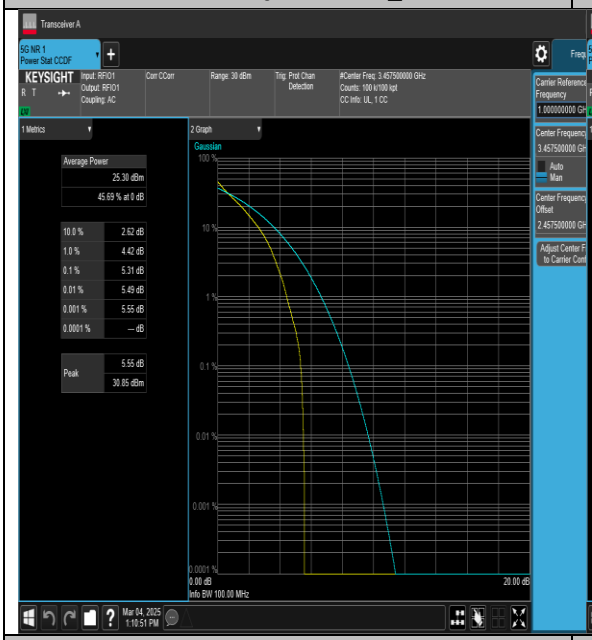

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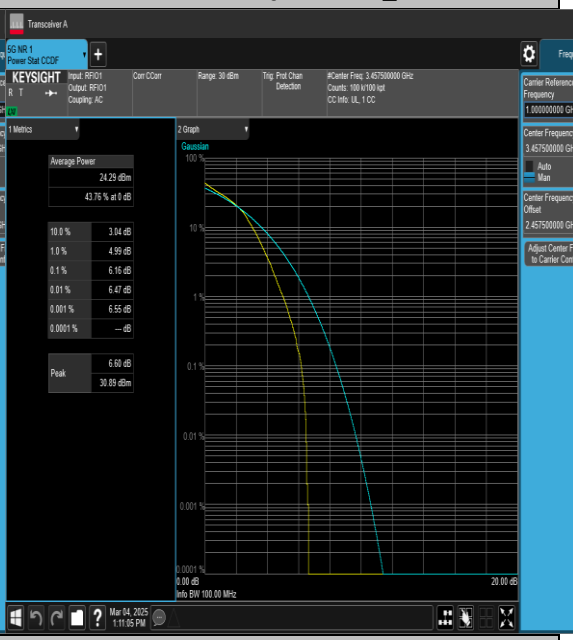

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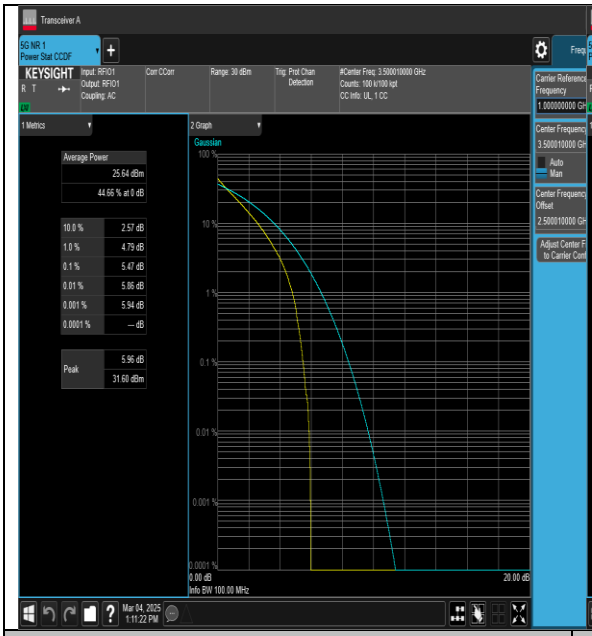

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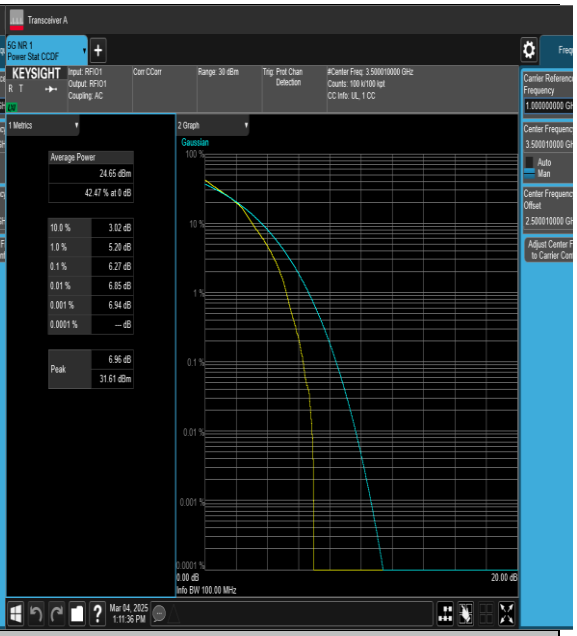

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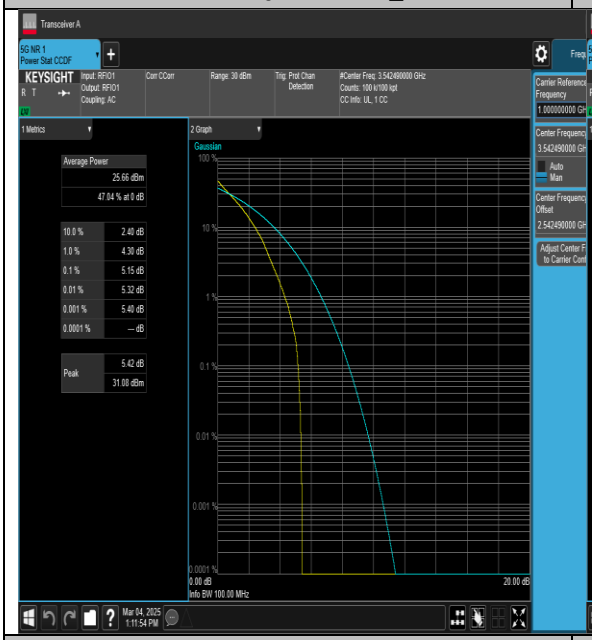

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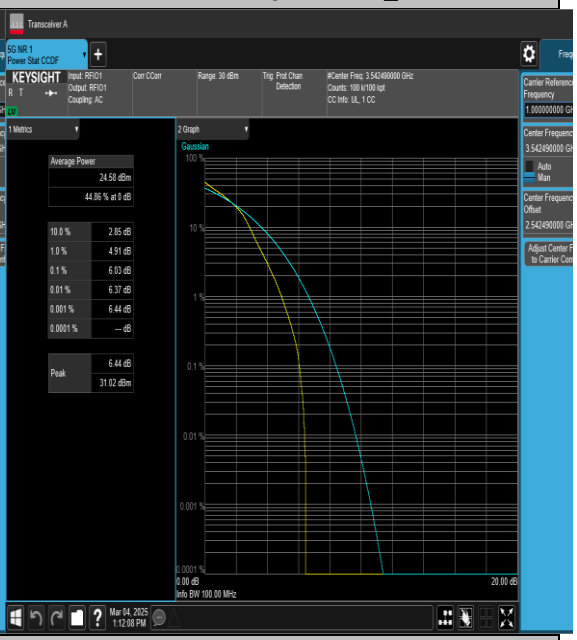

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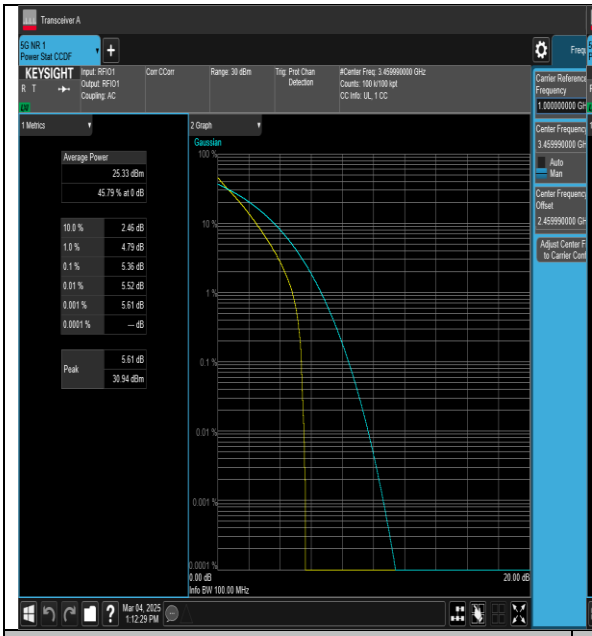

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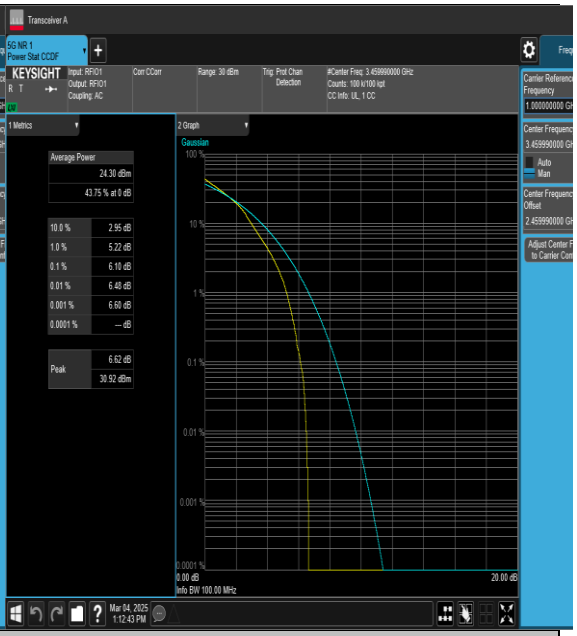

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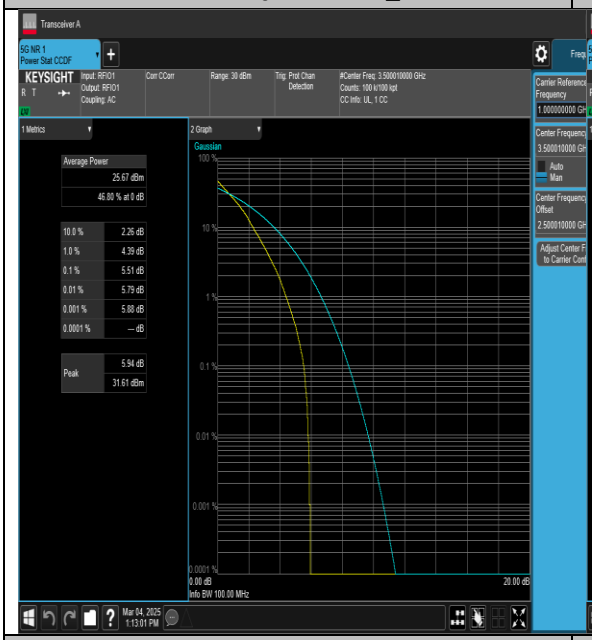

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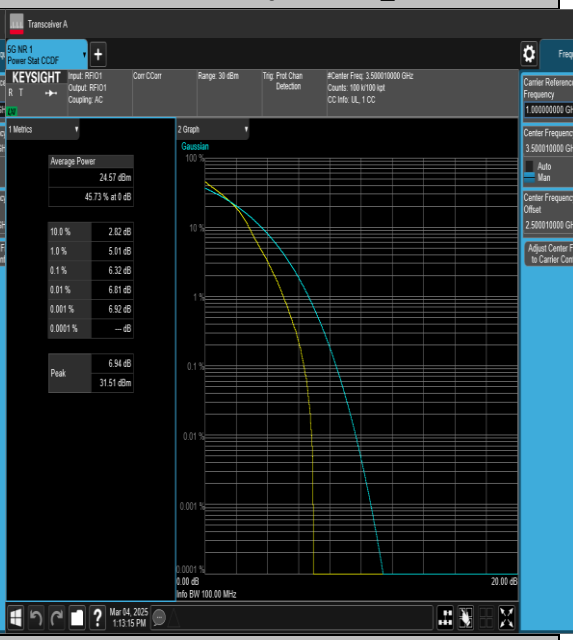

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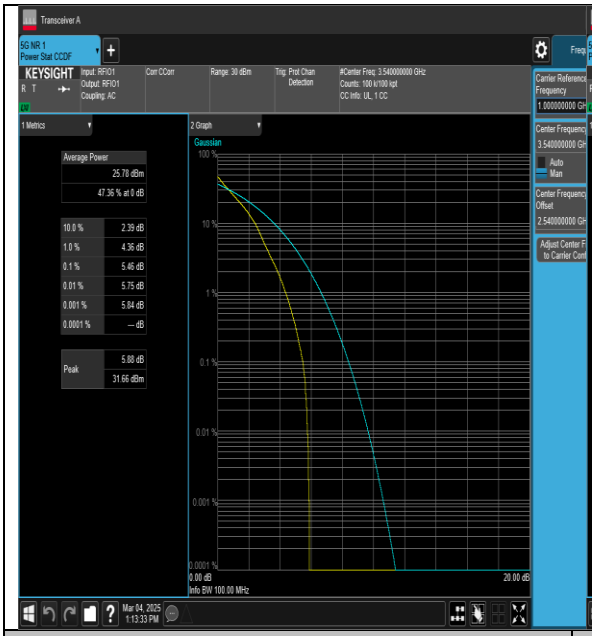

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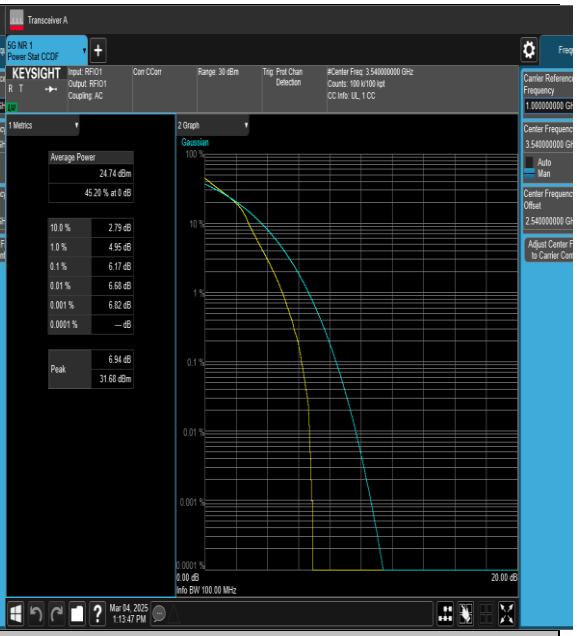

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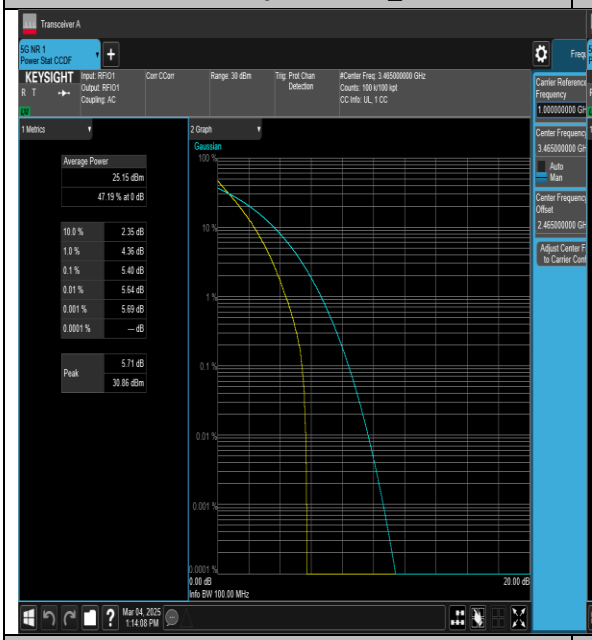

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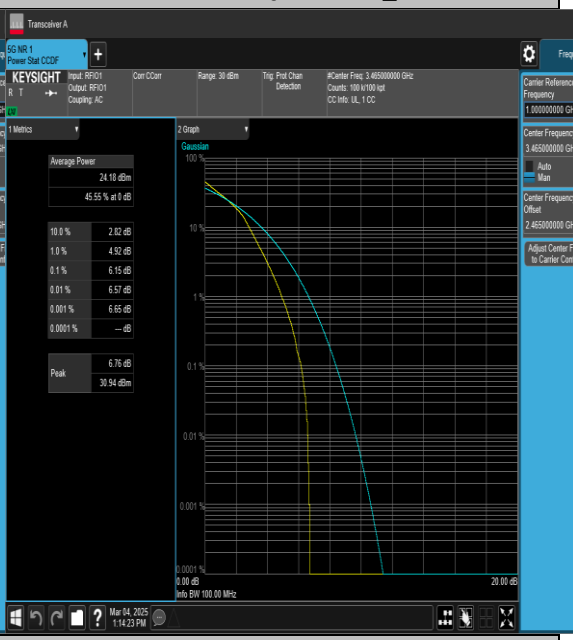

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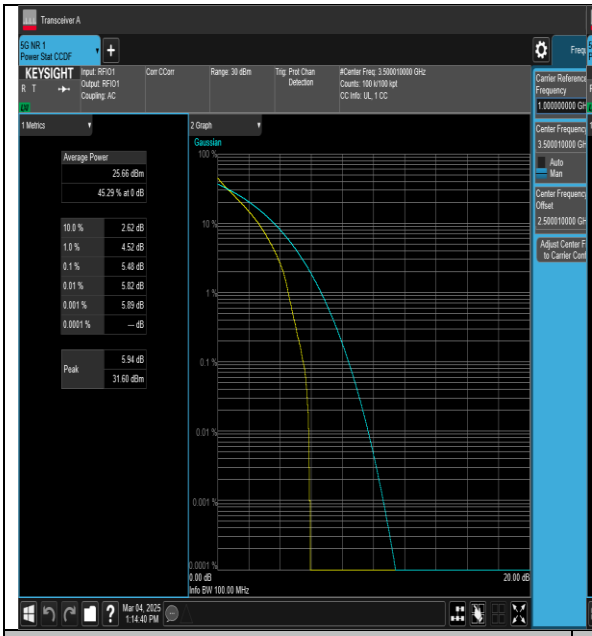

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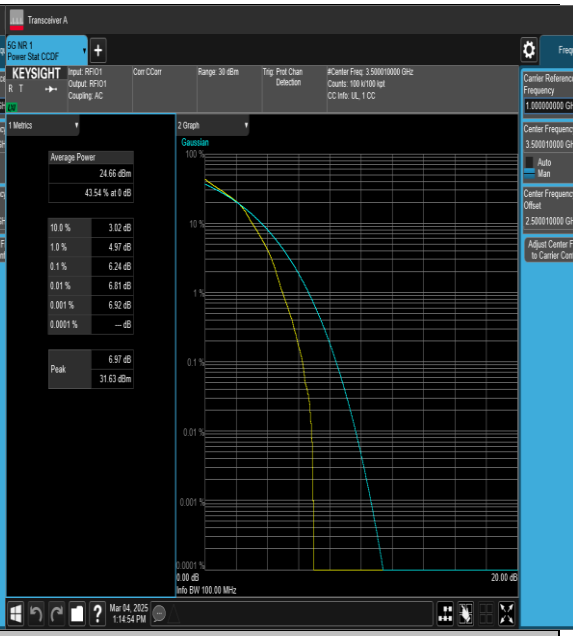

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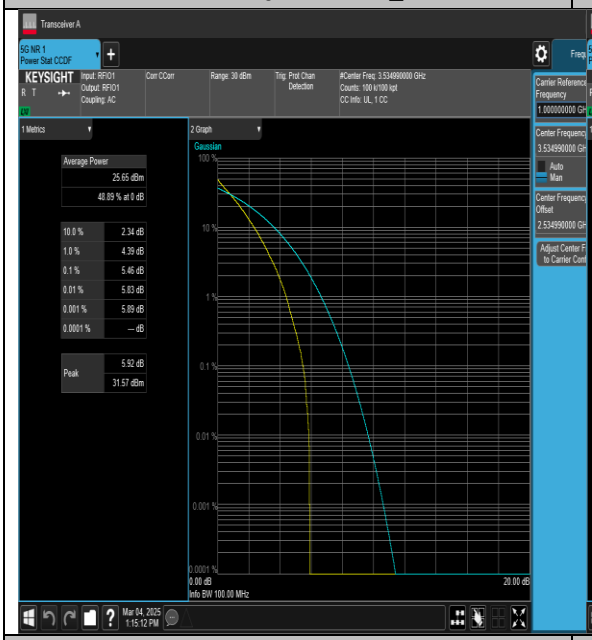

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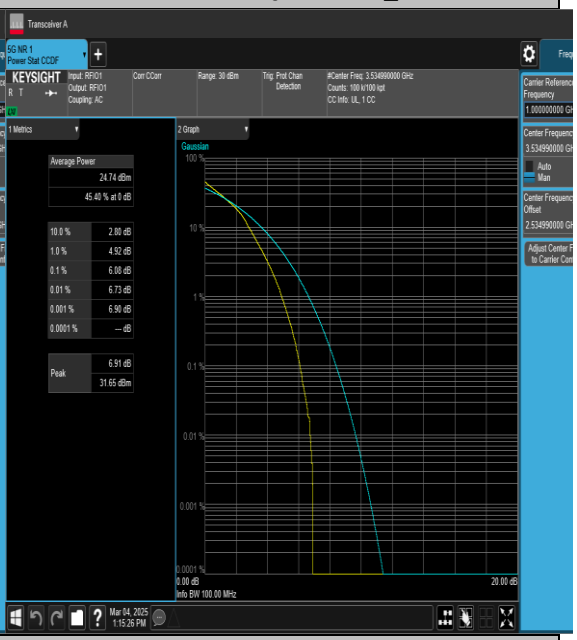

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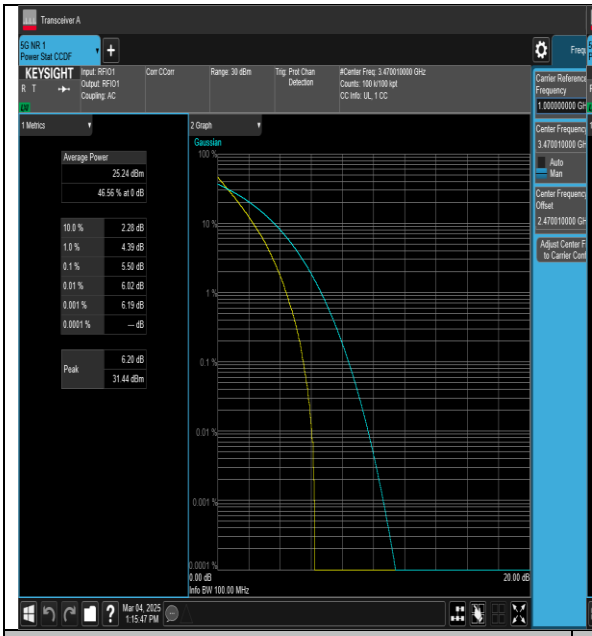

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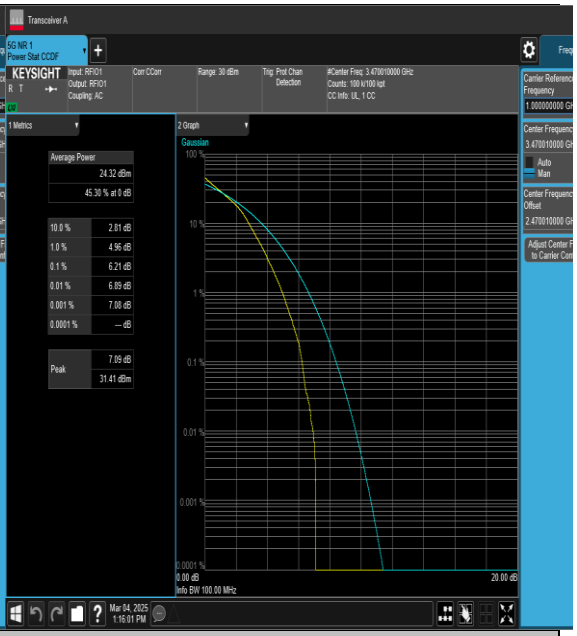

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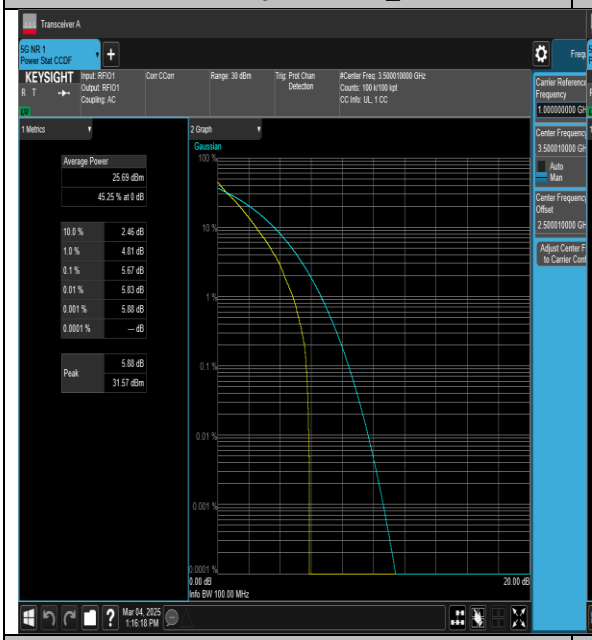

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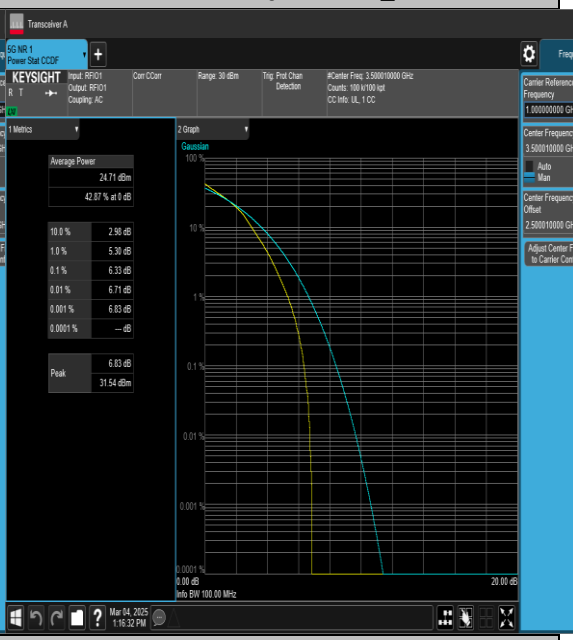

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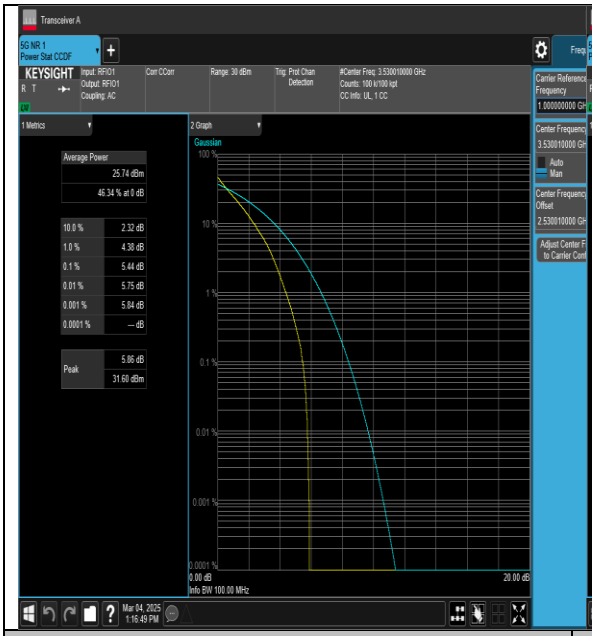

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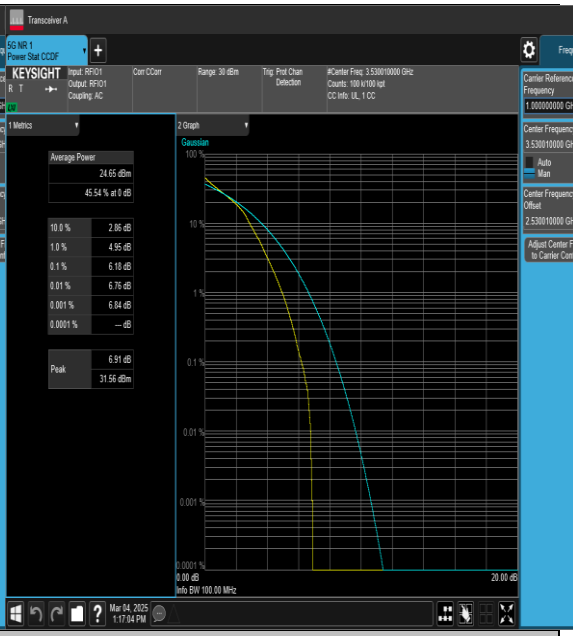

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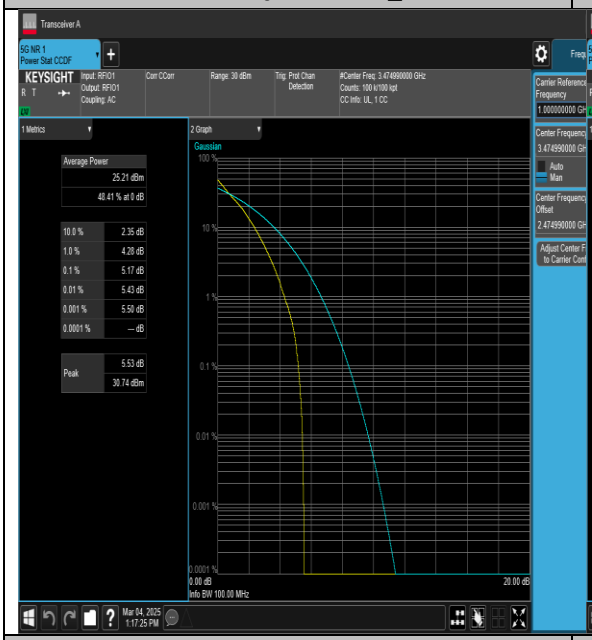

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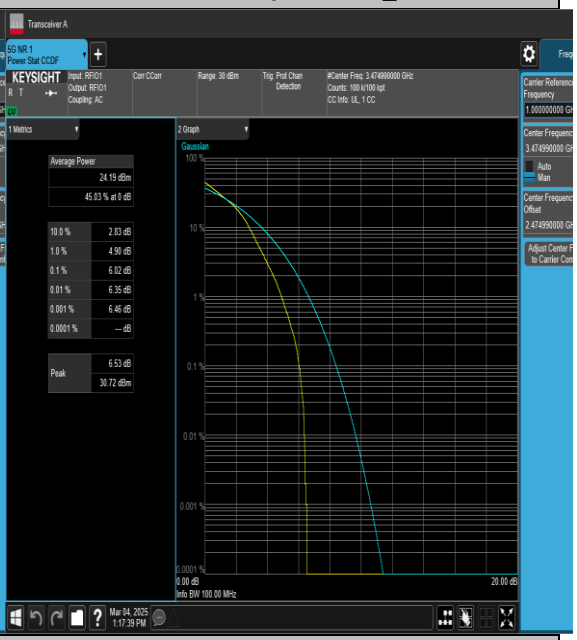

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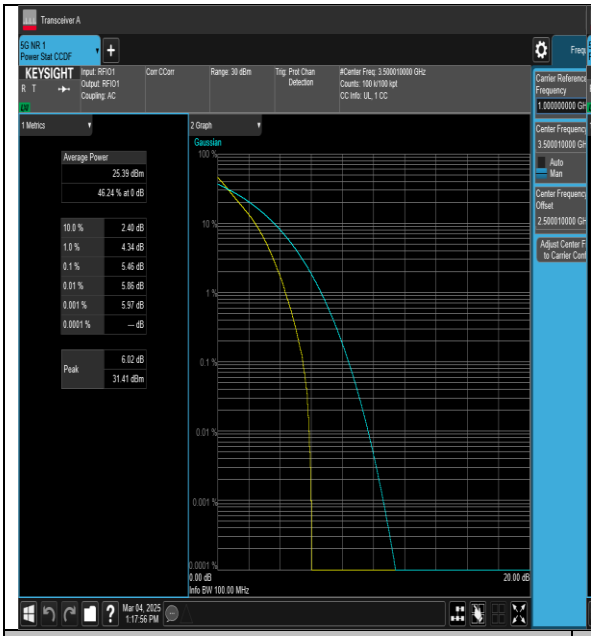

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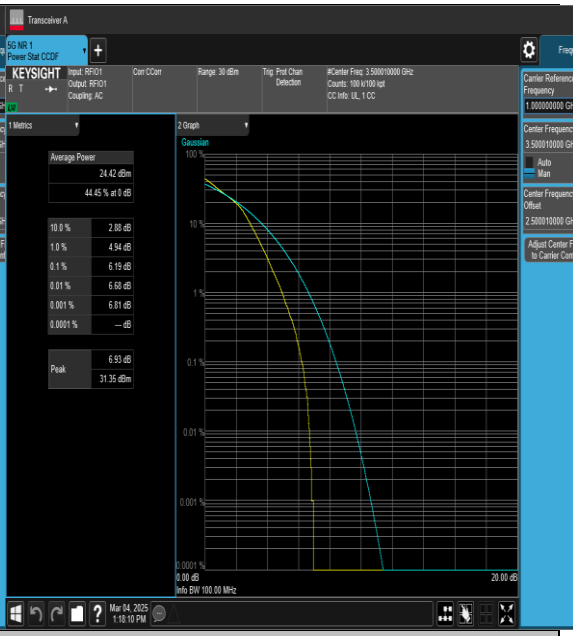

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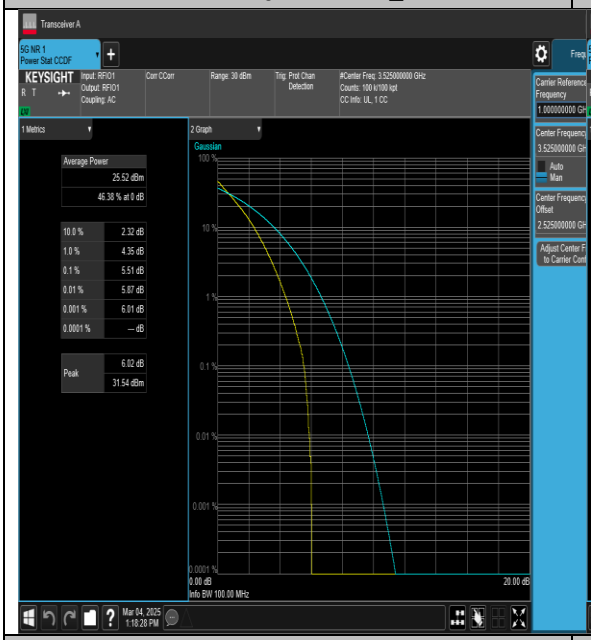

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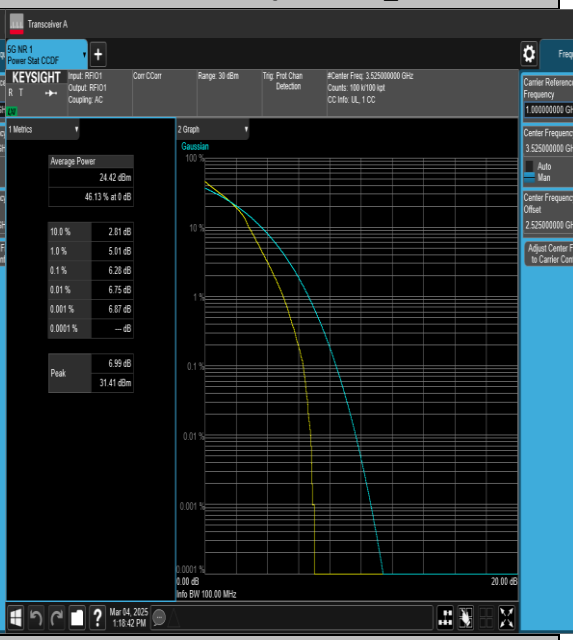

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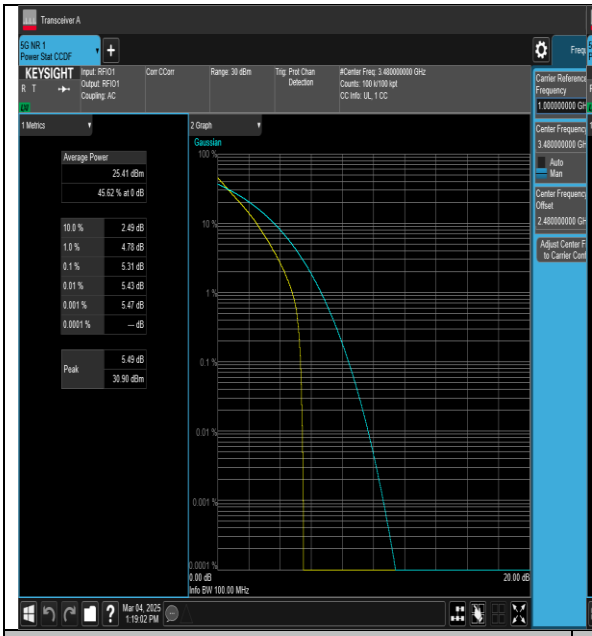

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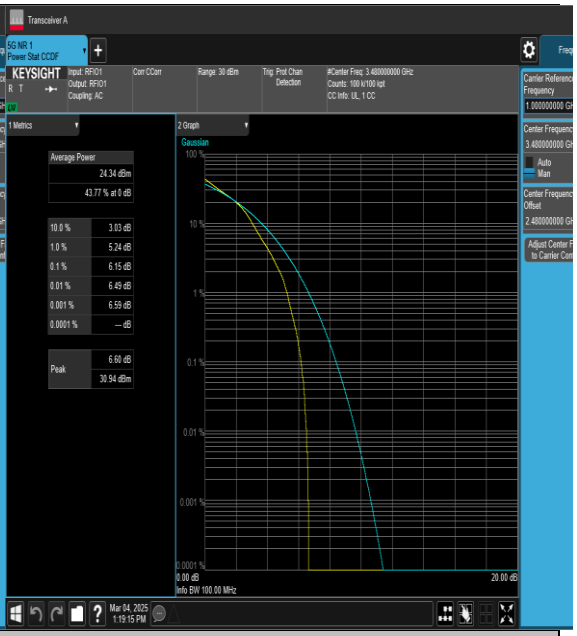

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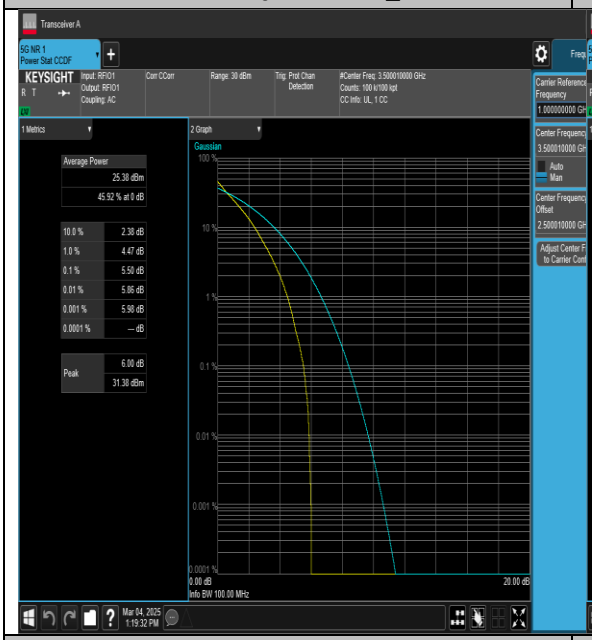

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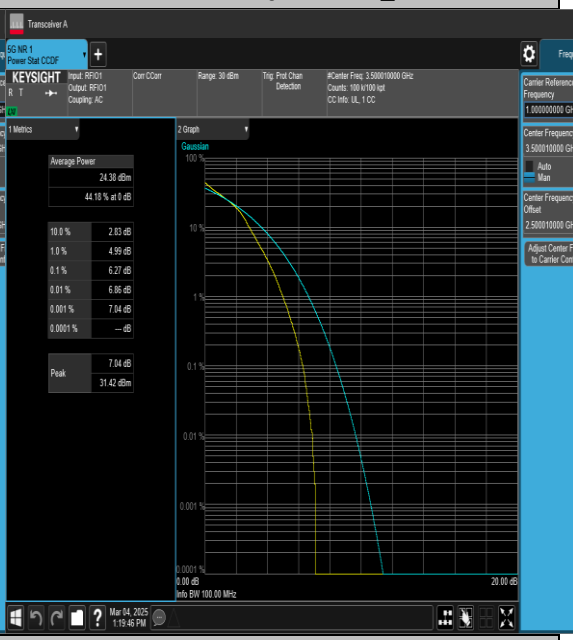

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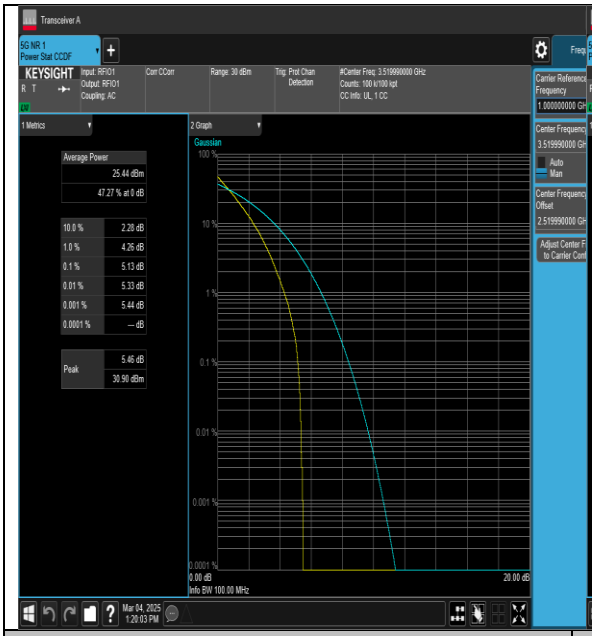

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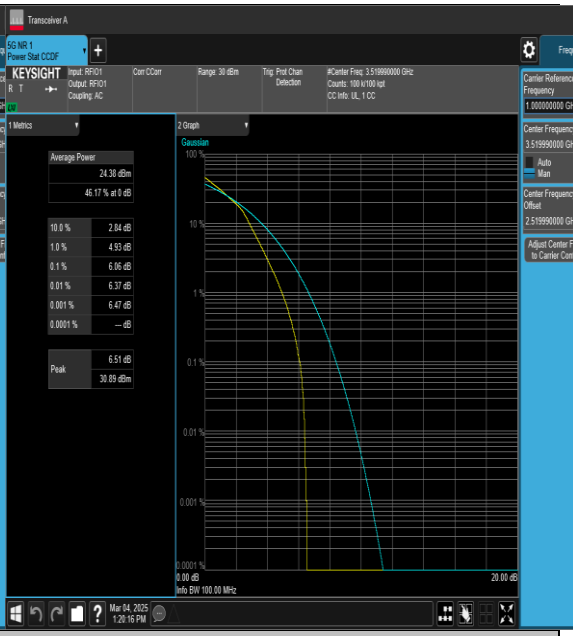

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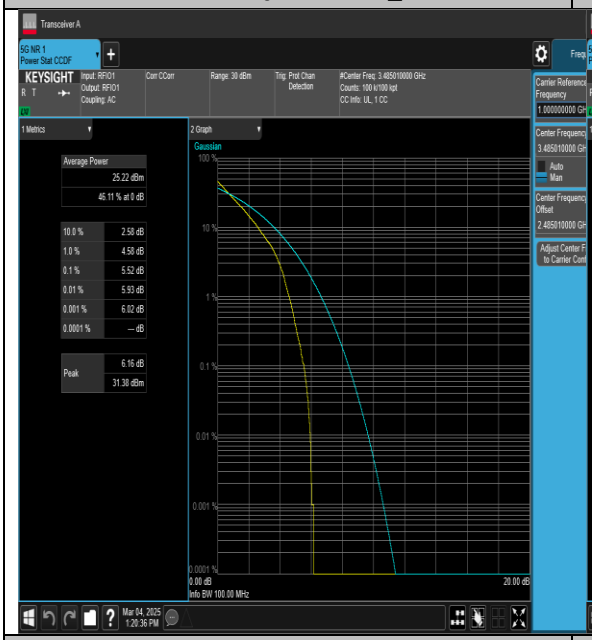

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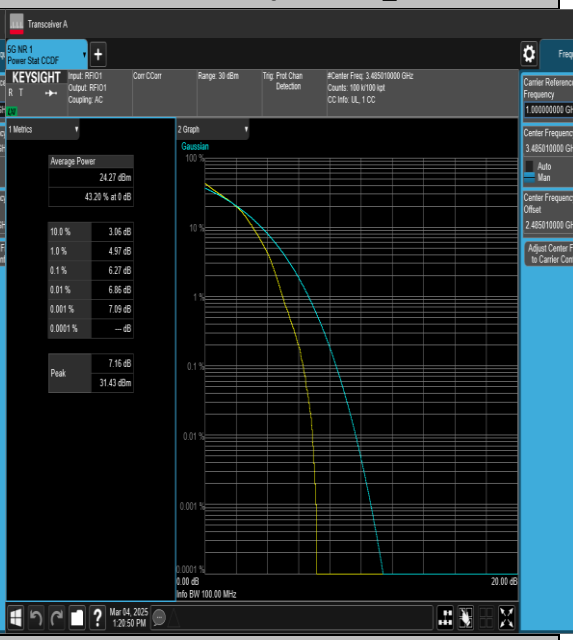

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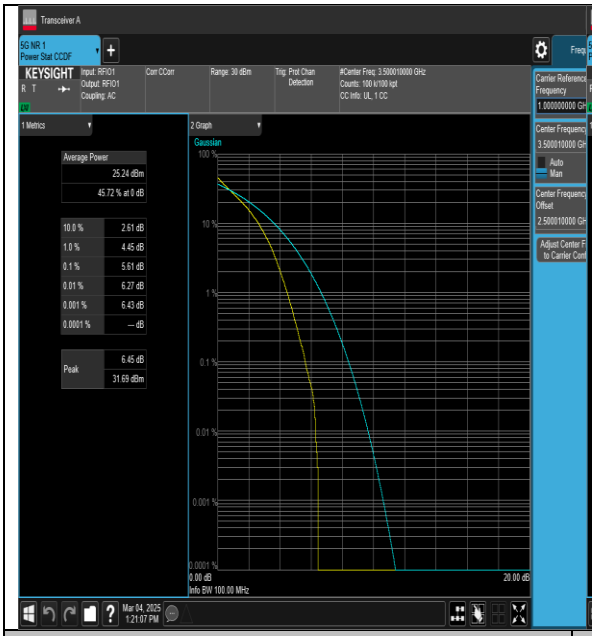

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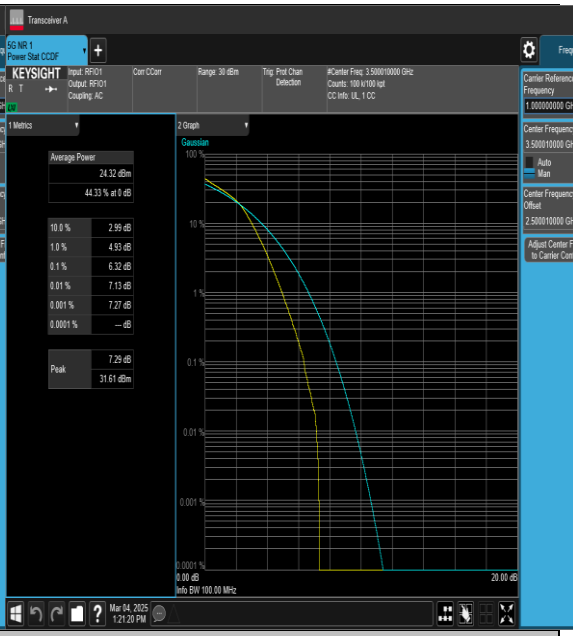

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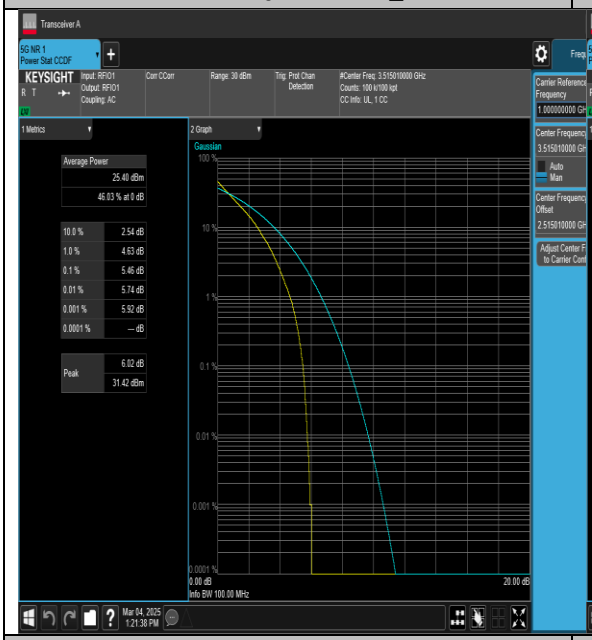

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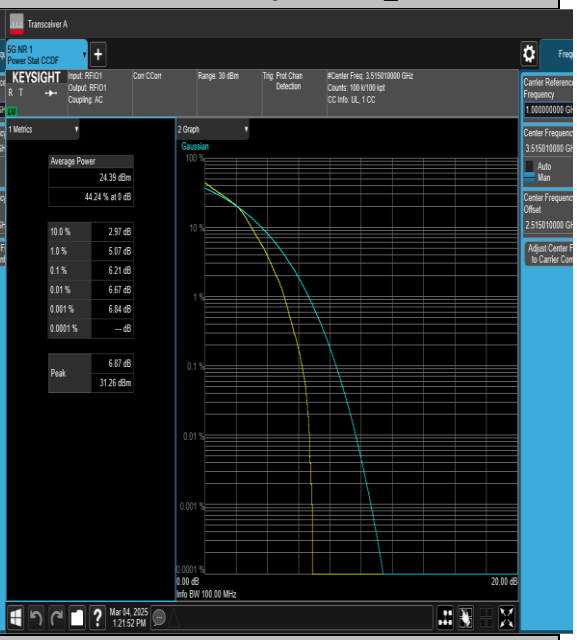

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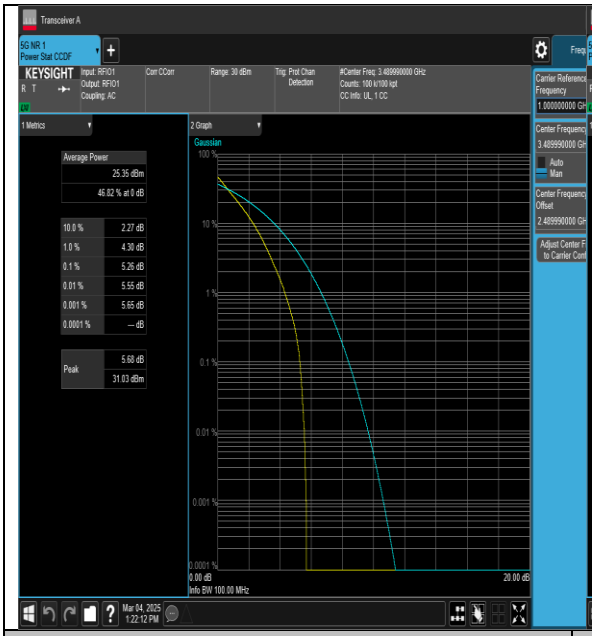

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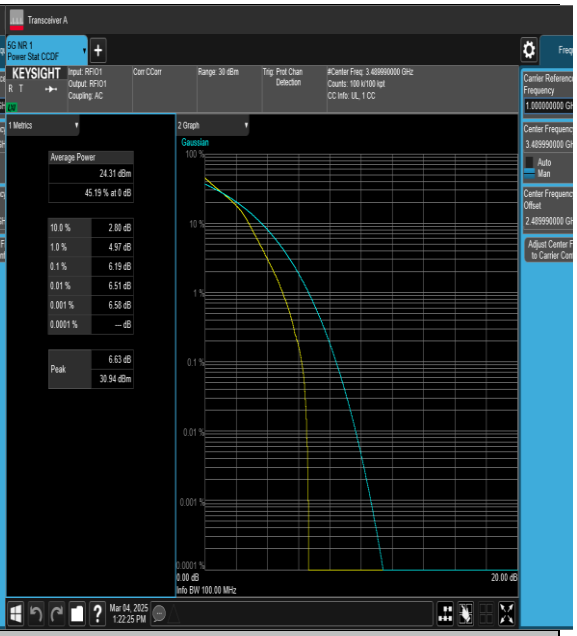

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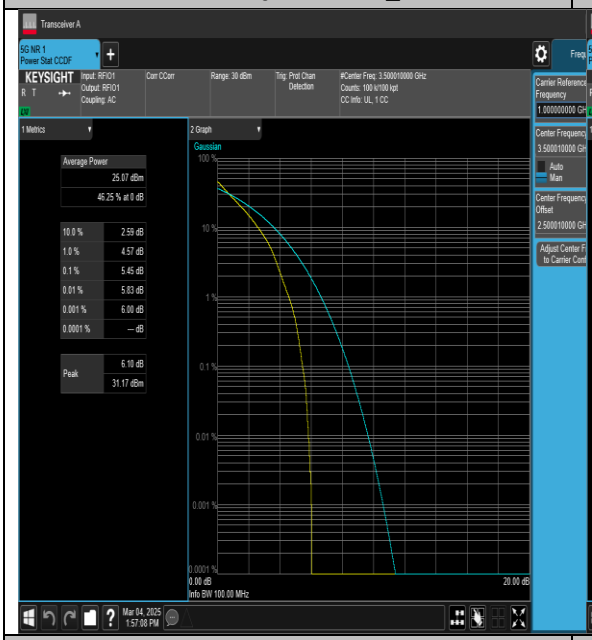

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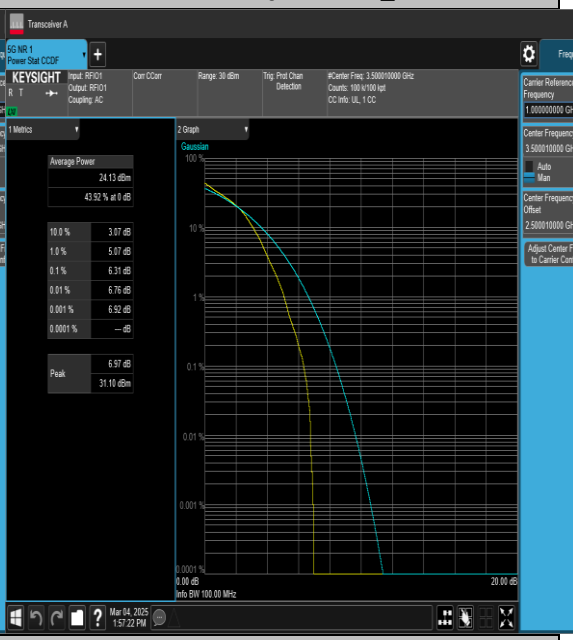

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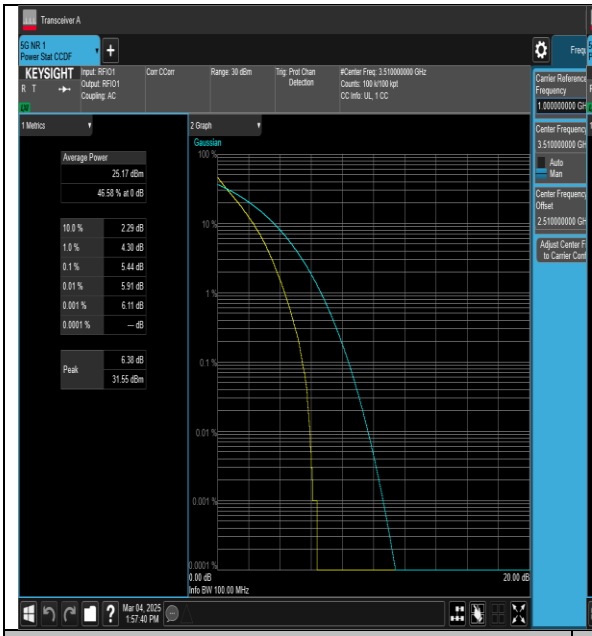

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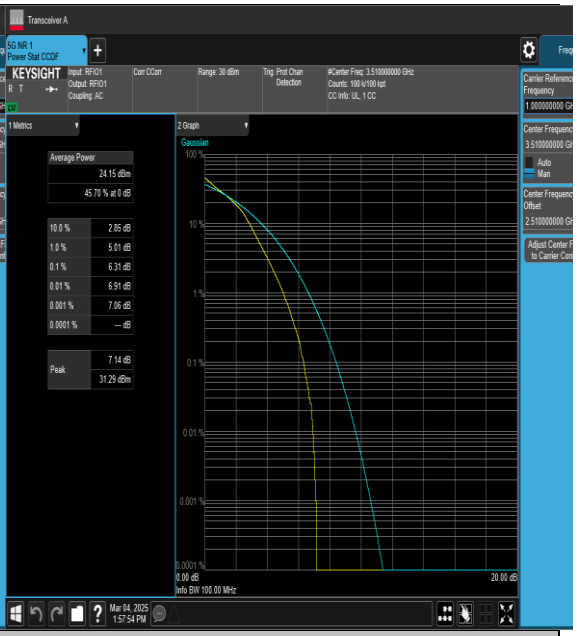

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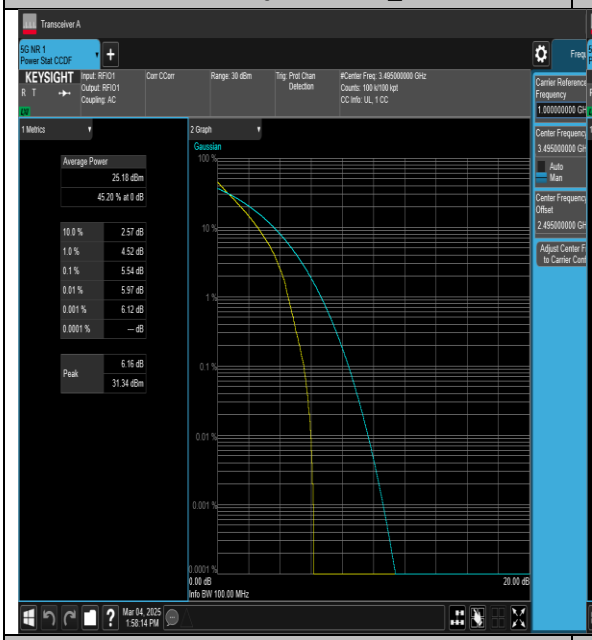

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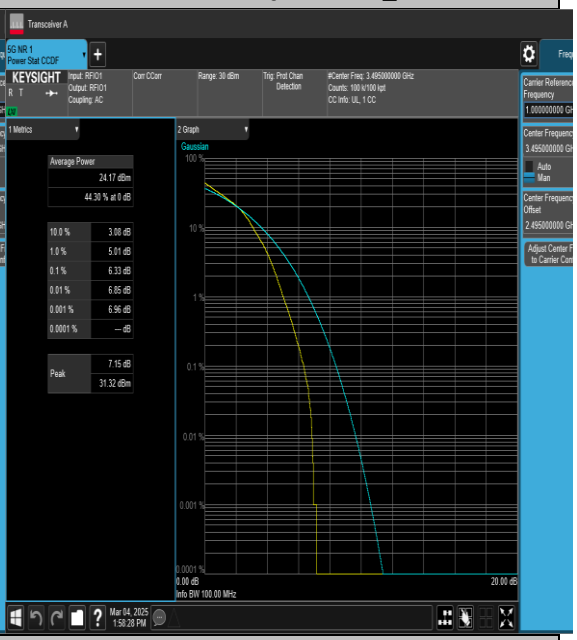

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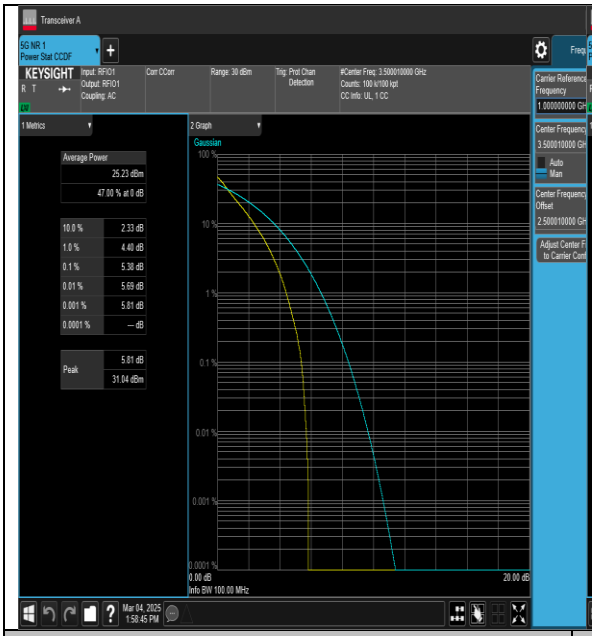

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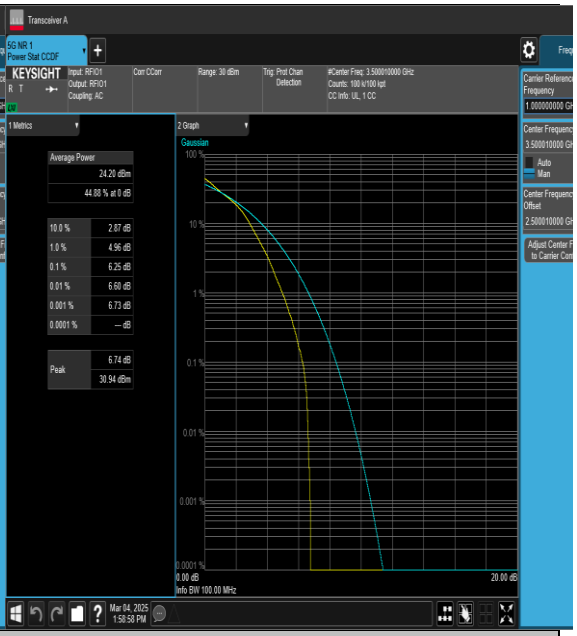

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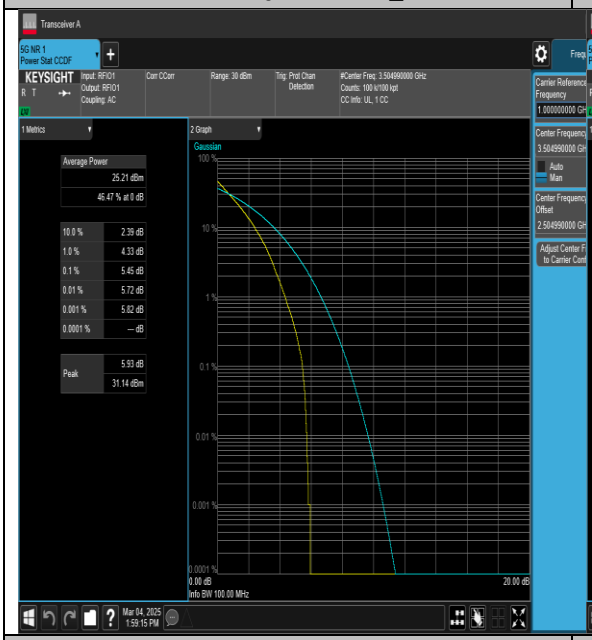

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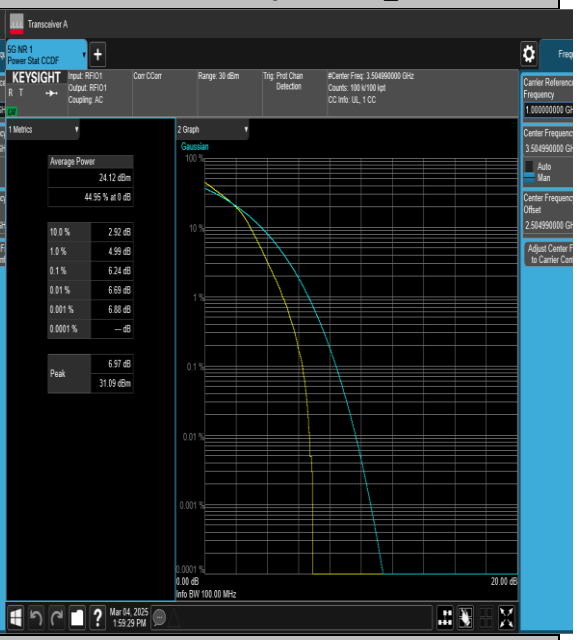

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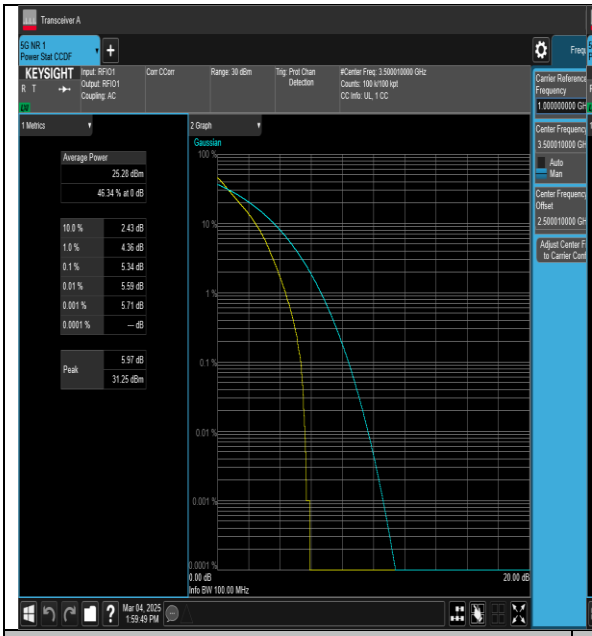

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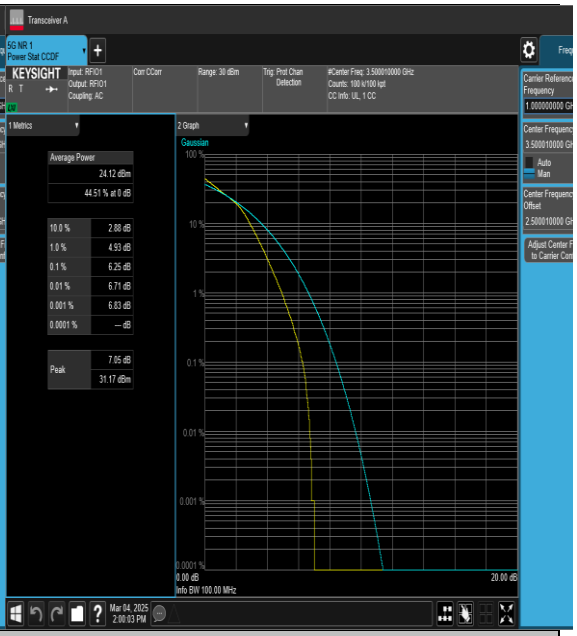

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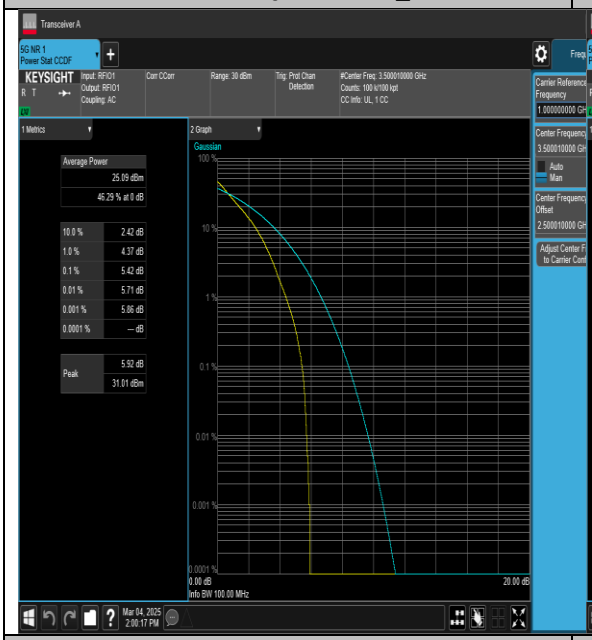

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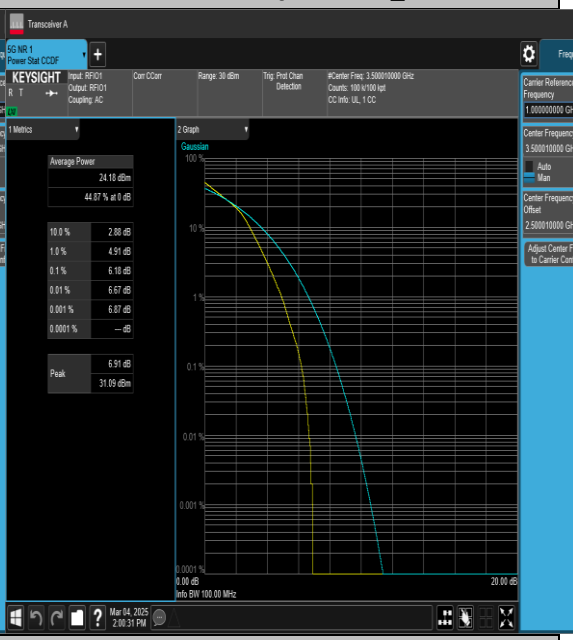

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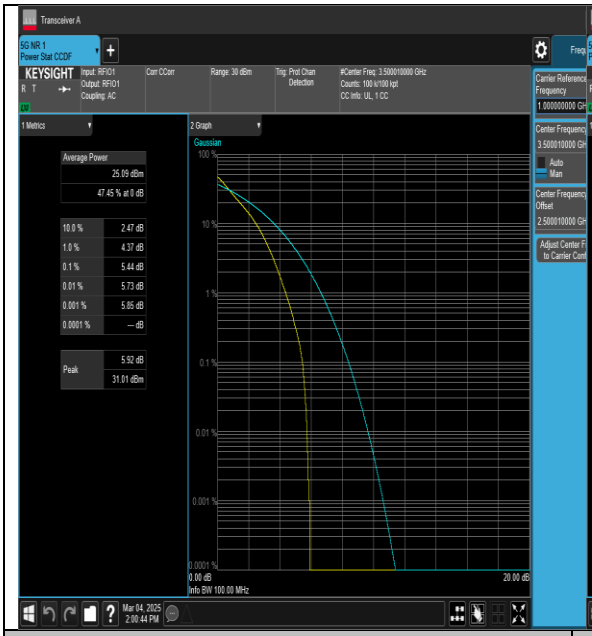	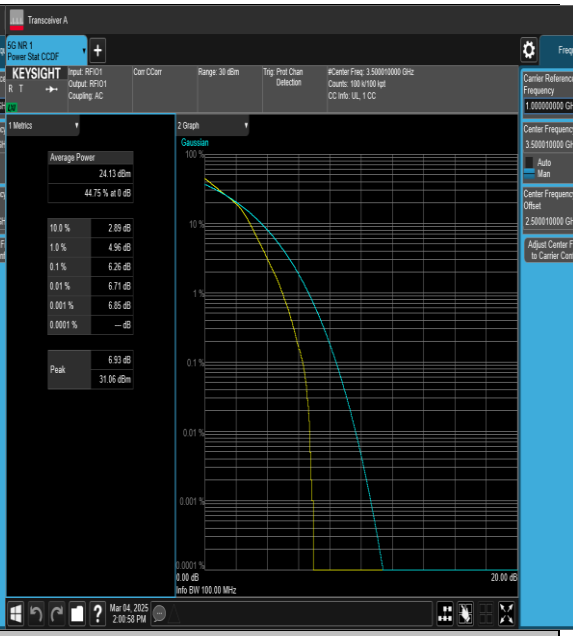
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Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

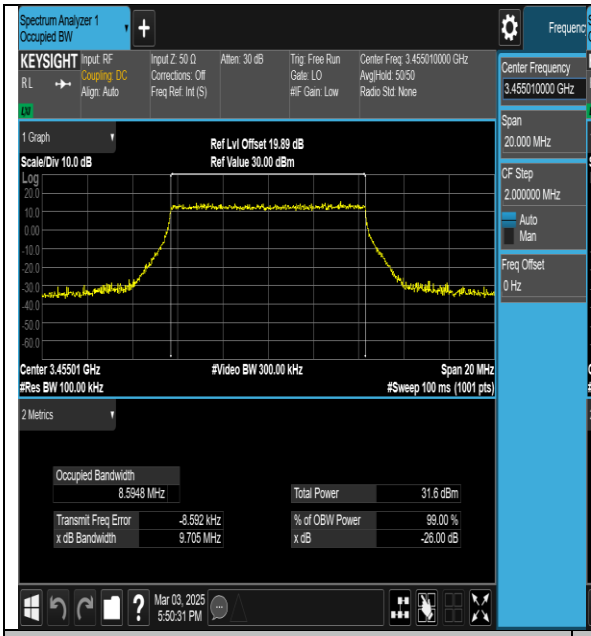
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	8.5948	9.705	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	8.6125	9.721	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6037	9.894	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	8.5955	9.736	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	8.5965	9.709	PASS
DC_38A_n78A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5884	9.656	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	12.888	14.32	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	12.873	14.35	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	12.887	14.35	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	12.885	14.35	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	12.895	14.32	PASS
DC_38A_n78A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	12.887	14.30	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	17.850	19.31	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	17.866	19.50	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	17.866	19.45	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	17.875	19.46	PASS
DC_38A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	17.882	19.46	PASS

DC_38A_n78A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	17.868	19.34	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26.783	28.86	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26.779	28.73	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26.760	28.59	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26.747	28.56	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26.813	28.80	PASS
DC_38A_n78A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26.820	28.80	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	35.689	37.67	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	35.686	37.70	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	35.736	37.71	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	35.725	37.77	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	35.706	37.81	PASS
DC_38A_n78A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	35.715	37.88	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	45.694	47.95	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	45.710	48.05	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	45.647	47.86	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	45.626	48.06	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	45.636	47.92	PASS
DC_38A_n78A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	45.637	48.08	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	57.820	60.43	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	57.757	60.39	PASS

DC_38A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	57.748	60.41	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	57.811	60.44	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	57.825	60.41	PASS
DC_38A_n78A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	57.759	60.43	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	64.211	66.87	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	64.176	67.05	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	64.206	66.96	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	64.161	66.96	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	64.163	67.13	PASS
DC_38A_n78A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	64.197	66.99	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	77.127	80.10	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	77.049	80.08	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	77.010	80.12	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	77.025	80.09	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	77.081	80.16	PASS
DC_38A_n78A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	77.139	80.18	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	86.578	89.93	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	86.599	89.94	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	86.599	90.21	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	86.672	90.19	PASS
DC_38A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	86.620	90.18	PASS

DC_38A_n78A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	86.719	89.97	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	96.224	100.1	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	96.283	99.84	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	96.288	100.1	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	96.272	99.86	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	96.285	99.90	PASS
DC_38A_n78A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	96.287	99.89	PASS

Test Graphs



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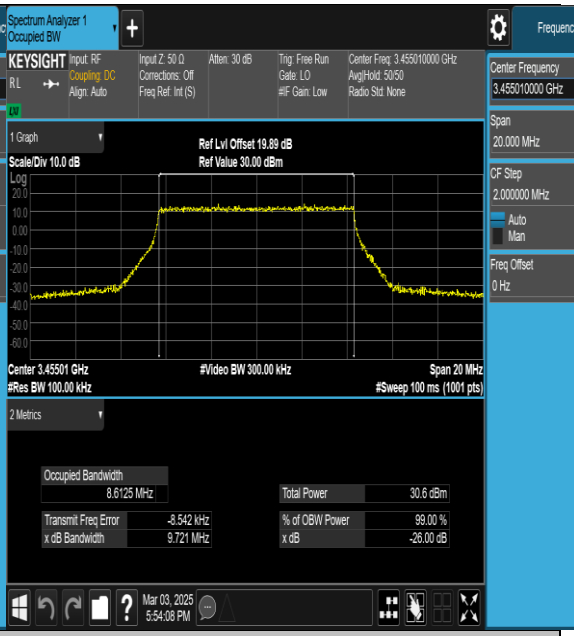

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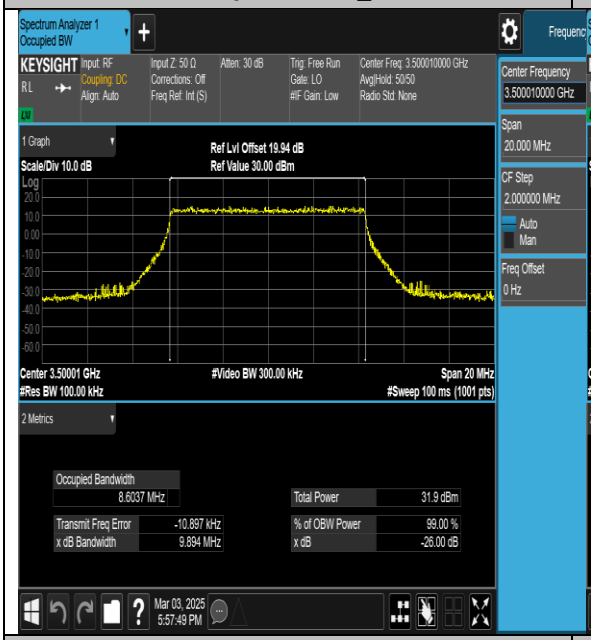

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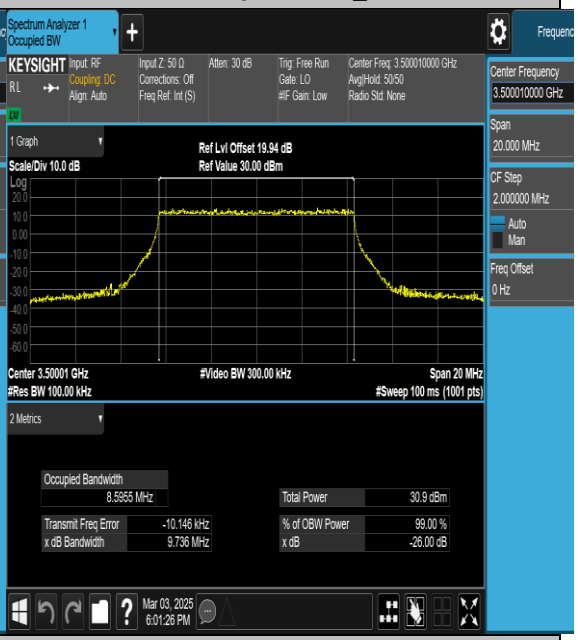

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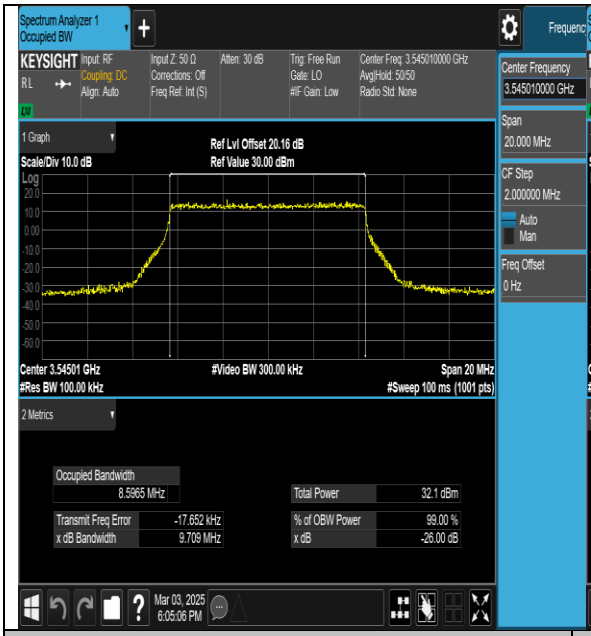

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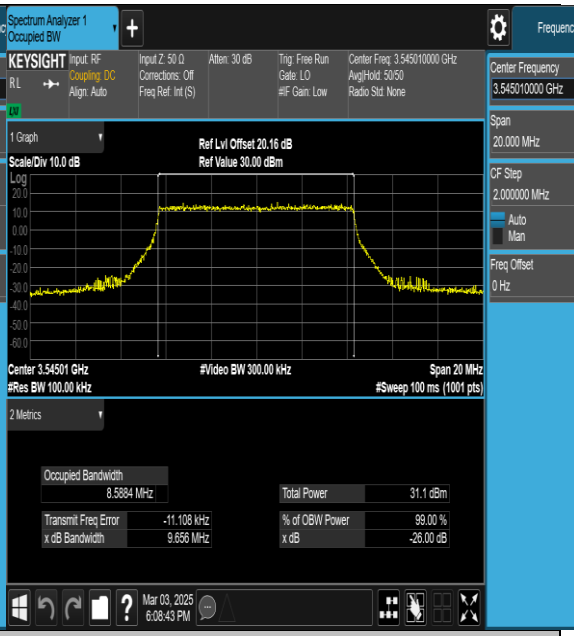

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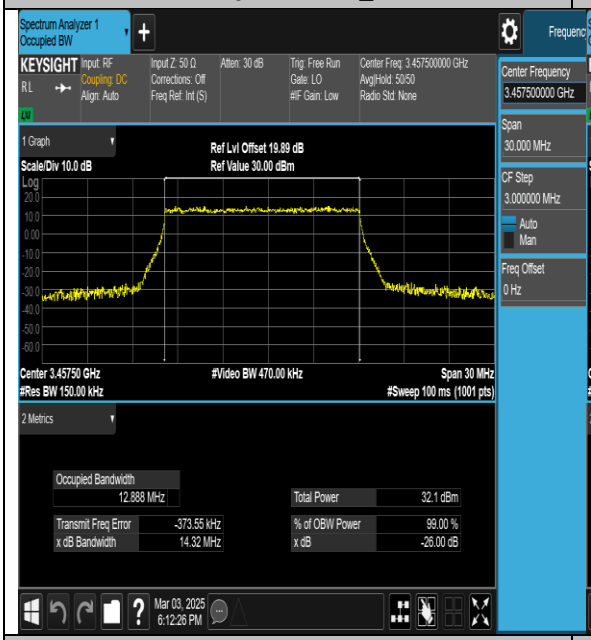

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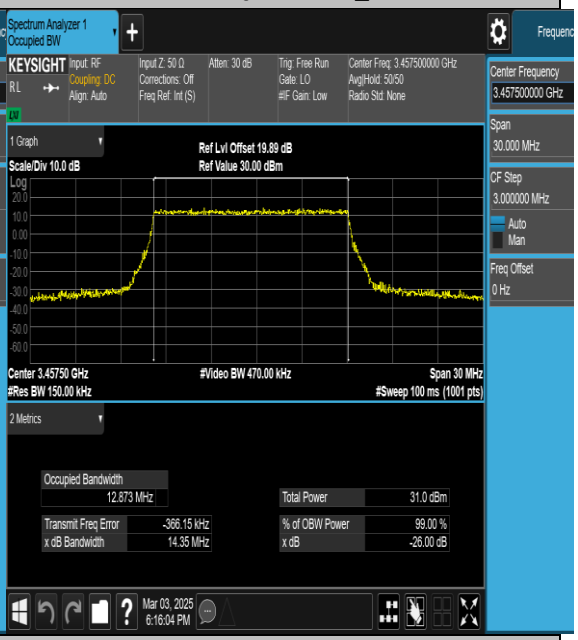

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