

Appendix for 4A_n41A

Product Name: TITAN 5400

Model No: G5G-X62O

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	23.24	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	24.18	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.27	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.13	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	22.05	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Inner_Full	23.07	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.84	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.86	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	23.13	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	24.07	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.66	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.03	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	22.14	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Inner_Full	23.08	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.98	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.18	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	23.00	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	23.93	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.00	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.94	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	21.92	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Inner_Full	22.96	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.80	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.10	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	23.25	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Inner_Full	24.19	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.35	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	23.29	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	22.17	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Inner_Full	23.17	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.36	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	22.38	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	23.14	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Inner_Full	24.07	PC2	PASS

DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.97	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.25	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	22.14	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Inner_Full	23.09	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.99	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.03	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	22.83	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Inner_Full	23.96	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.99	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.15	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	21.96	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Inner_Full	22.96	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	21.97	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.07	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	23.31	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	24.17	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.31	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.19	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	22.12	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Inner_Full	23.22	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.25	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.33	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	23.14	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	24.14	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.17	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	22.05	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Inner_Full	23.04	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	22.27	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.13	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	22.91	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	23.89	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.94	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.00	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	22.12	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Inner_Full	22.92	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.27	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.10	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	23.41	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+L	Inner_Full	24.45	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.13	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.04	PC2	PASS

DC_4A_n41A	30	5+50	DFT-16QAM	M+L	Outer_Full	22.34	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+L	Inner_Full	23.54	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.01	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.09	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	23.08	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+M	Inner_Full	24.09	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	22.99	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.20	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	22.08	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+M	Inner_Full	23.14	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	22.11	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.39	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	23.03	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+H	Inner_Full	24.03	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.19	PC2	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.14	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+H	Outer_Full	22.04	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+H	Inner_Full	23.02	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.02	PC2	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.32	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	23.30	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	24.52	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.94	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.97	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	22.24	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Inner_Full	23.37	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	21.88	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.62	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	23.14	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	24.14	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.96	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.09	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	22.08	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Inner_Full	23.06	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.71	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	21.96	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	22.92	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	24.05	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.98	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.00	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	22.06	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Inner_Full	23.01	PC2	PASS

DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.17	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.02	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	23.10	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+L	Inner_Full	24.12	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.15	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	22.75	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+L	Outer_Full	21.94	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+L	Inner_Full	23.12	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.07	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	21.80	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	22.89	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+M	Inner_Full	23.99	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	22.89	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	22.78	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	21.98	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+M	Inner_Full	22.93	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	21.97	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	21.55	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	22.86	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+H	Inner_Full	23.80	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	22.76	PC2	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.70	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+H	Outer_Full	21.87	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+H	Inner_Full	22.96	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	21.78	PC2	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	21.83	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	23.05	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Inner_Full	24.00	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.19	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.89	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	21.99	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Inner_Full	23.03	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	21.75	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	21.80	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	23.03	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Inner_Full	23.98	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	22.96	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.86	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	21.97	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Inner_Full	22.95	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	21.65	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.84	PC2	PASS

DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	22.76	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Inner_Full	23.78	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.82	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.91	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	21.89	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Inner_Full	22.84	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.93	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	21.67	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	23.21	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Inner_Full	24.09	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.11	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.89	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	22.09	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Inner_Full	23.04	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.10	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	21.87	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	22.96	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Inner_Full	23.99	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.88	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.78	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	22.01	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Inner_Full	22.99	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	21.90	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	21.90	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	22.82	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Inner_Full	23.93	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.80	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.73	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	21.86	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Inner_Full	22.89	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	21.94	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	21.69	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	22.96	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	24.01	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.97	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.78	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	21.92	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Inner_Full	22.98	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	21.96	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.57	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	22.90	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	24.00	PC2	PASS

DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.81	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.65	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	21.87	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Inner_Full	22.94	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	21.65	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.86	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	22.83	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	23.85	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.68	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.70	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	21.87	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Inner_Full	22.90	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	21.54	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.89	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

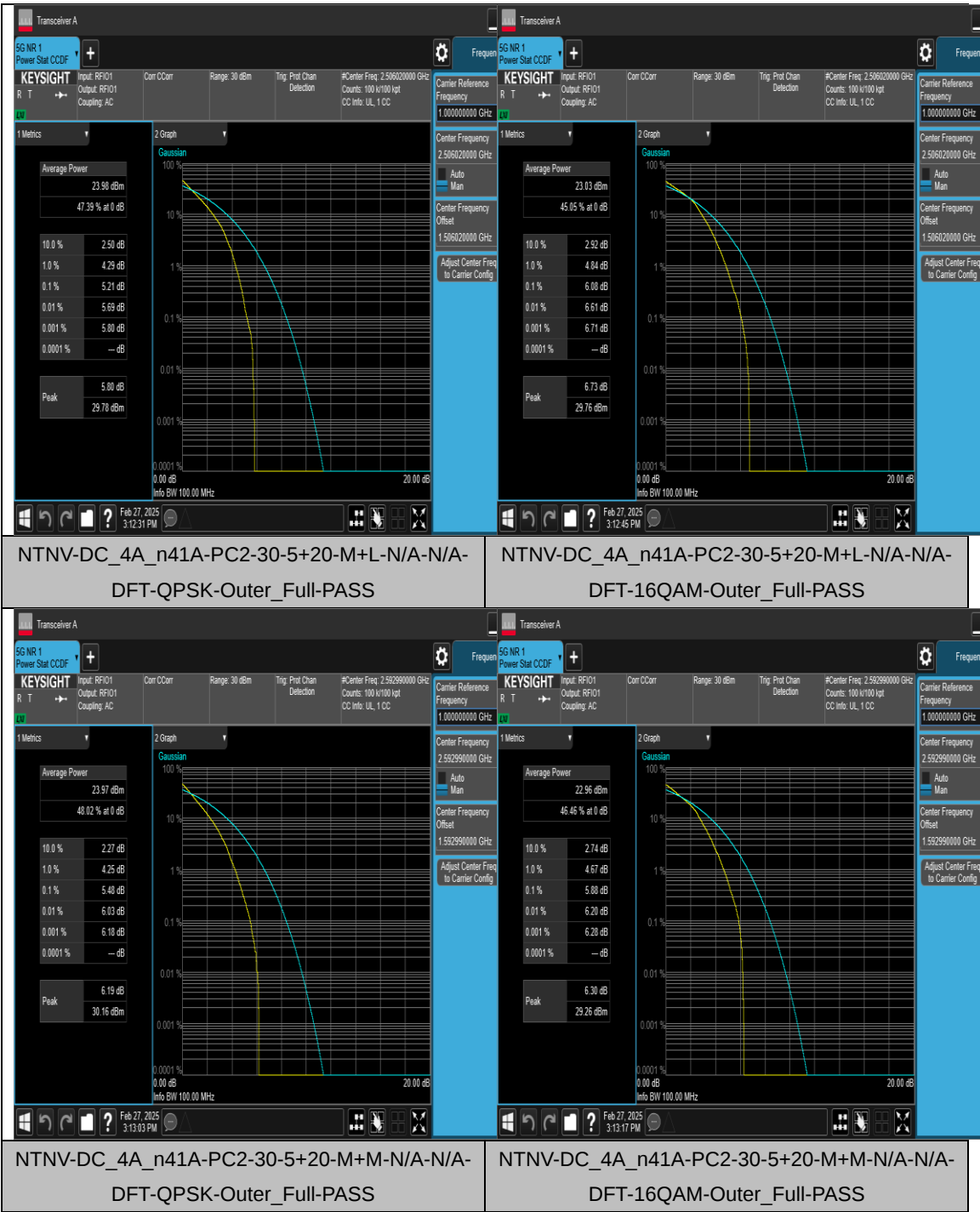
Test Result

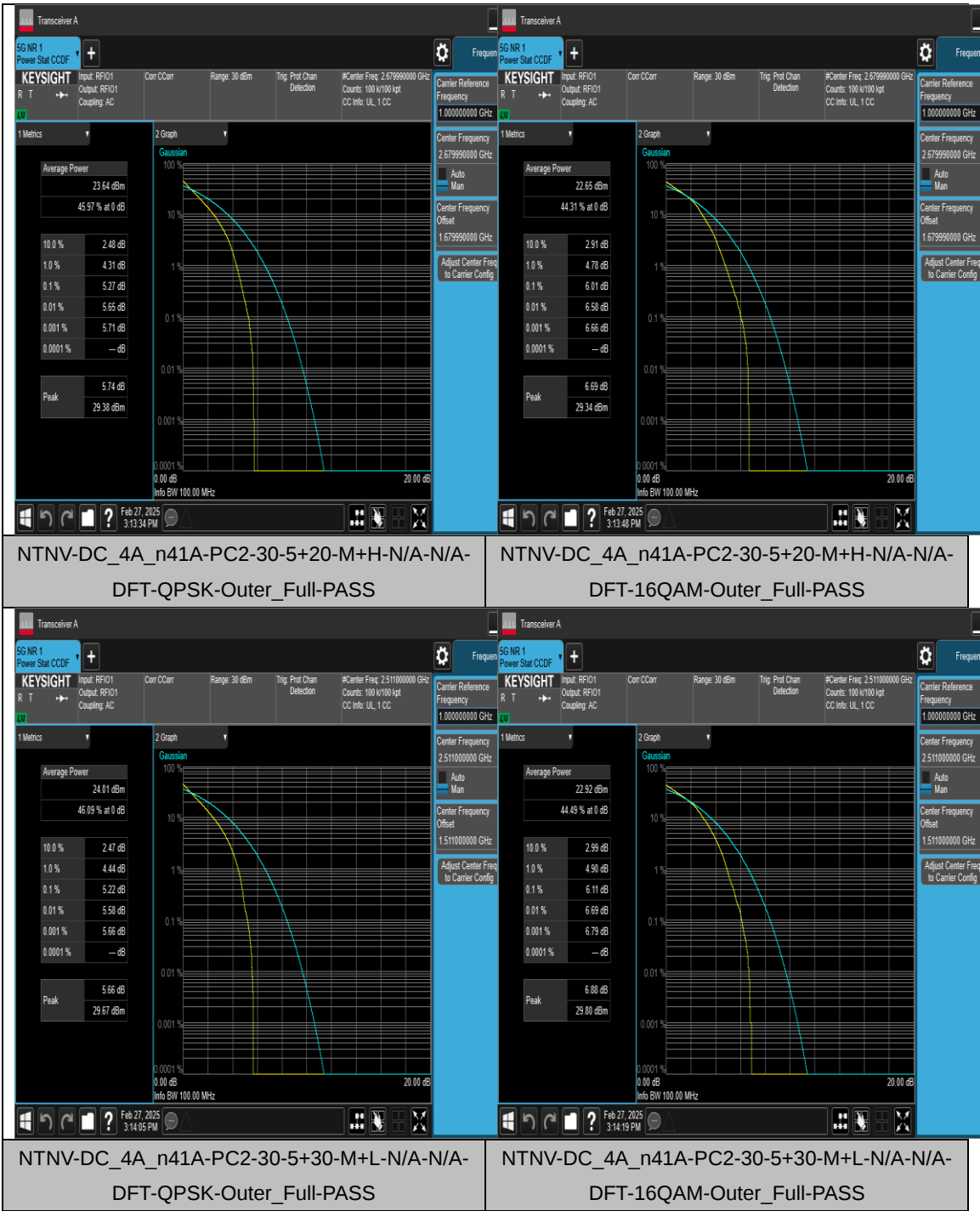
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_4A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	5.21	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	6.08	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	5.48	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	5.88	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	5.27	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-QPSK	M+L	Outer_Full	5.22	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-16QAM	M+L	Outer_Full	6.11	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-QPSK	M+M	Outer_Full	5.36	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-16QAM	M+M	Outer_Full	6.20	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-QPSK	M+H	Outer_Full	5.33	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-16QAM	M+H	Outer_Full	6.09	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-QPSK	M+L	Outer_Full	5.22	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-16QAM	M+L	Outer_Full	5.92	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-QPSK	M+M	Outer_Full	5.37	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-16QAM	M+M	Outer_Full	5.90	≤13	PASS
DC_4A_n4	30	5+40	DFT-QPSK	M+H	Outer_Full	5.32	≤13	PASS

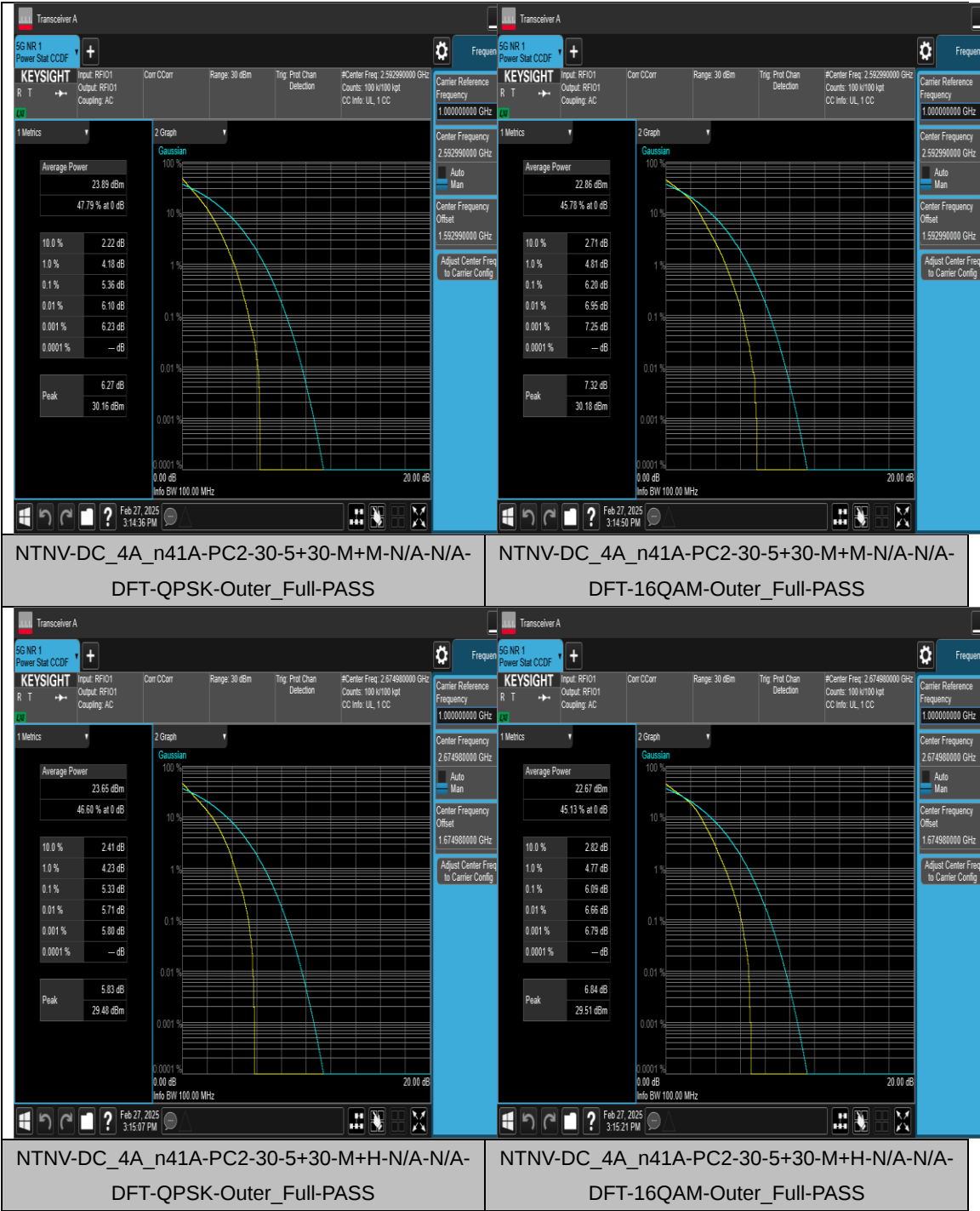
1A								
DC_4A_n4 1A	30	5+40	DFT-16QAM	M+H	Outer_Full	6.05	≤13	PASS
DC_4A_n4 1A	30	5+50	DFT-QPSK	M+L	Outer_Full	5.15	≤13	PASS
DC_4A_n4 1A	30	5+50	DFT-16QAM	M+L	Outer_Full	5.92	≤13	PASS
DC_4A_n4 1A	30	5+50	DFT-QPSK	M+M	Outer_Full	5.10	≤13	PASS
DC_4A_n4 1A	30	5+50	DFT-16QAM	M+M	Outer_Full	5.96	≤13	PASS
DC_4A_n4 1A	30	5+50	DFT-QPSK	M+H	Outer_Full	5.48	≤13	PASS
DC_4A_n4 1A	30	5+50	DFT-16QAM	M+H	Outer_Full	6.17	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-QPSK	M+L	Outer_Full	5.10	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-16QAM	M+L	Outer_Full	5.93	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-QPSK	M+M	Outer_Full	5.28	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-16QAM	M+M	Outer_Full	6.06	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-QPSK	M+H	Outer_Full	5.09	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-16QAM	M+H	Outer_Full	5.90	≤13	PASS
DC_4A_n4 1A	30	5+70	DFT-QPSK	M+L	Outer_Full	5.40	≤13	PASS
DC_4A_n4 1A	30	5+70	DFT-16QAM	M+L	Outer_Full	6.15	≤13	PASS
DC_4A_n4 1A	30	5+70	DFT-QPSK	M+M	Outer_Full	5.49	≤13	PASS
DC_4A_n4 1A	30	5+70	DFT-16QAM	M+M	Outer_Full	6.13	≤13	PASS
DC_4A_n4 1A	30	5+70	DFT-QPSK	M+H	Outer_Full	5.36	≤13	PASS
DC_4A_n4 1A	30	5+70	DFT-16QAM	M+H	Outer_Full	6.09	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-QPSK	M+L	Outer_Full	5.34	≤13	PASS
DC_4A_n4	30	5+80	DFT-16QAM	M+L	Outer_Full	6.03	≤13	PASS

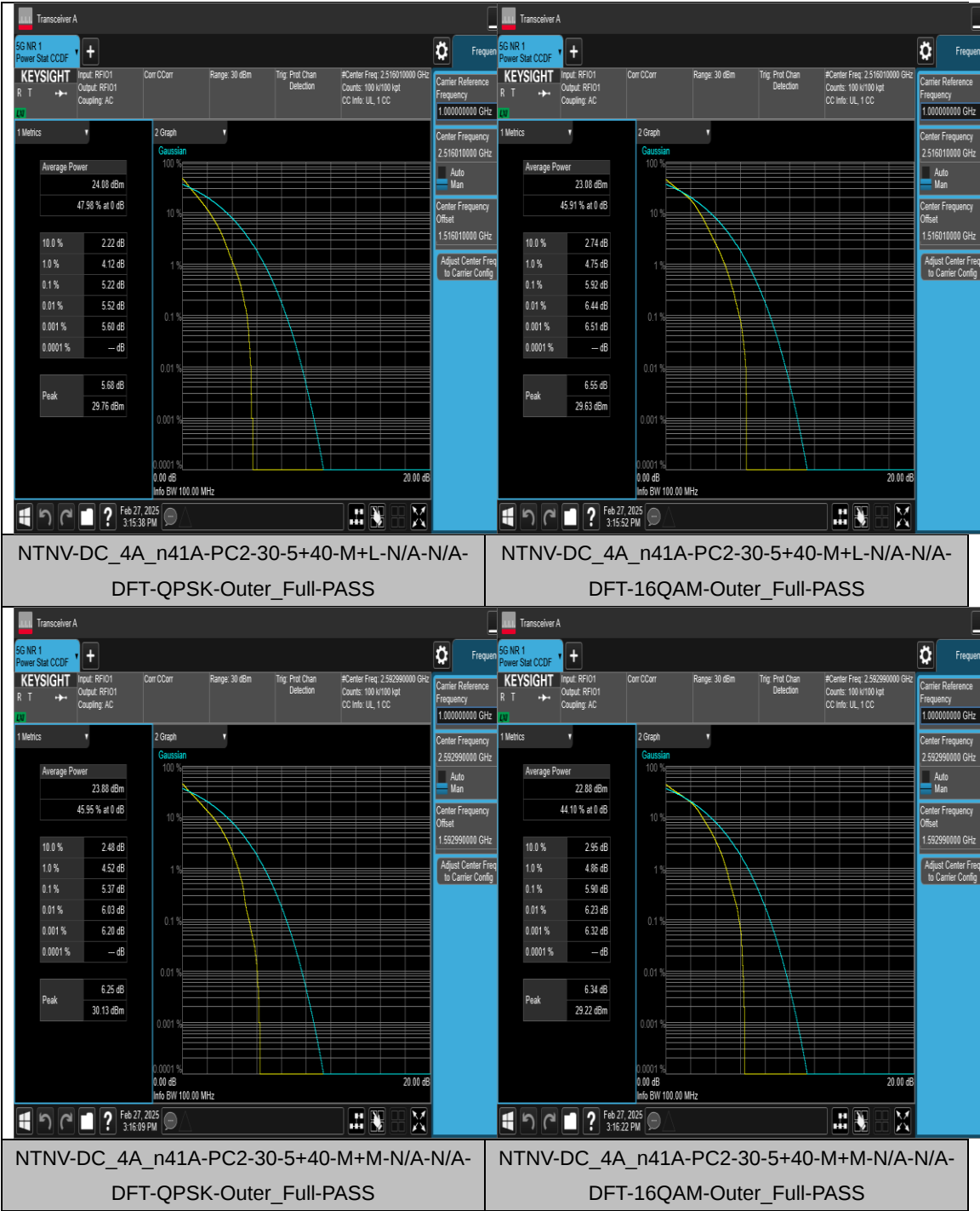
1A								
DC_4A_n4 1A	30	5+80	DFT-QPSK	M+M	Outer_Full	5.36	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-16QAM	M+M	Outer_Full	6.14	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-QPSK	M+H	Outer_Full	5.19	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-16QAM	M+H	Outer_Full	5.98	≤13	PASS
DC_4A_n4 1A	30	5+90	DFT-QPSK	M+L	Outer_Full	5.34	≤13	PASS
DC_4A_n4 1A	30	5+90	DFT-16QAM	M+L	Outer_Full	6.00	≤13	PASS
DC_4A_n4 1A	30	5+90	DFT-QPSK	M+M	Outer_Full	5.49	≤13	PASS
DC_4A_n4 1A	30	5+90	DFT-16QAM	M+M	Outer_Full	6.18	≤13	PASS
DC_4A_n4 1A	30	5+90	DFT-QPSK	M+H	Outer_Full	5.35	≤13	PASS
DC_4A_n4 1A	30	5+90	DFT-16QAM	M+H	Outer_Full	6.02	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-QPSK	M+L	Outer_Full	5.46	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-16QAM	M+L	Outer_Full	6.19	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-QPSK	M+M	Outer_Full	5.49	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-16QAM	M+M	Outer_Full	6.17	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-QPSK	M+H	Outer_Full	5.23	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-16QAM	M+H	Outer_Full	6.03	≤13	PASS

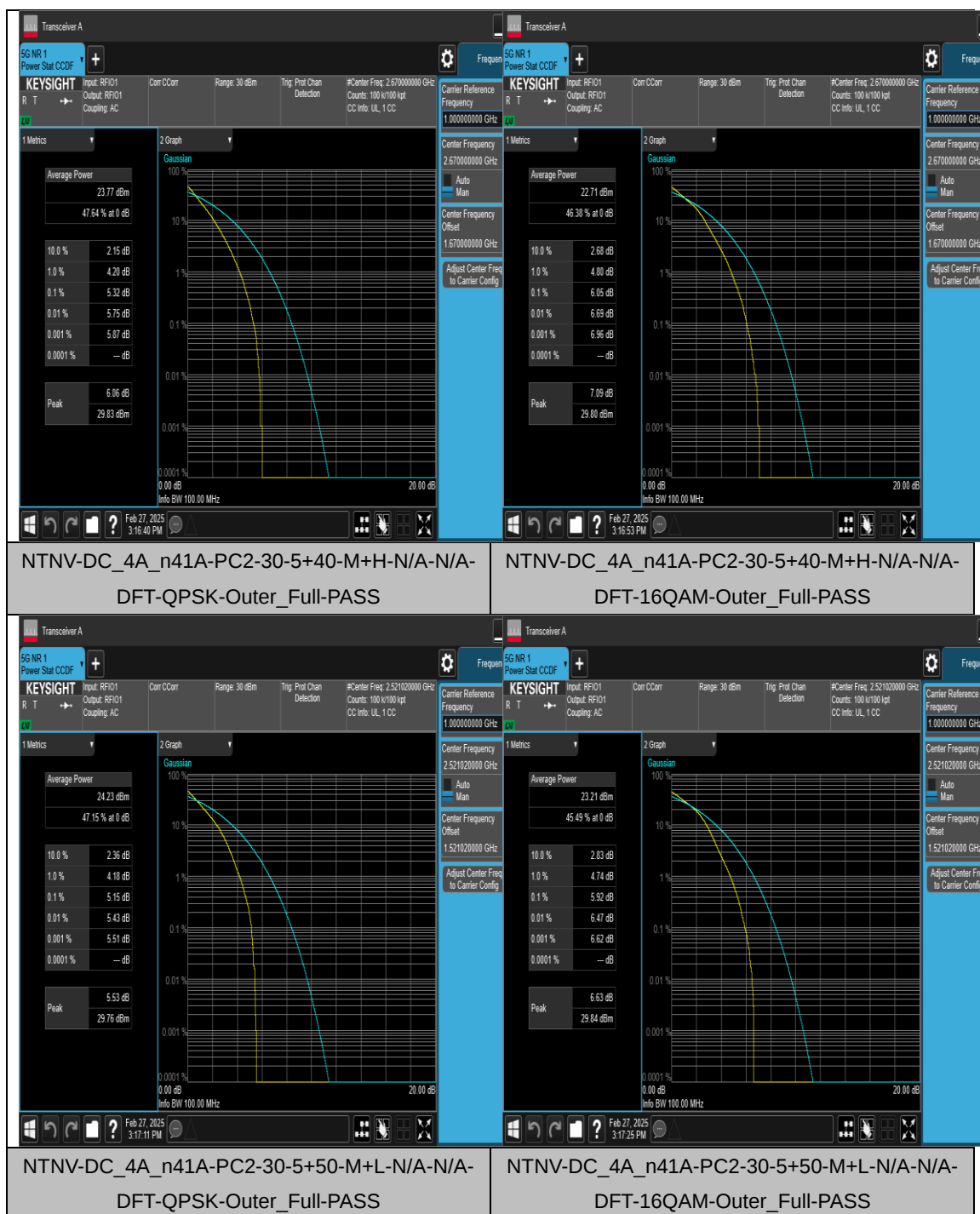
Test Graphs

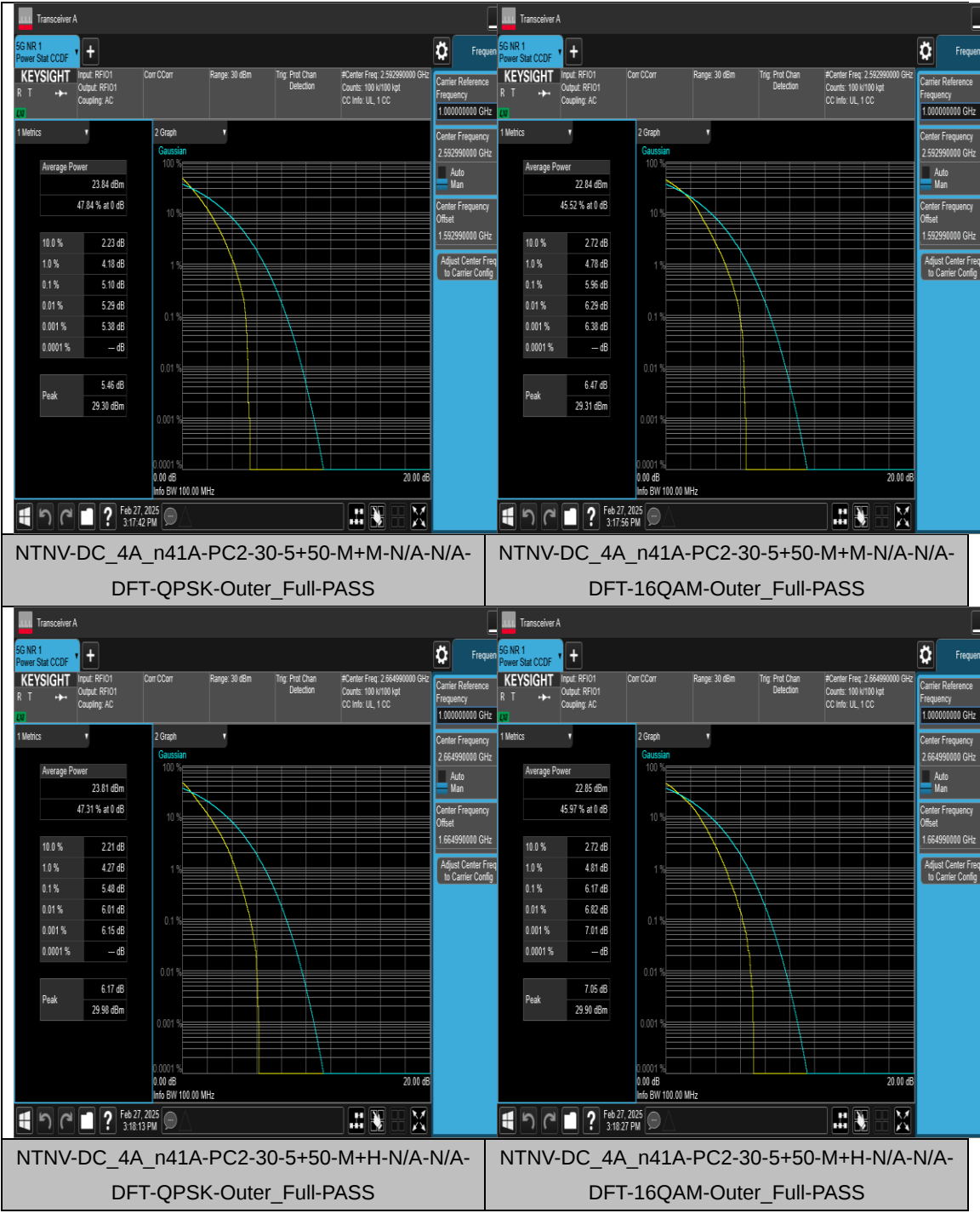


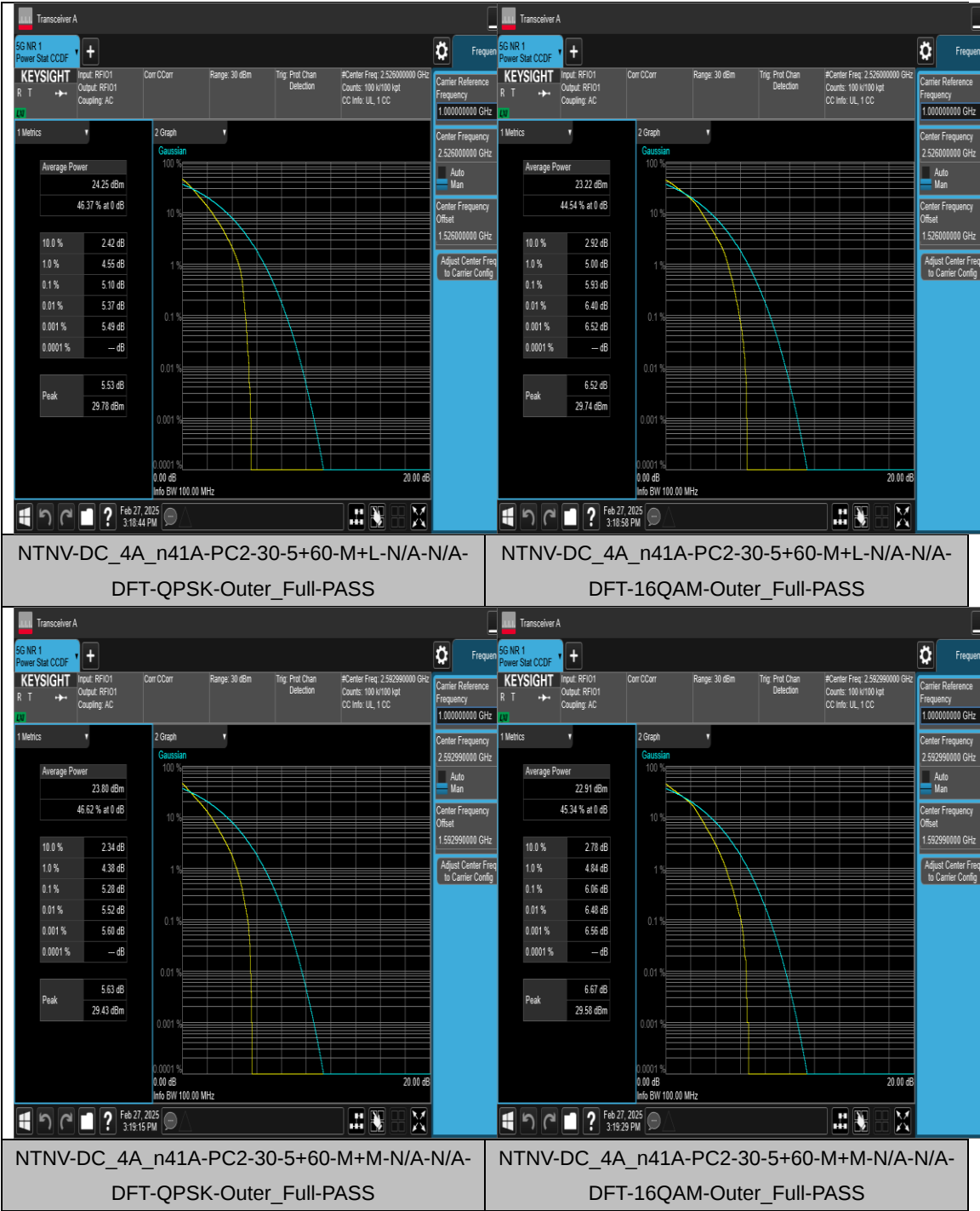


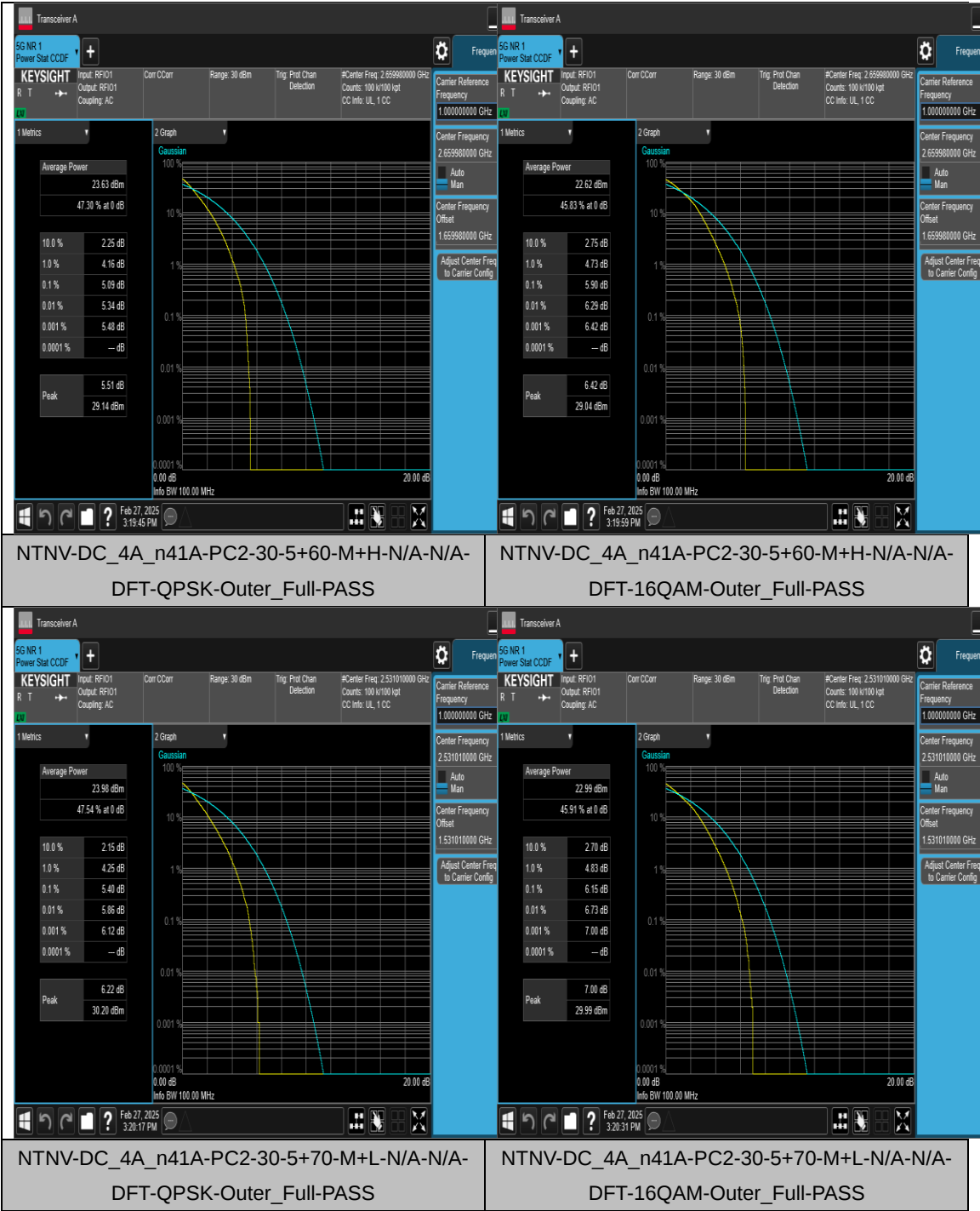


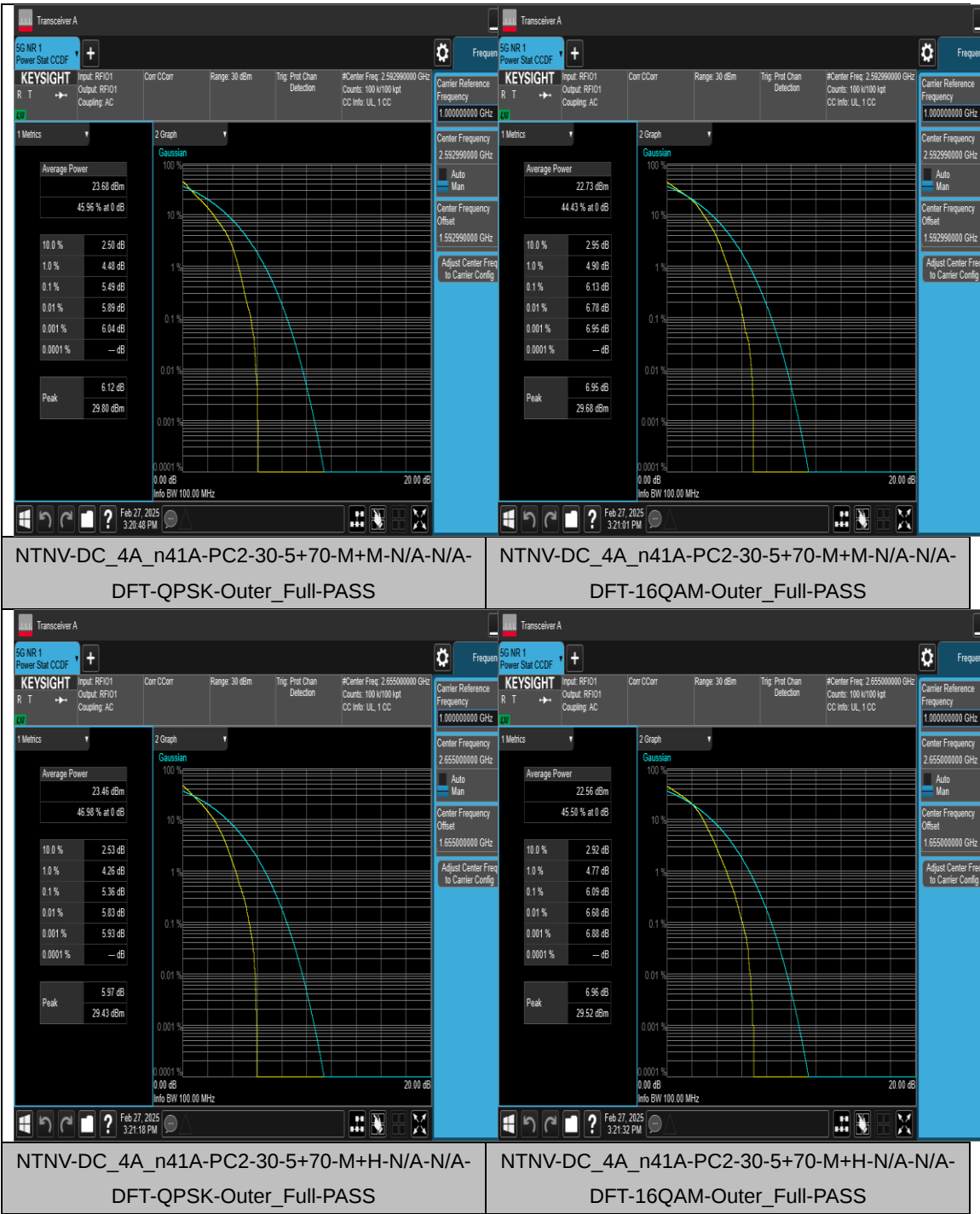


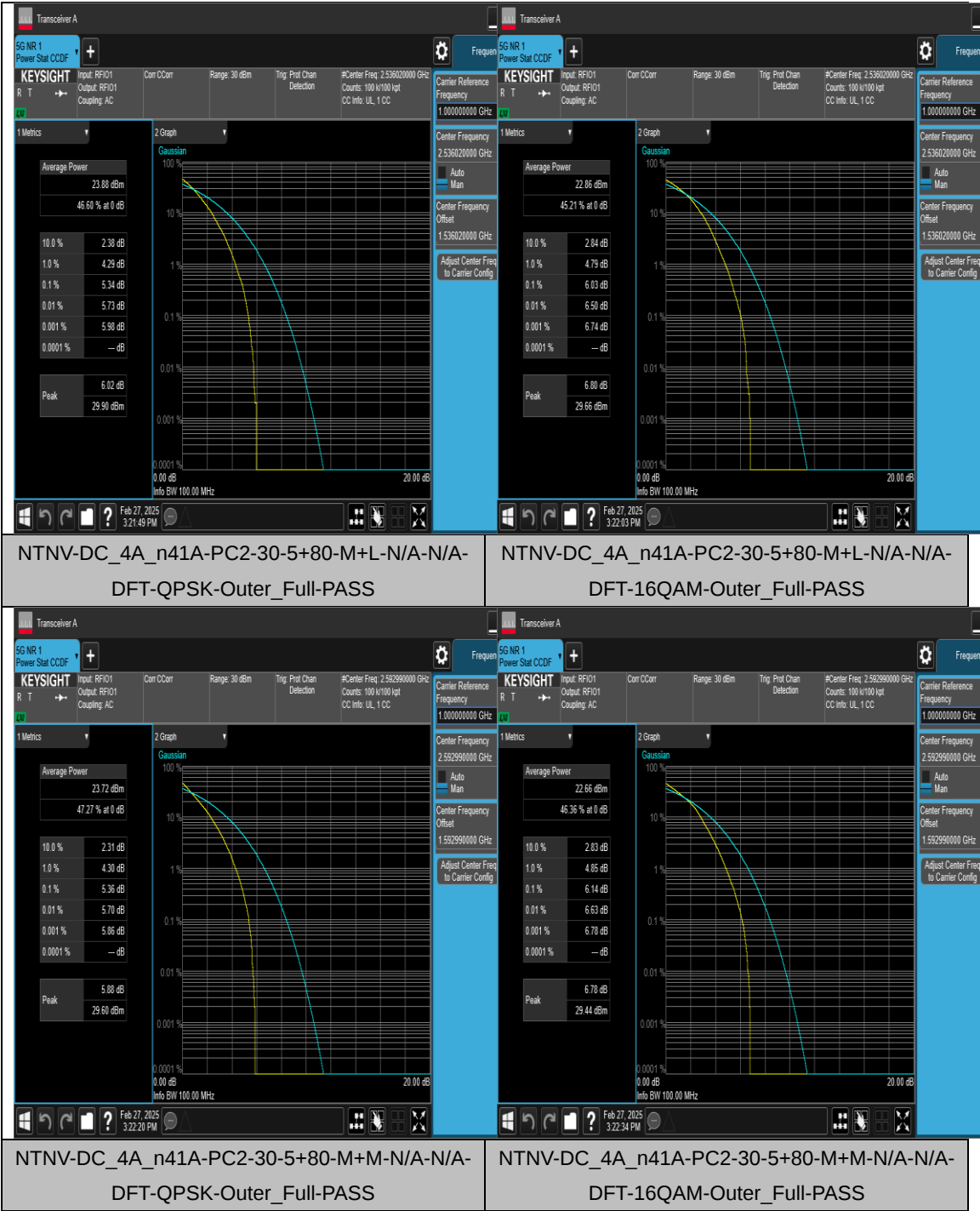


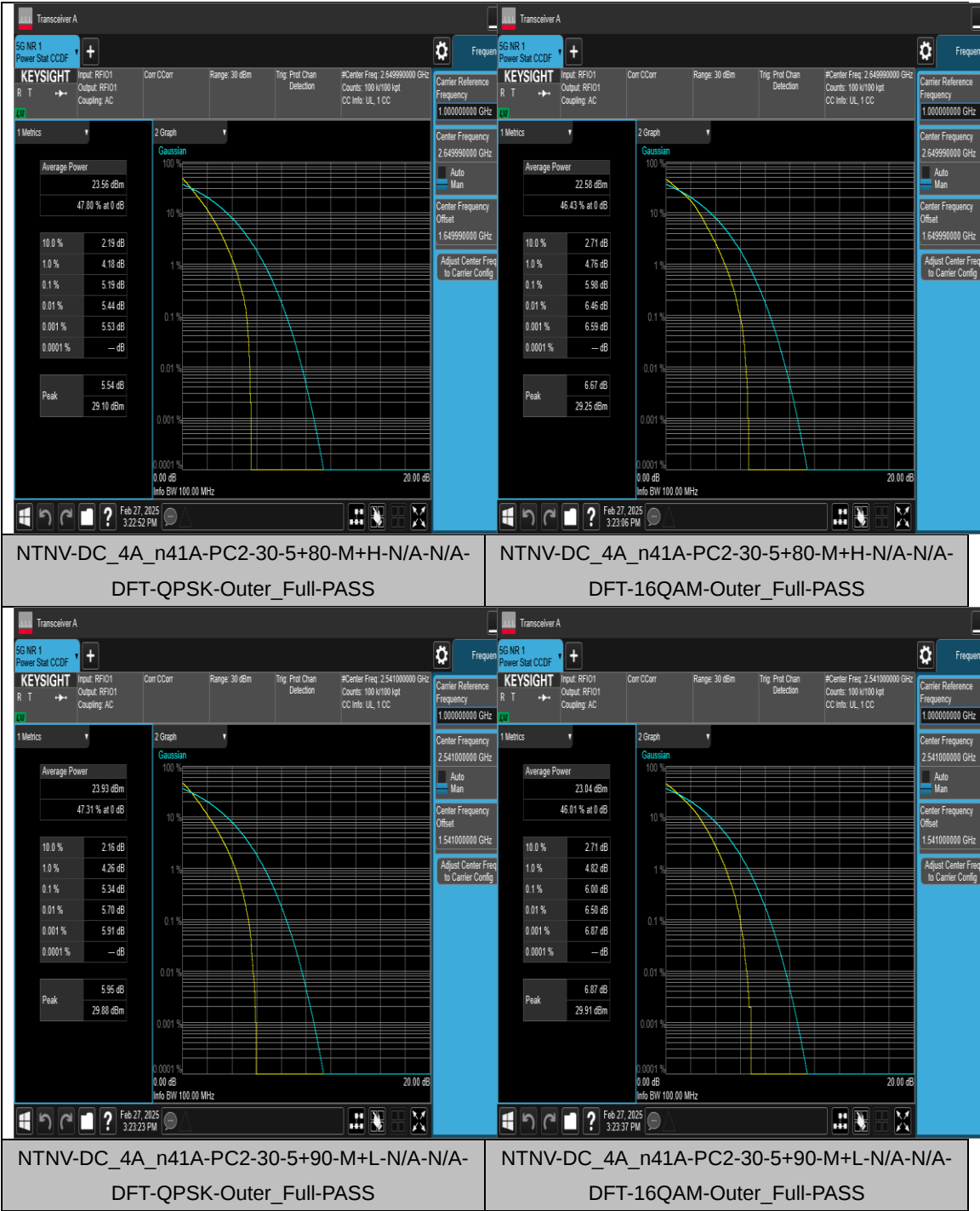


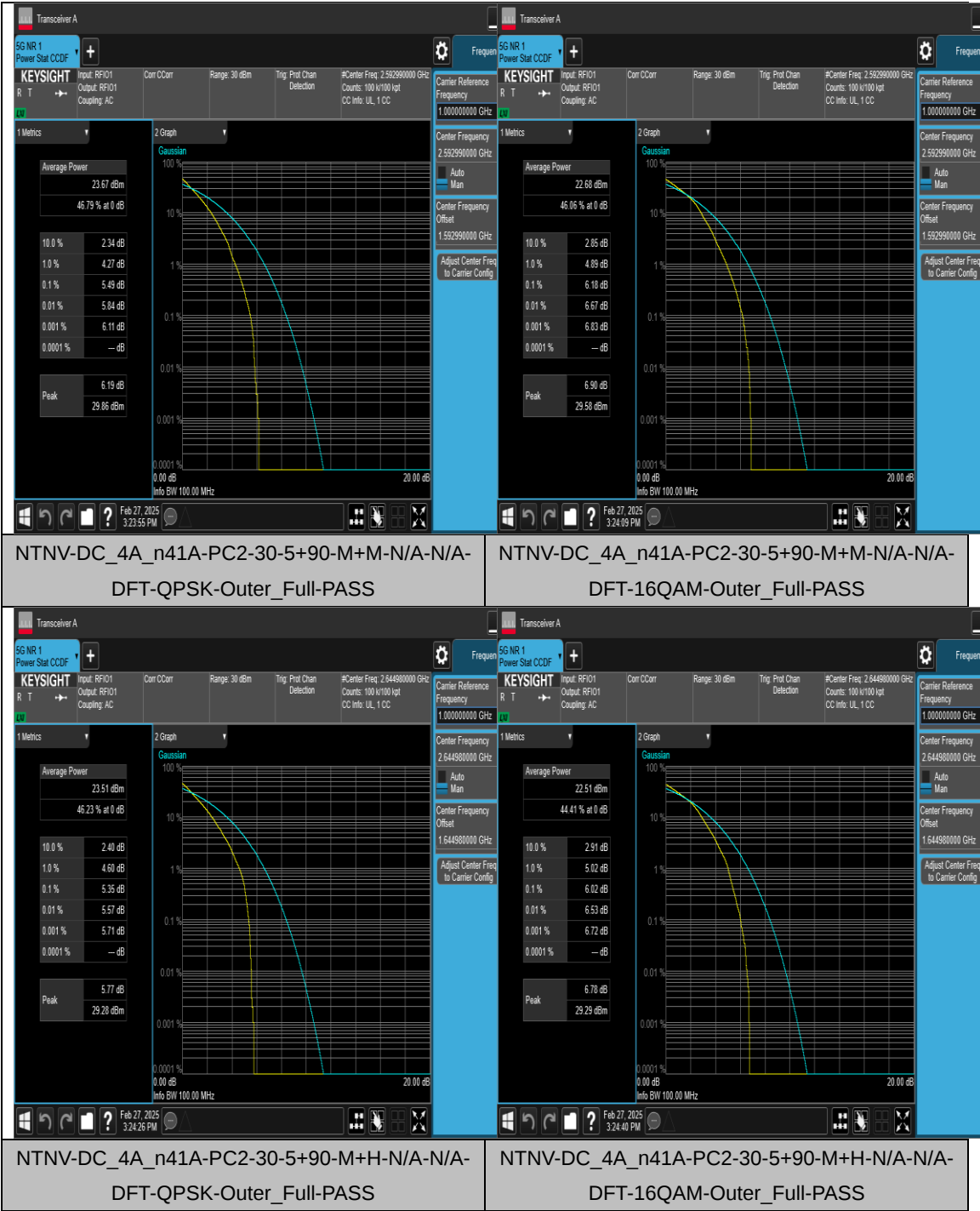


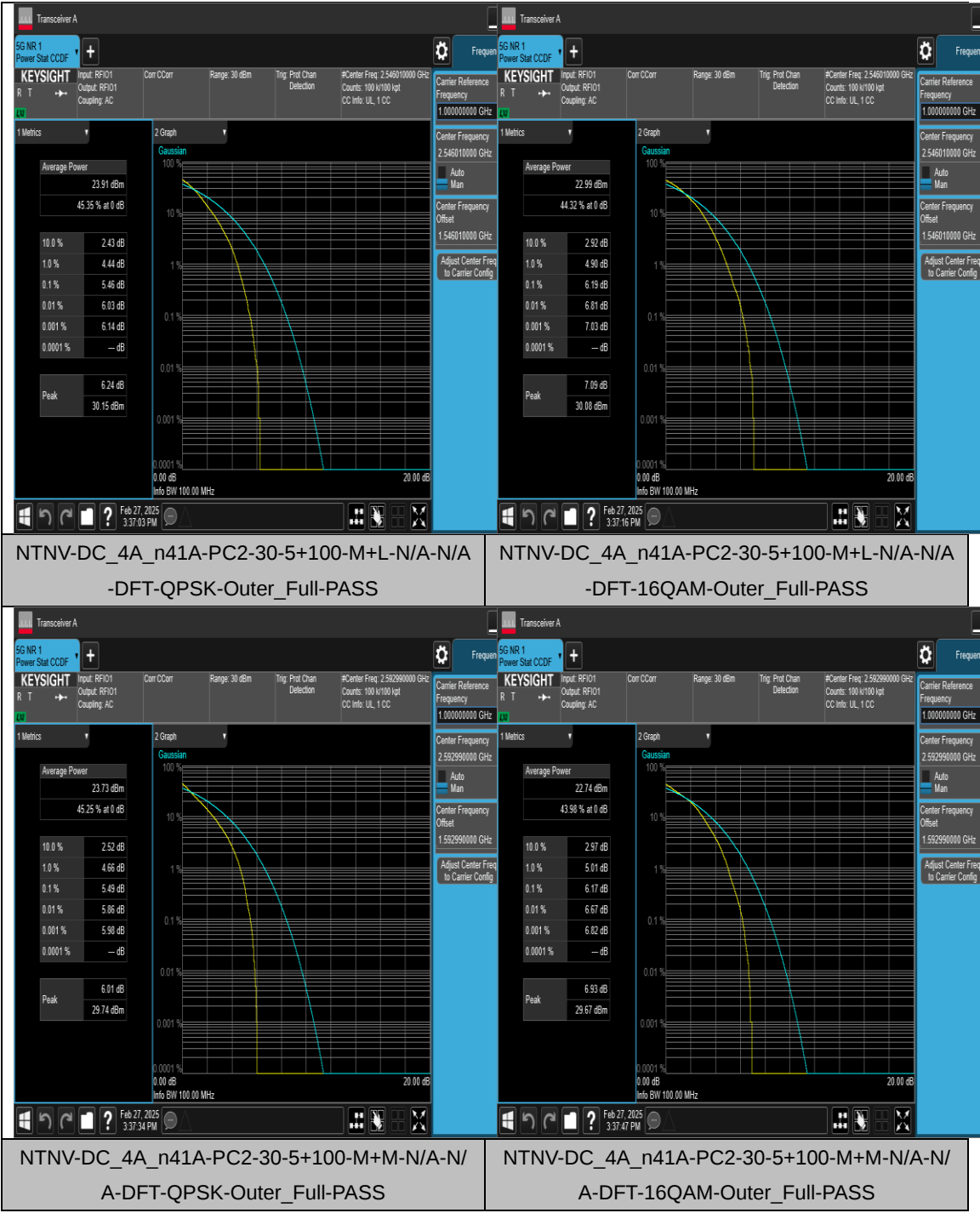


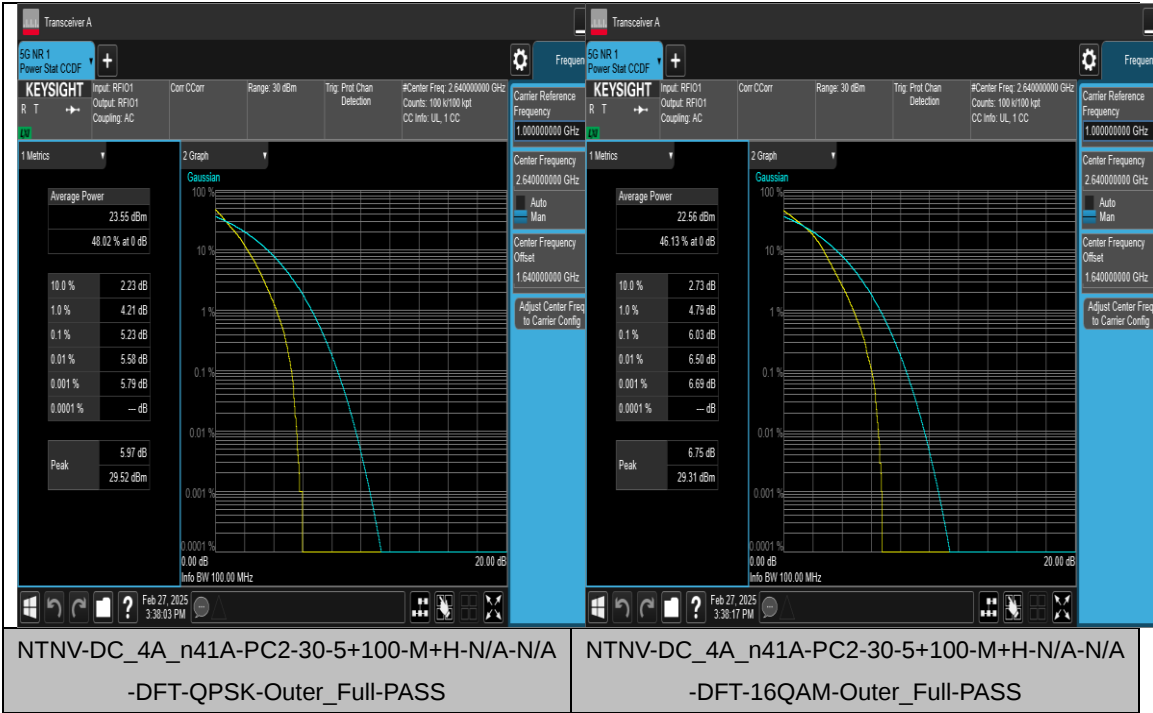












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	17.854	19.38	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	17.845	19.22	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	17.852	19.43	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	17.887	19.40	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	17.886	19.57	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	17.895	19.36	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	26.751	28.47	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	26.696	28.36	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	26.776	28.60	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	26.786	28.54	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	26.807	28.72	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	26.801	28.67	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	35.605	37.60	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	35.603	37.62	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	35.728	37.59	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	35.664	37.71	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	35.712	37.80	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	35.629	37.54	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	45.505	47.74	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+L	Outer_Full	45.439	47.84	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	45.681	47.94	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	45.618	47.93	PASS
DC_4A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	45.669	47.94	PASS
DC_4A_n41A	30	5+50	DFT-16QAM	M+H	Outer_Full	45.625	47.98	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	57.462	60.32	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	57.461	60.17	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	57.524	60.42	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	57.656	60.19	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	57.843	60.30	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	57.931	60.24	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	63.749	66.72	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+L	Outer_Full	63.830	66.84	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	63.871	66.80	PASS

DC_4A_n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	63.935	66.90	PASS
DC_4A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	64.375	66.92	PASS
DC_4A_n41A	30	5+70	DFT-16QAM	M+H	Outer_Full	64.365	67.03	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	76.348	79.90	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	76.530	79.90	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	76.498	79.69	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	76.542	80.05	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	77.401	80.37	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	77.365	80.28	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	85.976	89.75	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	86.082	89.85	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	85.985	89.69	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	86.082	89.63	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	87.073	90.20	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	87.122	90.28	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	95.567	99.59	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	95.467	99.52	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	95.719	99.85	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	95.648	99.59	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	96.865	100.1	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	96.745	99.92	PASS

Test Graphs

