

Appendix for 26A_n25A

Product Name: 5G Window CPE

Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Outer_Full	21.04	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Inner_Full	21.96	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	21.12	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	21.10	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+L	Outer_Full	19.97	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+L	Inner_Full	20.88	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	19.38	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	19.62	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+M	Outer_Full	20.65	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+M	Inner_Full	21.76	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	20.74	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	20.86	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+M	Outer_Full	19.64	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+M	Inner_Full	20.66	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	20.00	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	20.00	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Outer_Full	19.03	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Inner_Full	20.01	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	18.84	PC3	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	18.71	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+H	Outer_Full	18.04	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+H	Inner_Full	19.07	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	17.96	PC3	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	18.47	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Outer_Full	20.97	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Inner_Full	22.03	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.03	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	20.89	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+L	Outer_Full	19.97	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+L	Inner_Full	20.97	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.31	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.20	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+M	Outer_Full	20.64	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+M	Inner_Full	21.60	PC3	PASS

DC_26A_n25A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.78	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.67	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+M	Outer_Full	19.66	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+M	Inner_Full	20.66	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.92	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	18.98	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Outer_Full	19.22	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Inner_Full	20.09	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.20	PC3	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	19.09	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+H	Outer_Full	18.07	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+H	Inner_Full	19.30	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18.88	PC3	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	17.89	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Outer_Full	21.08	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Inner_Full	22.09	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.12	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.17	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+L	Outer_Full	20.05	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+L	Inner_Full	21.00	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.11	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.41	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+M	Outer_Full	20.92	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+M	Inner_Full	21.88	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.90	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.79	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+M	Outer_Full	19.89	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+M	Inner_Full	20.80	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	19.95	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	19.74	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Outer_Full	19.51	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Inner_Full	20.43	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.96	PC3	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	19.14	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+H	Outer_Full	18.50	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+H	Inner_Full	19.47	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	18.76	PC3	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	18.45	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Outer_Full	21.15	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Inner_Full	22.11	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.06	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	20.86	PC3	PASS

DC_26A_n25A	15	5+20	DFT-16QAM	M+L	Outer_Full	20.09	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+L	Inner_Full	21.08	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.35	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.12	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+M	Outer_Full	20.89	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+M	Inner_Full	21.97	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.90	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.59	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+M	Outer_Full	19.88	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+M	Inner_Full	20.84	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.82	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.77	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Outer_Full	19.74	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Inner_Full	20.66	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	20.18	PC3	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.21	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+H	Outer_Full	18.68	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+H	Inner_Full	19.67	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.25	PC3	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	18.25	PC3	PASS

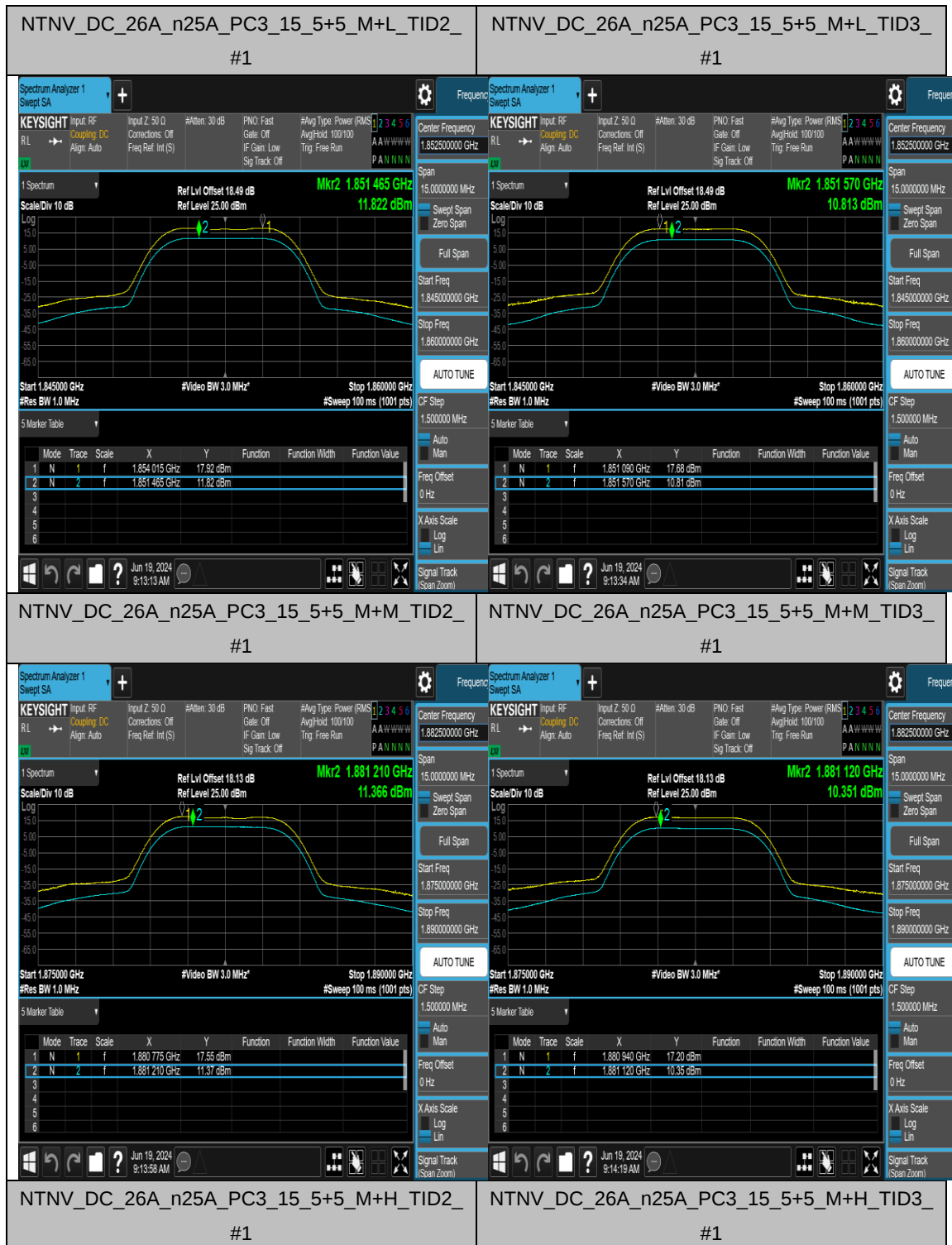
Appendix B: Peak-to-Average Ratio for NSA

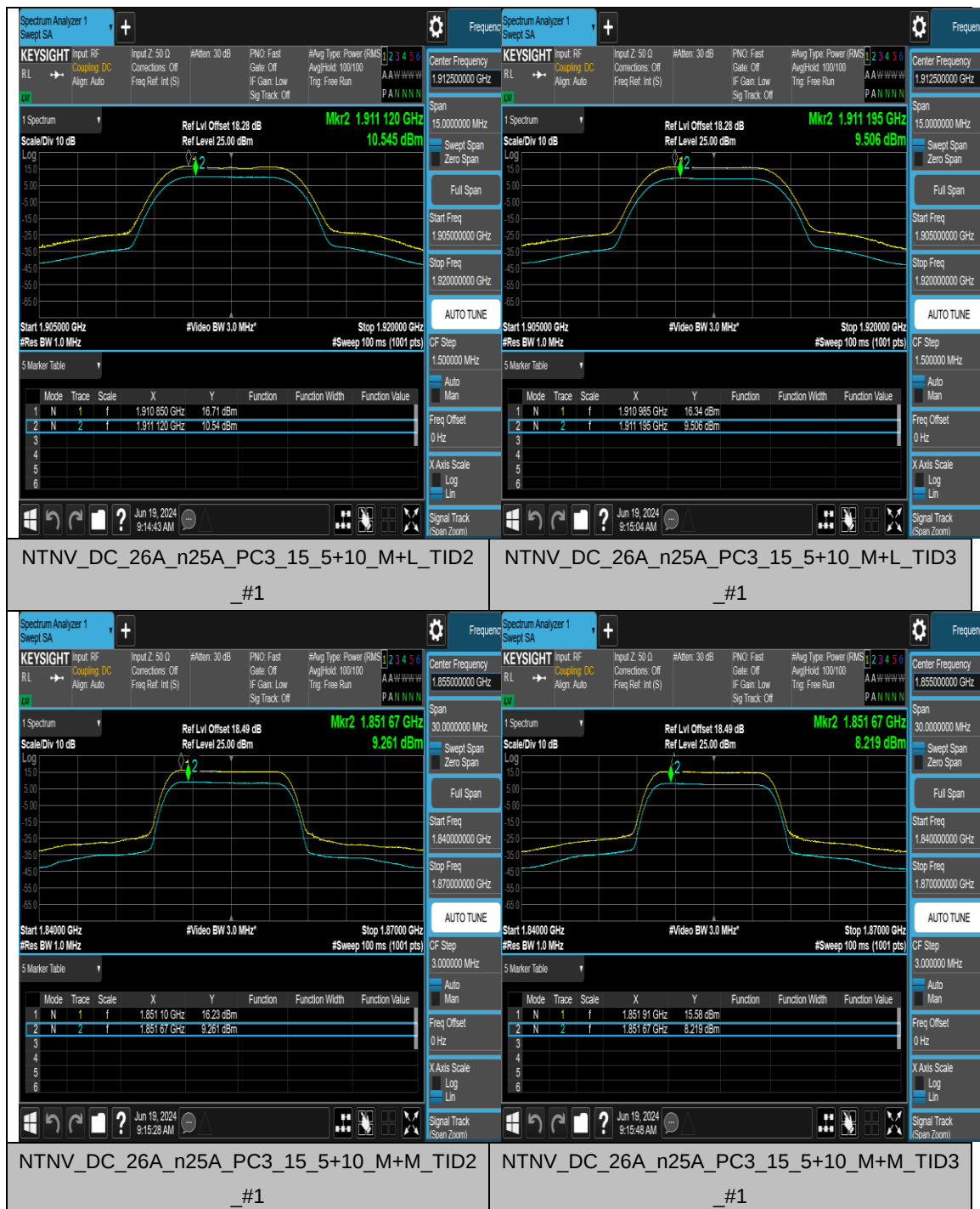
Peak-to-Average Ratio(PAPR)

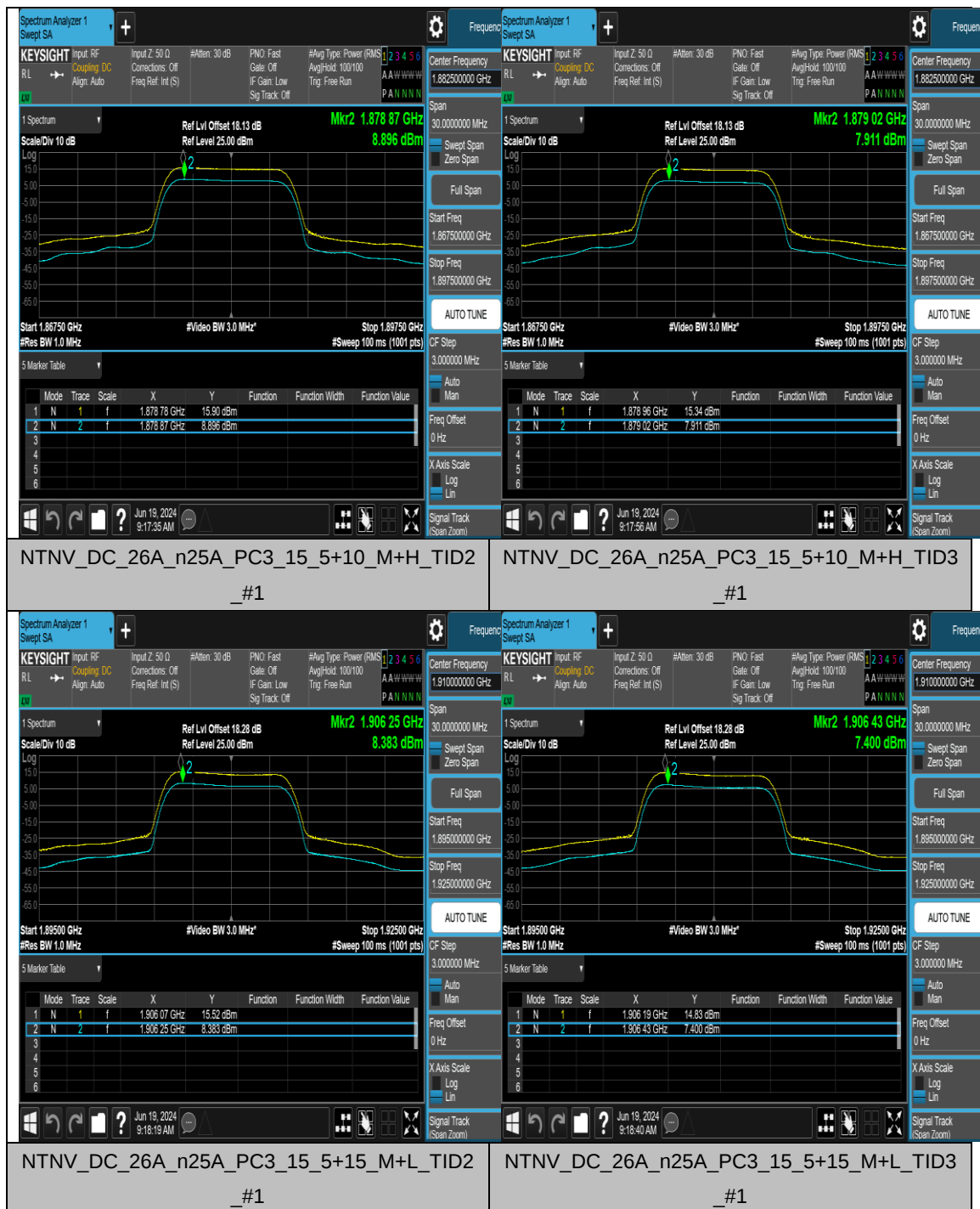
Test Result

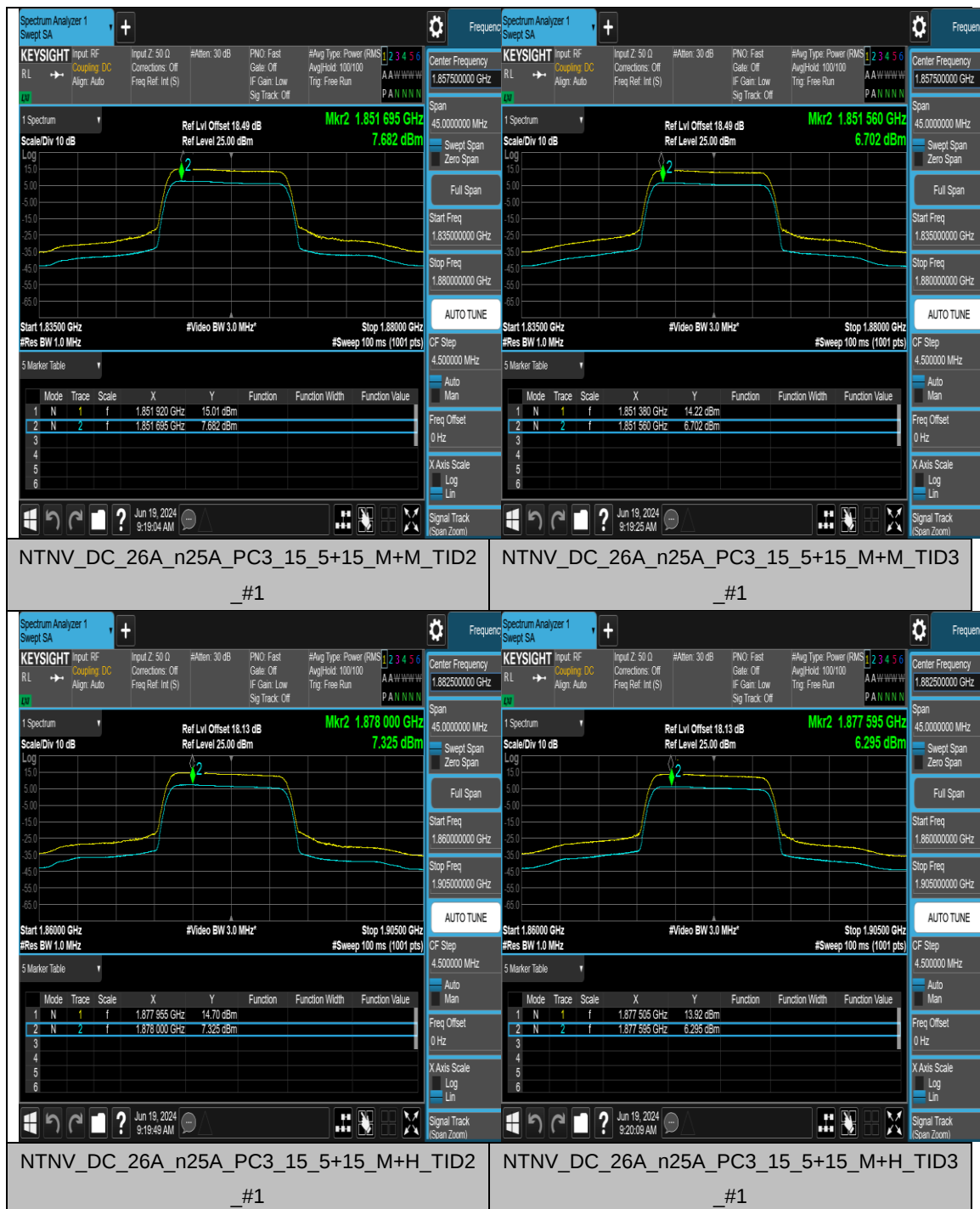
Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Outer_Full	100%	0.00	6.11	≤13	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+L	Outer_Full	100%	0.00	6.87	≤13	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+M	Outer_Full	100%	0.00	6.18	≤13	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+M	Outer_Full	100%	0.00	6.85	≤13	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Outer_Full	100%	0.00	6.16	≤13	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+H	Outer_Full	100%	0.00	6.83	≤13	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	6.97	≤13	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.36	≤13	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.00	≤13	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.43	≤13	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.14	≤13	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.43	≤13	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.33	≤13	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.52	≤13	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.37	≤13	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.63	≤13	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.37	≤13	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.62	≤13	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.49	≤13	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.68	≤13	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.48	≤13	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.66	≤13	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.48	≤13	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.77	≤13	PASS

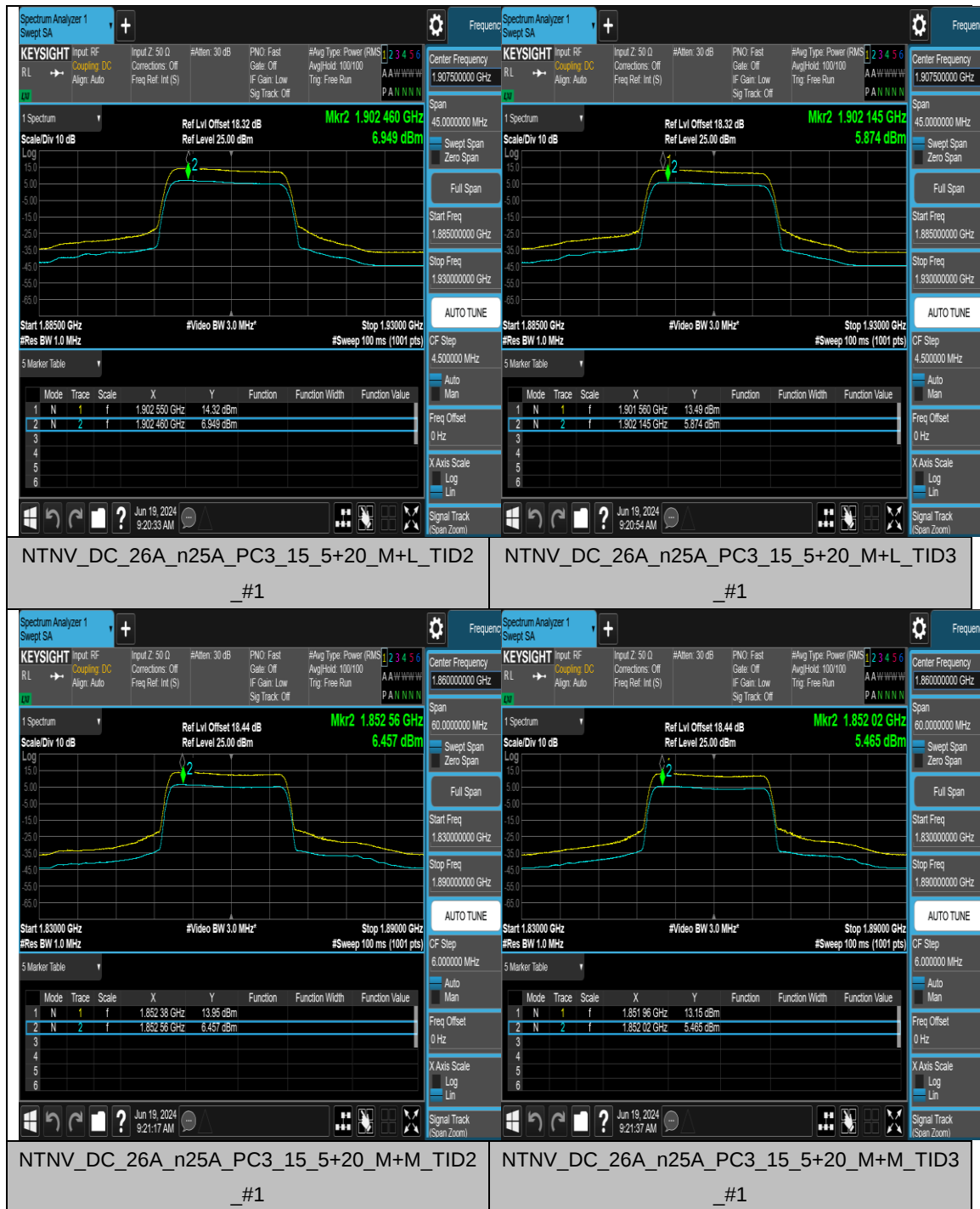
Test Graphs

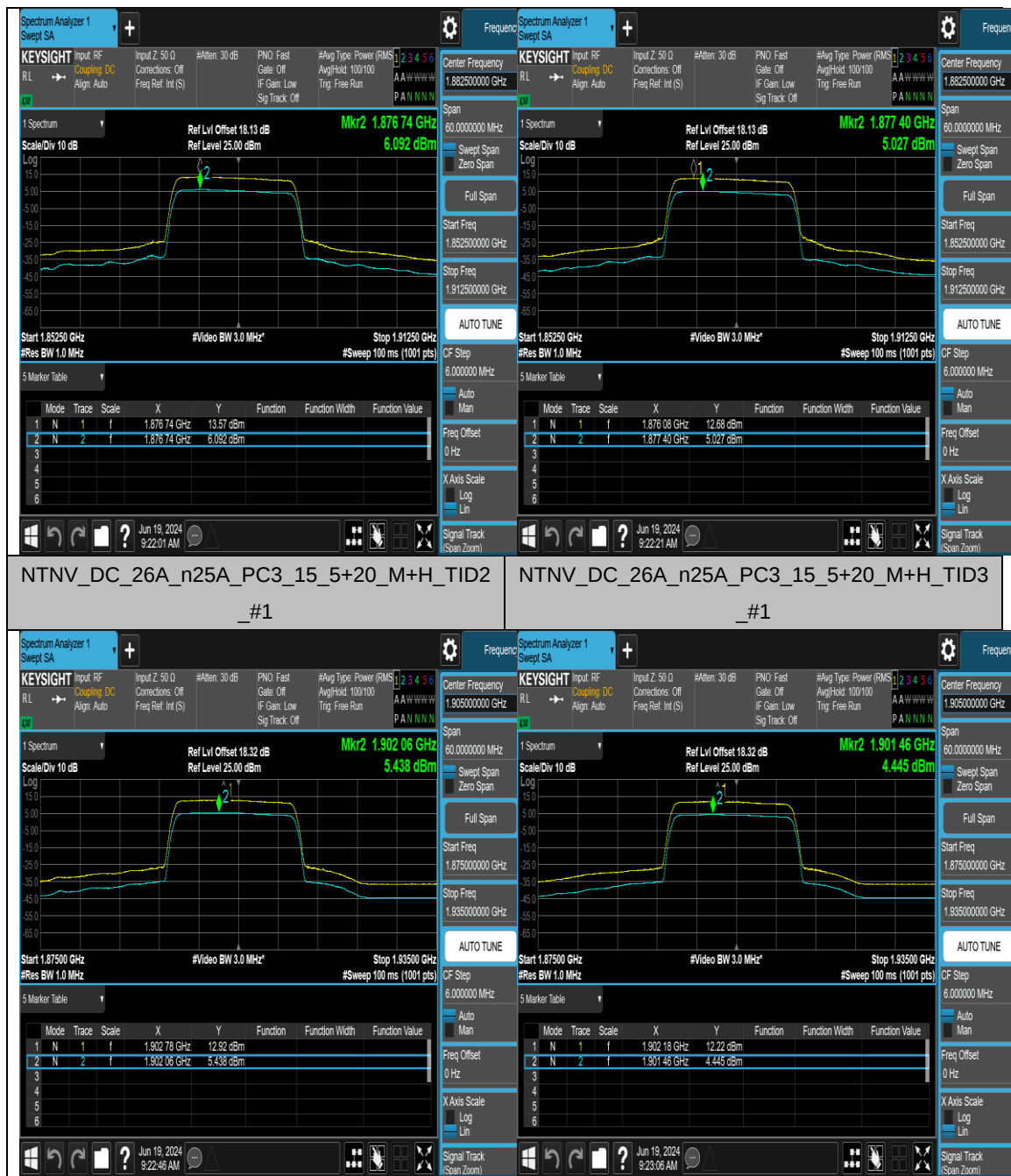












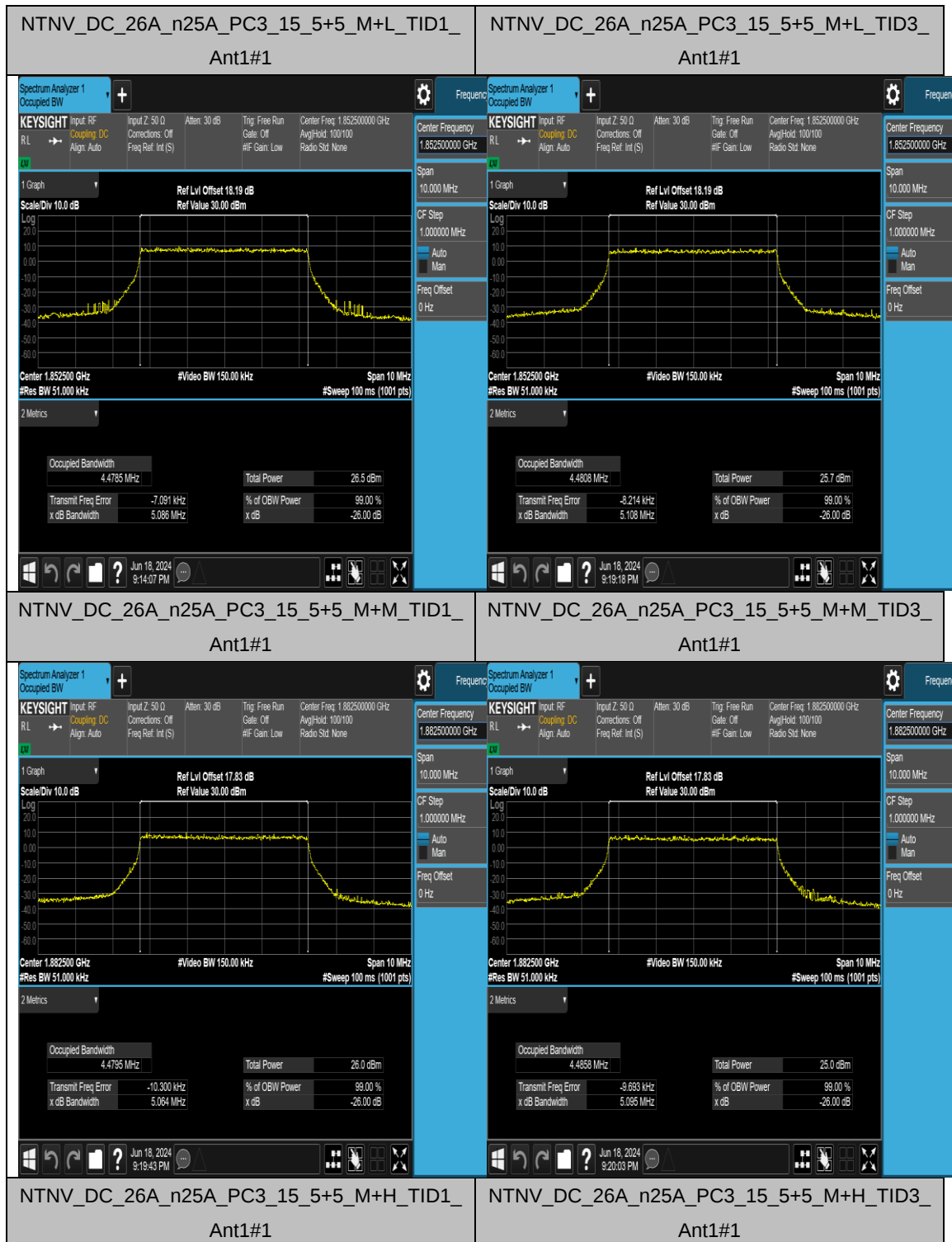
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

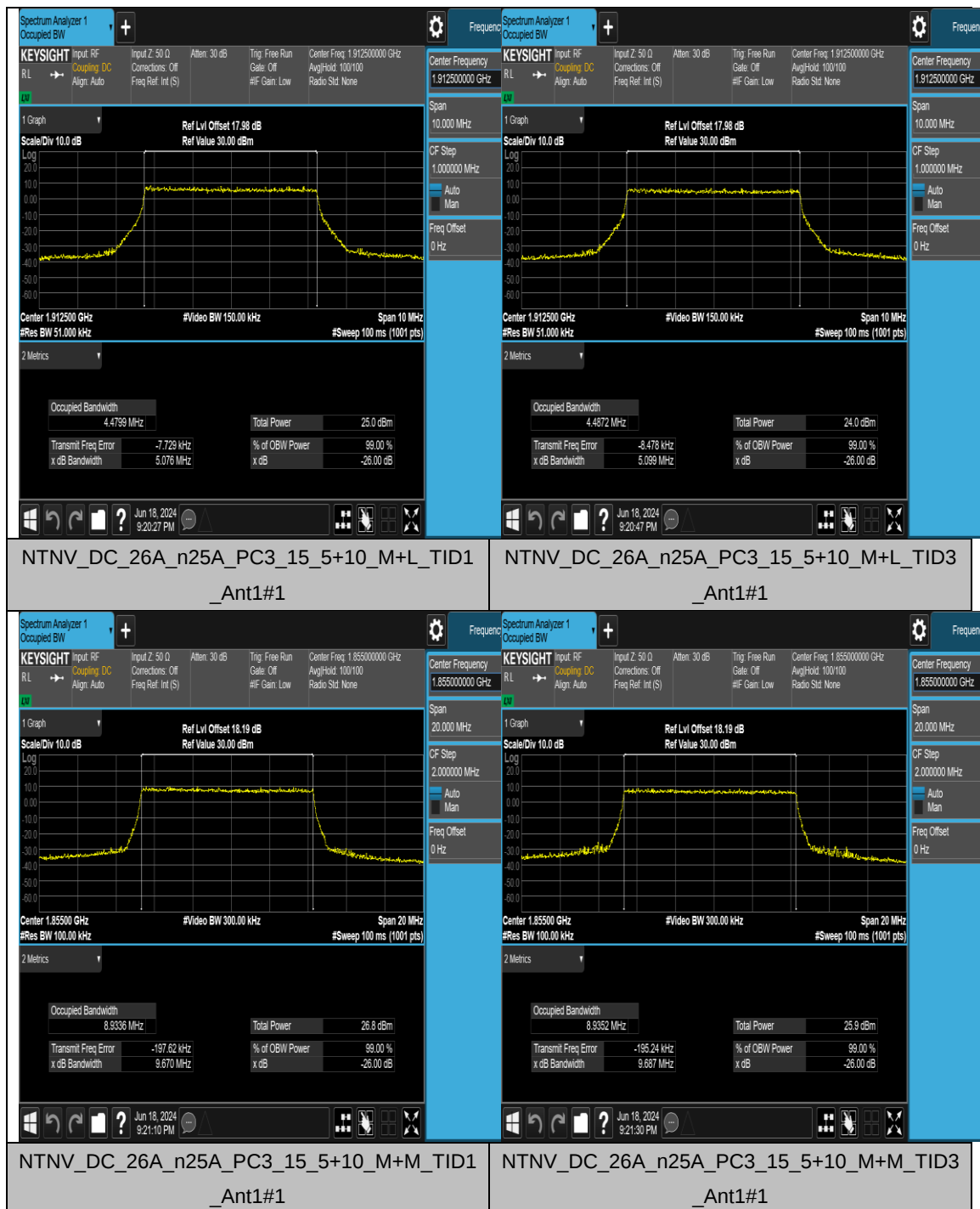
NSA

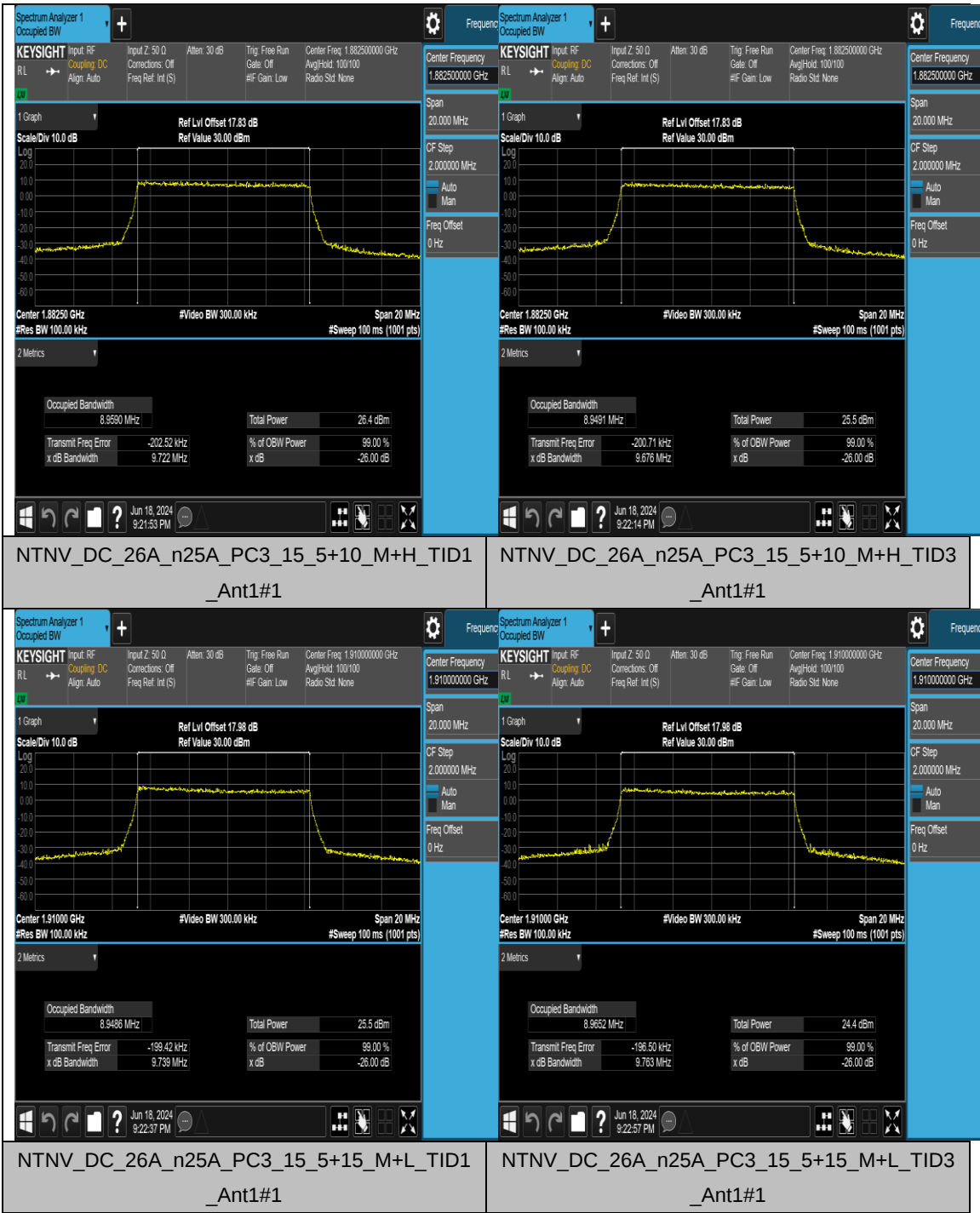
Test Result

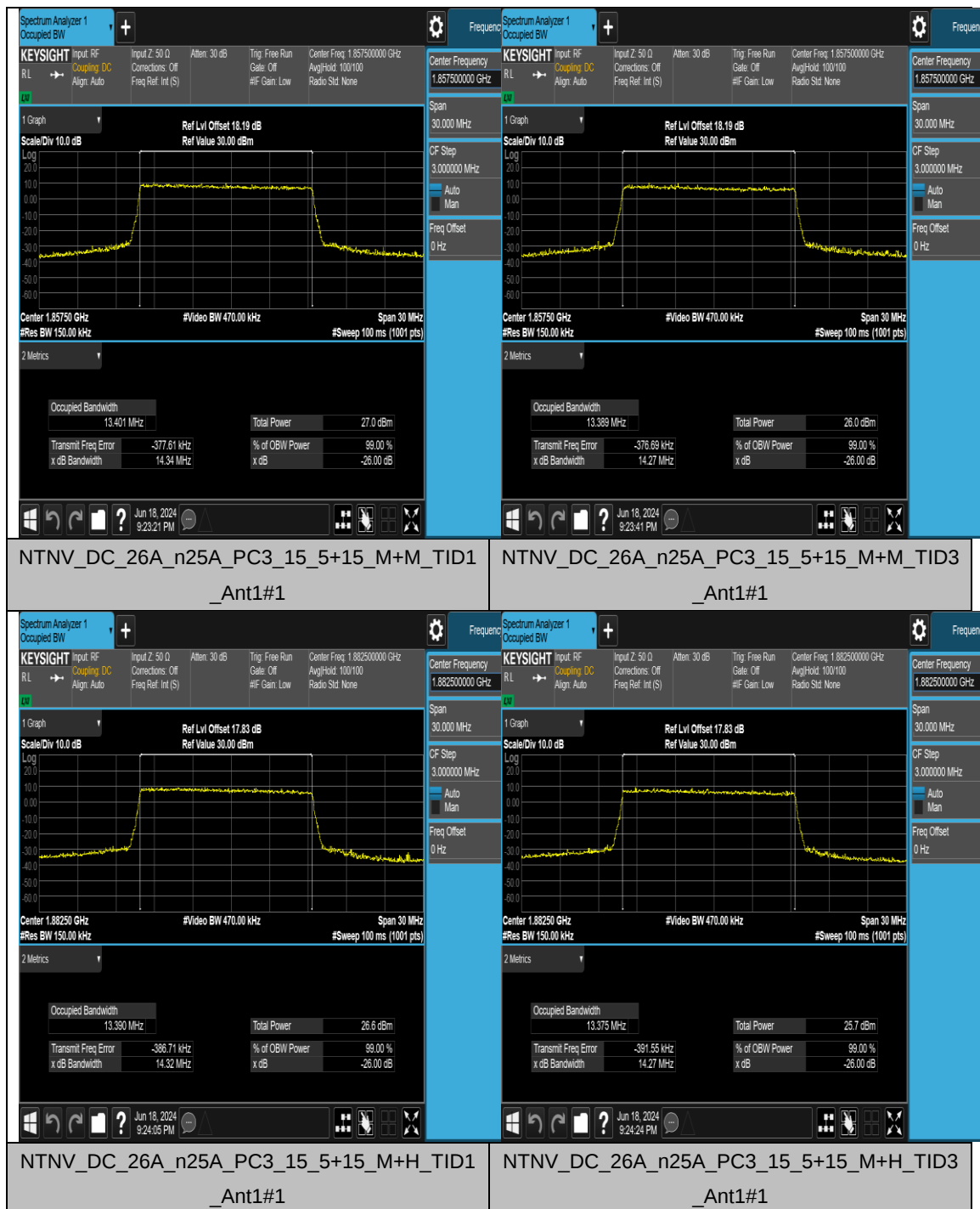
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Outer_Full	4.4785	5.086	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+L	Outer_Full	4.4808	5.108	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.4795	5.064	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+M	Outer_Full	4.4858	5.095	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Outer_Full	4.4799	5.076	PASS
DC_26A_n25A	15	5+5	DFT-16QAM	M+H	Outer_Full	4.4872	5.099	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Outer_Full	8.9336	9.670	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+L	Outer_Full	8.9352	9.687	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+M	Outer_Full	8.9590	9.722	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+M	Outer_Full	8.9491	9.676	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Outer_Full	8.9486	9.739	PASS
DC_26A_n25A	15	5+10	DFT-16QAM	M+H	Outer_Full	8.9652	9.763	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Outer_Full	13.401	14.34	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+L	Outer_Full	13.389	14.27	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+M	Outer_Full	13.390	14.32	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+M	Outer_Full	13.375	14.27	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Outer_Full	13.410	14.32	PASS
DC_26A_n25A	15	5+15	DFT-16QAM	M+H	Outer_Full	13.402	14.31	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Outer_Full	17.863	18.88	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+L	Outer_Full	17.874	18.91	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+M	Outer_Full	17.842	18.89	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+M	Outer_Full	17.812	18.90	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Outer_Full	17.835	18.88	PASS
DC_26A_n25A	15	5+20	DFT-16QAM	M+H	Outer_Full	17.820	18.84	PASS

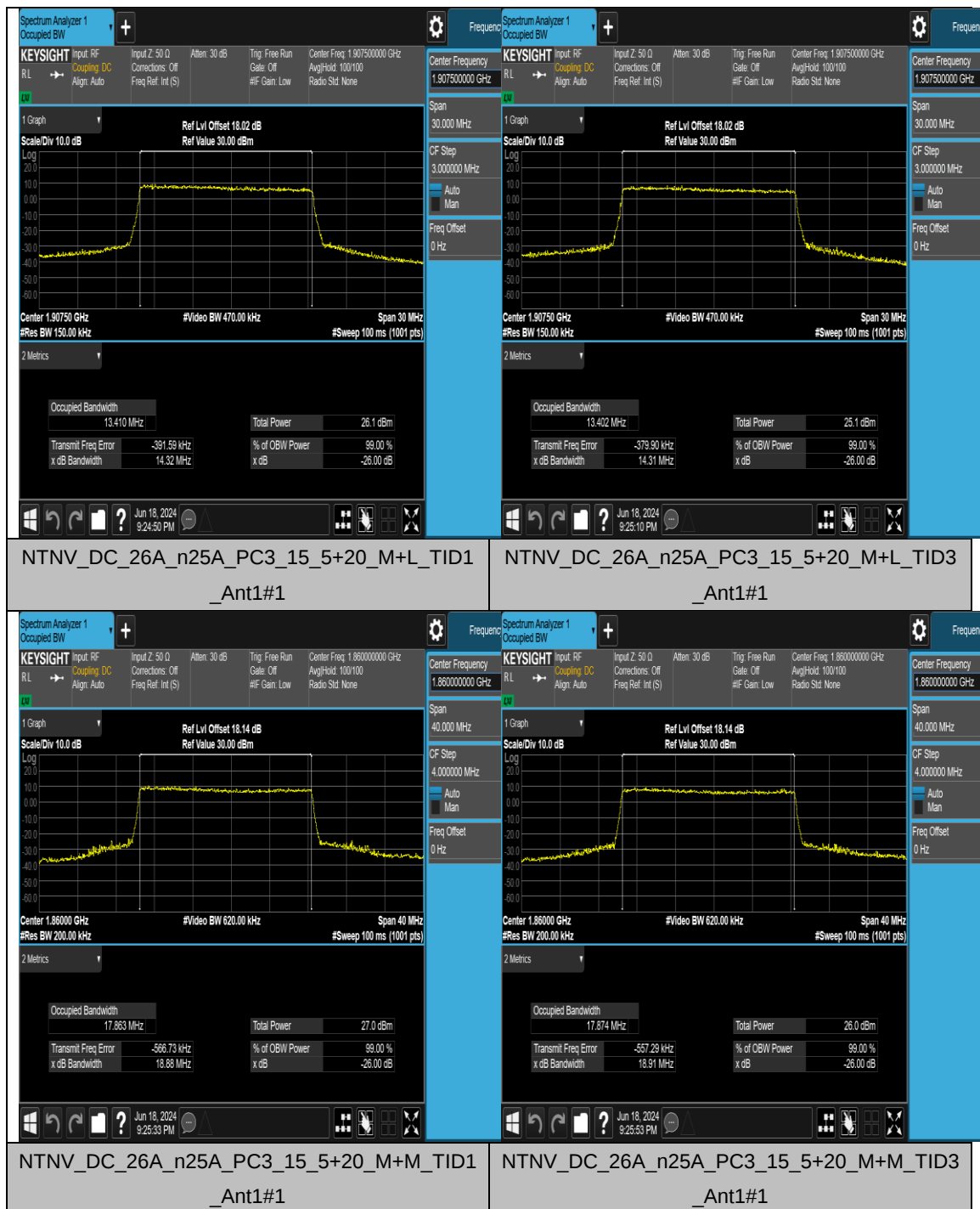
Test Graphs

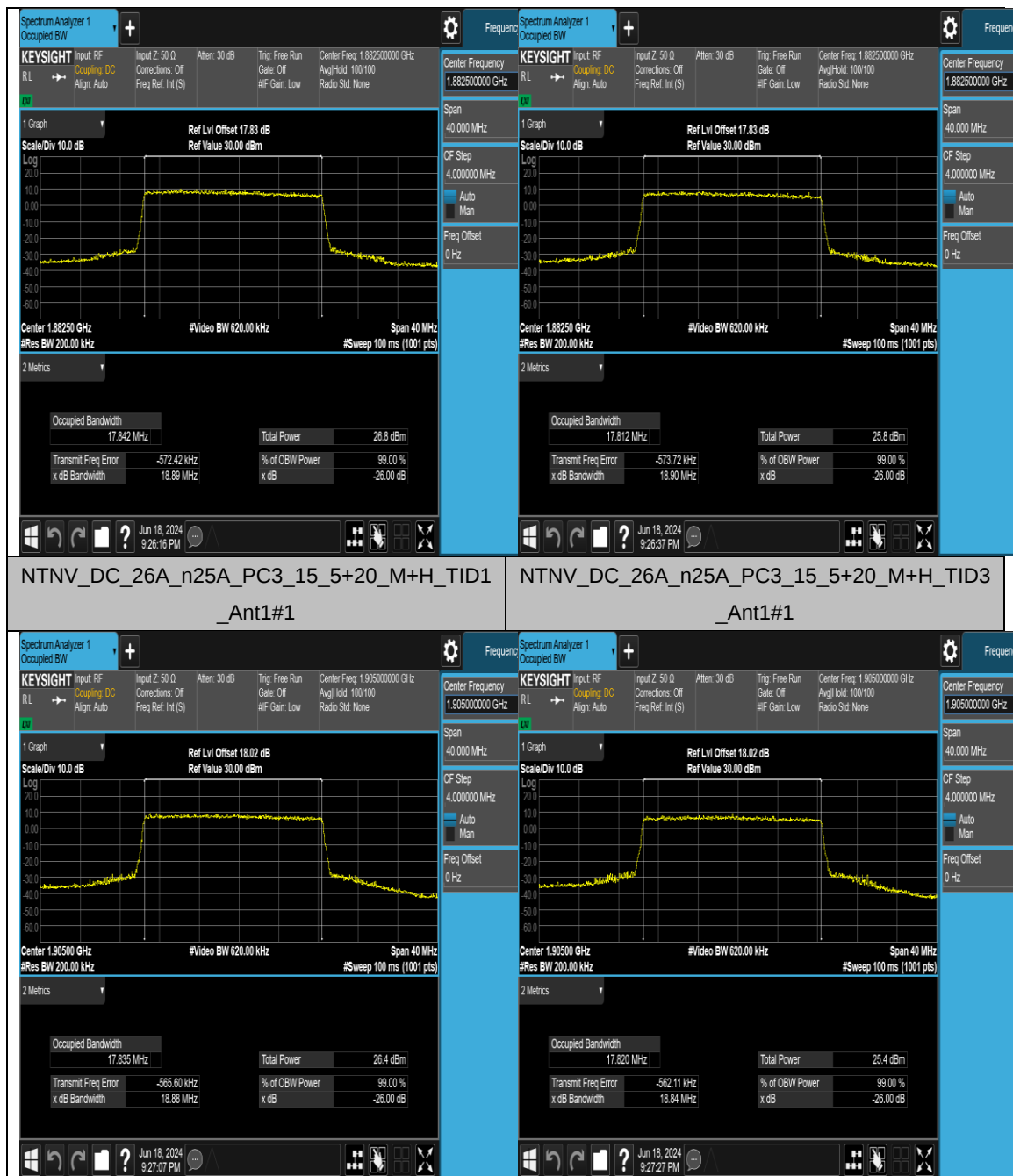












Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Outer_Full	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Outer_Full	PASS
DC_26A_n25A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_26A_n25A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_26A_n25A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_26A_n25A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

