

Appendix for 5A_N77A(3700-3980MHz)

Product Name: 5G Window CPE

Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	23.81	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Inner_Full	24.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.77	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.84	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	22.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Inner_Full	23.75	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	23.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	23.01	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	23.82	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Inner_Full	24.85	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.98	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.48	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	22.91	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Inner_Full	23.80	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.02	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.93	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	23.97	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Inner_Full	25.02	PC2	PASS

DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.82	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	24.11	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	23.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Inner_Full	23.87	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.82	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.84	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	23.95	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Inner_Full	24.85	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.92	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	24.00	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	22.92	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Inner_Full	23.80	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	23.10	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.98	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	24.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Inner_Full	25.01	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.96	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.85	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	22.94	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+M	Inner_Full	24.17	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.34	PC2	PASS
DC_5A_n77A-37	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	22.95	PC2	PASS

00-3980								
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	23.91	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Inner_Full	24.89	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	23.75	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	24.06	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	22.94	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Inner_Full	23.96	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	23.13	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	23.97	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	24.96	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.83	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.05	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	22.92	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	23.97	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.90	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.08	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	23.96	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Inner_Full	25.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.80	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	22.97	PC2	PASS

DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Inner_Full	24.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.95	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.12	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	23.86	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Inner_Full	24.90	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.88	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.95	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	22.96	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Inner_Full	23.91	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	24.08	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+L	Inner_Full	25.02	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.84	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.30	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	23.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+L	Inner_Full	24.02	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.88	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.37	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	24.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+M	Inner_Full	24.94	PC2	PASS
DC_5A_n77A-37	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	23.93	PC2	PASS

00-3980								
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.73	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	23.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+M	Inner_Full	23.95	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	22.90	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	23.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	23.82	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+H	Inner_Full	24.80	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	23.80	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	24.02	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	22.84	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+H	Inner_Full	23.80	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.68	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	23.22	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	24.34	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Inner_Full	25.05	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.88	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.29	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	23.09	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Inner_Full	24.09	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.93	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.25	PC2	PASS

DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	24.06	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Inner_Full	25.01	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.46	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	22.99	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Inner_Full	24.06	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.57	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.97	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	23.85	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Inner_Full	24.78	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	24.25	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	22.84	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Inner_Full	23.74	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.05	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	24.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Inner_Full	25.02	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.93	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.15	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	23.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Inner_Full	24.04	PC2	PASS

00-3980								
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.83	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.22	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	23.75	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Inner_Full	24.86	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.21	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.60	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	22.78	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Inner_Full	23.83	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.11	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.44	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	23.61	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Inner_Full	24.59	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.57	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.94	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	22.66	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Inner_Full	23.64	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.82	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.70	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	24.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Inner_Full	25.00	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.60	PC2	PASS

DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	24.04	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	23.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Inner_Full	24.05	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.58	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.09	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	23.95	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Inner_Full	24.87	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	24.11	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.28	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	22.97	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Inner_Full	23.87	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.30	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	23.57	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Inner_Full	24.51	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.56	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.88	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	22.67	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Inner_Full	23.49	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.64	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.77	PC2	PASS
DC_5A_n77A-37	30	5+70	DFT-QPSK	M+L	Outer_Full	23.99	PC2	PASS

00-3980								
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Inner_Full	25.06	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.65	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	24.12	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	23.07	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Inner_Full	24.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.54	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.01	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	23.65	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Inner_Full	24.67	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.08	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	22.72	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Inner_Full	23.75	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.29	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.03	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	23.19	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Inner_Full	24.14	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.14	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.58	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	22.18	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Inner_Full	23.13	PC2	PASS

DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.18	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.90	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	24.34	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Inner_Full	25.42	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.98	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.31	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	23.33	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Inner_Full	24.45	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.19	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.38	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	23.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Inner_Full	24.68	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	24.15	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	22.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Inner_Full	23.71	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.08	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.42	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	23.15	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Inner_Full	24.19	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.25	PC2	PASS
DC_5A_n77A-37	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.53	PC2	PASS

00-3980								
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	22.15	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Inner_Full	23.19	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.30	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.45	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	24.38	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Inner_Full	25.55	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.90	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.43	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	23.40	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Inner_Full	24.52	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.06	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.99	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	23.71	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Inner_Full	24.77	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.10	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	22.77	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Inner_Full	23.71	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.73	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.13	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	23.07	PC2	PASS

DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Inner_Full	24.06	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.34	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.69	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	22.14	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Inner_Full	23.08	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.55	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.76	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	24.41	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	25.46	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.89	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	24.10	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	23.42	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Inner_Full	24.48	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.78	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.77	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	23.73	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	24.73	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.97	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.94	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	22.68	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Inner_Full	23.76	PC2	PASS
DC_5A_n77A-37	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.90	PC2	PASS

00-3980								
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.90	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	23.22	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	24.17	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.37	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.53	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	22.21	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Inner_Full	23.08	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.35	PC2	PASS
DC_5A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.53	PC2	PASS

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_5A_n77A-3 700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.06	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.69	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.15	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.69	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.43	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.65	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.64	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.75	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.44	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.30	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.95	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.53	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.72	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.59	≤13	PASS

DC_5A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.53	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.06	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.26	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.84	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.48	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.47	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.75	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.86	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.85	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.11	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.86	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_5A_n77A-3	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.16	≤13	PASS

700-3980										
DC_5A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.60	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.20	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.61	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.05	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.38	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.31	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.37	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.34	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	0.00	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.37	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.38	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.39	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.98	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.90	≤13	PASS

DC_5A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.46	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.27	≤13	PASS

Test Graphs























