Appendix for 4A_N71A

Product Name: 5G Window CPE
Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	22.53	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Inner_Full	23.62	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	22.74	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.58	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	21.52	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+L	Inner_Full	22.53	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	21.26	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	21.33	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	22.49	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+M	Inner_Full	23.39	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	22.64	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	22.31	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+M	Outer_Full	21.35	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+M	Inner_Full	22.38	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	21.80	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	21.58	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	21.96	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Inner_Full	23.11	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	21.85	PC3	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	21.91	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	21.03	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+H	Inner_Full	22.04	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	20.80	PC3	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	20.55	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	22.44	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Inner_Full	23.50	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.86	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.37	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+L	Outer_Full	21.57	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+L	Inner_Full	22.43	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.23	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	21.35	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	22.47	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+M	Inner_Full	23.45	PC3	PASS

DC_4A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	22.82	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.44	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+M	Outer_Full	21.41	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+M	Inner_Full	22.45	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	21.56	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	21.41	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	22.08	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Inner_Full	23.11	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.13	PC3	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.96	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+H	Outer_Full	21.05	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+H	Inner_Full	22.10	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.85	PC3	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.88	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	22.25	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Inner_Full	23.57	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.91	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.68	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+L	Outer_Full	21.57	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+L	Inner_Full	22.34	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	21.72	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	21.35	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	22.70	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+M	Inner_Full	23.57	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	22.82	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.09	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Outer_Full	21.55	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Inner_Full	22.59	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	21.98	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.00	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	22.30	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Inner_Full	23.22	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.23	PC3	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.85	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	21.33	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Inner_Full	22.13	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	21.26	PC3	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	21.50	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	22.68	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Inner_Full	23.38	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.98	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.45	PC3	PASS

DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	21.53	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Inner_Full	22.35	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.14	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.73	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	22.64	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Inner_Full	23.68	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.66	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.21	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	21.74	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Inner_Full	22.64	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.97	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.14	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	22.28	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Inner_Full	23.22	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.81	PC3	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.96	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	21.47	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Inner_Full	22.47	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.52	PC3	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.68	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

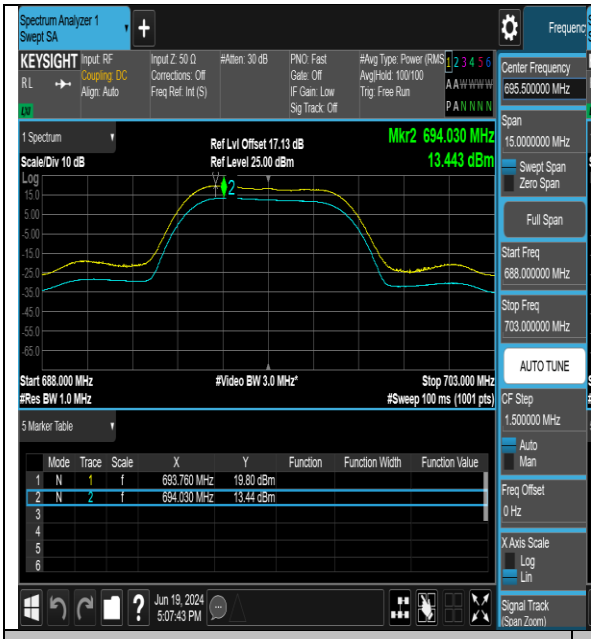
Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	100%	0.00	6.26	≤13	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	100%	0.00	6.89	≤13	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	100%	0.00	6.22	≤13	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+M	Outer_Full	100%	0.00	6.89	≤13	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	100%	0.00	6.36	≤13	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	100%	0.00	6.89	≤13	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.13	≤13	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.34	≤13	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.22	≤13	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.44	≤13	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	6.92	≤13	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.24	≤13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.24	≤13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.52	≤13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.33	≤13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.64	≤13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	11.36	≤13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.53	≤13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.51	≤13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.55	≤13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.49	≤13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.66	≤13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.41	≤13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.65	≤13	PASS

Test Graphs

NTNV_DC_4A_n71A_PC3_15_5+5_M+L_TID2_#1	NTNV_DC_4A_n71A_PC3_15_5+5_M+L_TID3_#1
NTNV_DC_4A_n71A_PC3_15_5+5_M+M_TID2_#1	NTNV_DC_4A_n71A_PC3_15_5+5_M+M_TID3_#1
NTNV_DC_4A_n71A_PC3_15_5+5_M+H_TID2_#1	NTNV_DC_4A_n71A_PC3_15_5+5_M+H_TID3_#1



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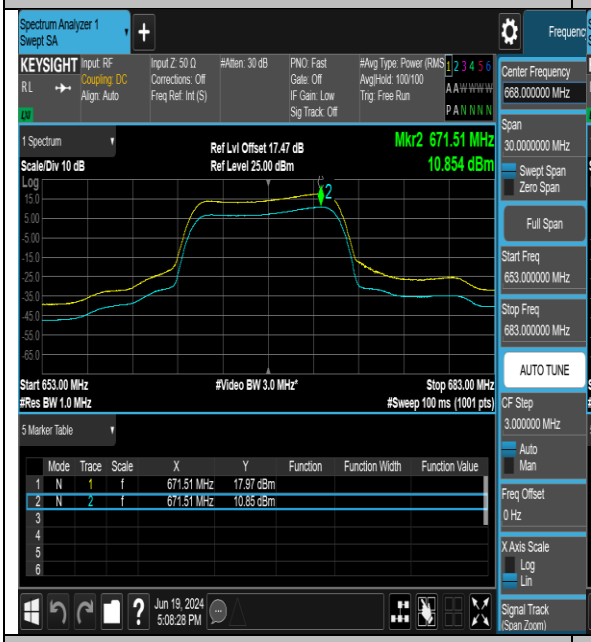
NTNV_DC_4A_n71A_PC3_15_5+10_M+L_TID3_#1



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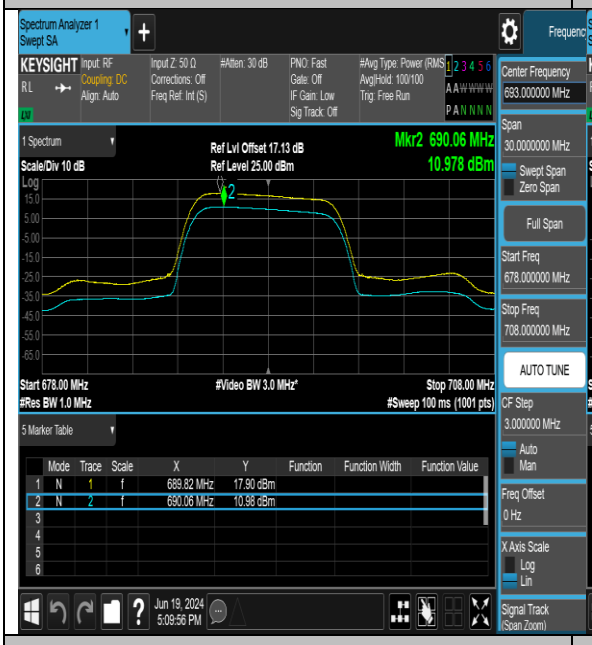
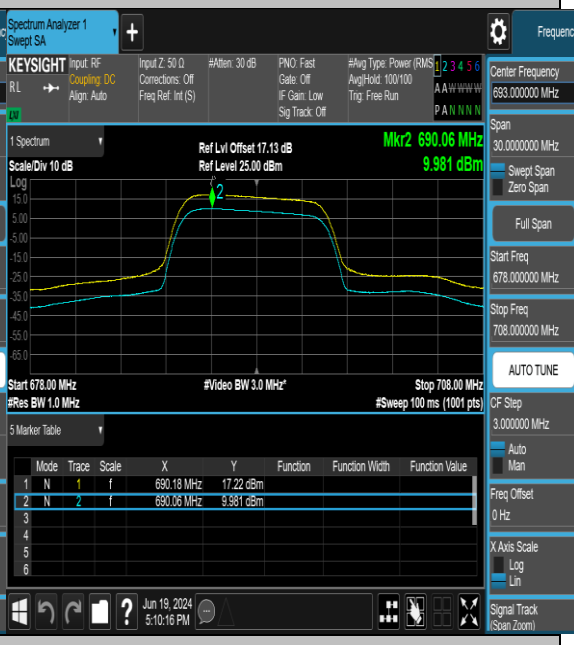
NTNV_DC_4A_n71A_PC3_15_5+10_M+H_TID2_#1

NTNV_DC_4A_n71A_PC3_15_5+10_M+H_TID3_#1



NTNV_DC_4A_n71A_PC3_15_5+15_M+L_TID2_#1



NTNV_DC_4A_n71A_PC3_15_5+15_M+L_TID3_#1

NTNV_DC_4A_n71A_PC3_15_5+15_M+M_TID2_#1

NTNV_DC_4A_n71A_PC3_15_5+15_M+M_TID3_#1

NTNV_DC_4A_n71A_PC3_15_5+15_M+H_TID2_#1

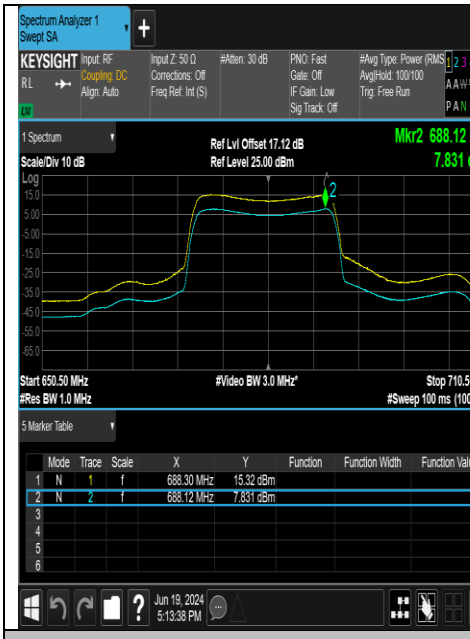
NTNV_DC_4A_n71A_PC3_15_5+15_M+H_TID3_#1

NTNV_DC_4A_n71A_PC3_15_5+20_M+L_TID2_#1

NTNV_DC_4A_n71A_PC3_15_5+20_M+L_TID3_#1

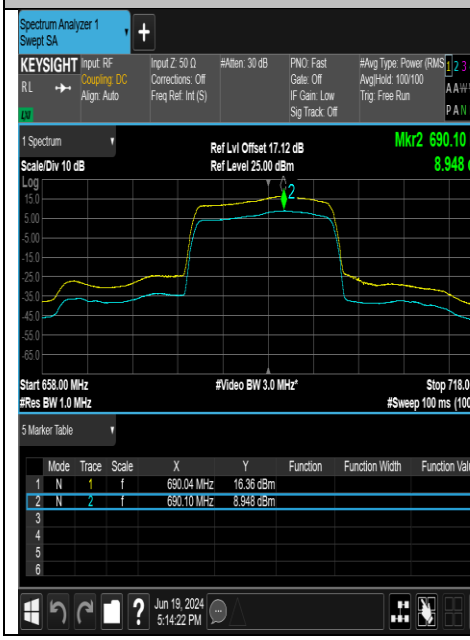
NTNV_DC_4A_n71A_PC3_15_5+20_M+M_TID2_#1

NTNV_DC_4A_n71A_PC3_15_5+20_M+M_TID3_#1



NTNV_DC_4A_n71A_PC3_15_5+20_M+H_TID2_#1

NTNV_DC_4A_n71A_PC3_15_5+20_M+H_TID3_#1

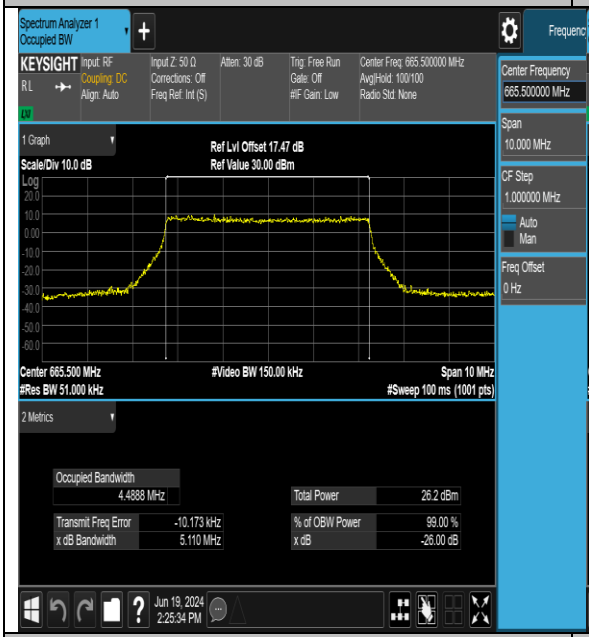
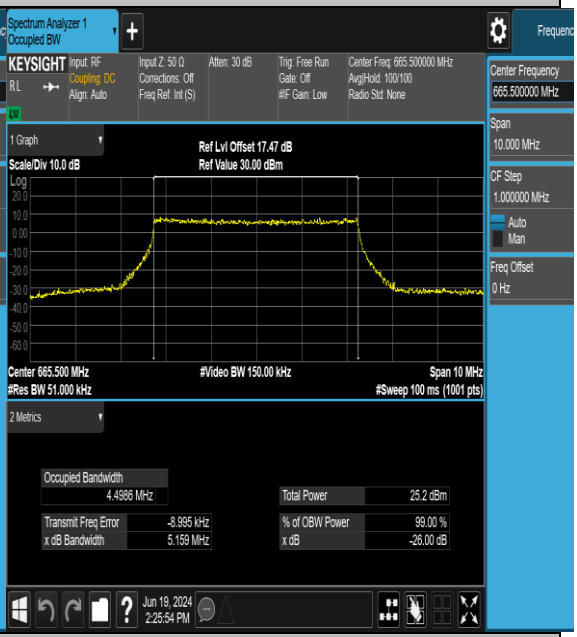


Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	4.4888	5.110	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	4.4986	5.159	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.4875	5.142	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+M	Outer_Full	4.4839	5.085	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	4.4745	5.070	PASS
DC_4A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	4.4802	4.994	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	8.9733	9.708	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+L	Outer_Full	8.9701	9.679	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	8.9688	9.805	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+M	Outer_Full	8.9667	9.796	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	8.9080	9.652	PASS
DC_4A_n71A	15	5+10	DFT-16QAM	M+H	Outer_Full	8.9133	9.595	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	13.372	14.21	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+L	Outer_Full	13.368	14.25	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	13.460	14.31	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Outer_Full	13.458	14.29	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	13.298	14.17	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	13.299	14.16	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	17.750	18.75	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	17.769	18.75	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	17.930	18.98	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	17.911	18.91	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	17.734	18.73	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	17.735	18.78	PASS

Test Graphs

NTNV_DC_4A_n71A_PC3_15_5+5_M+L_TID1_Ant1#1	NTNV_DC_4A_n71A_PC3_15_5+5_M+L_TID3_Ant1#1
	
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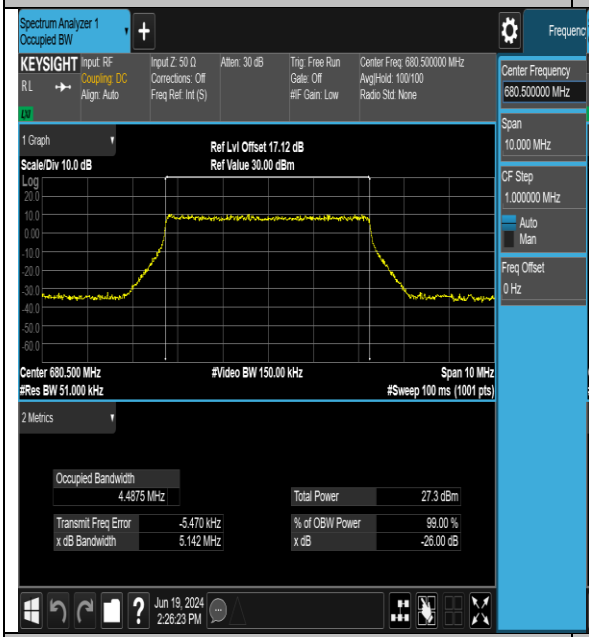	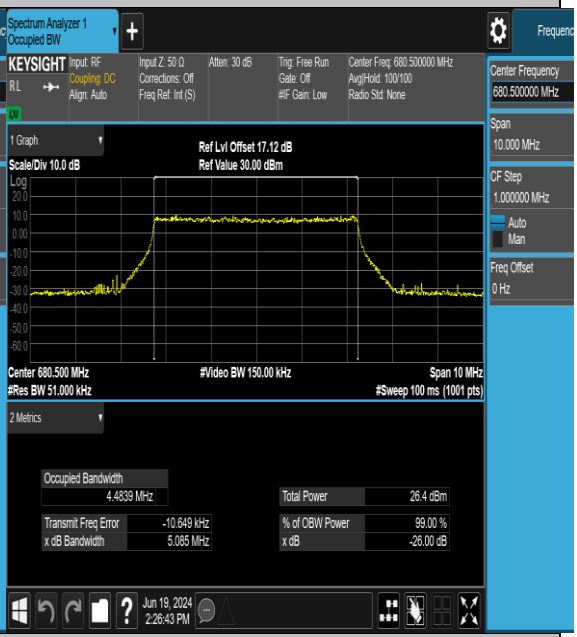
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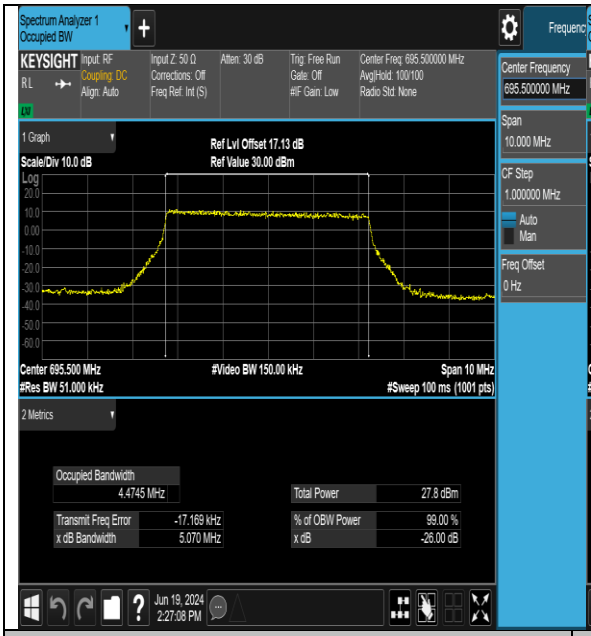

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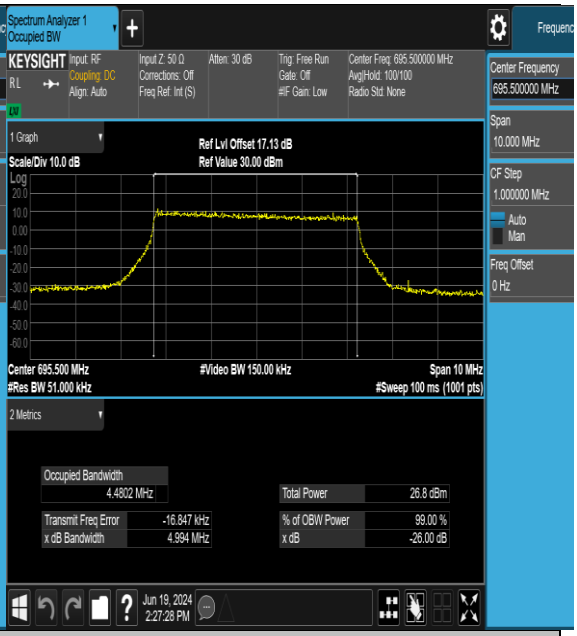

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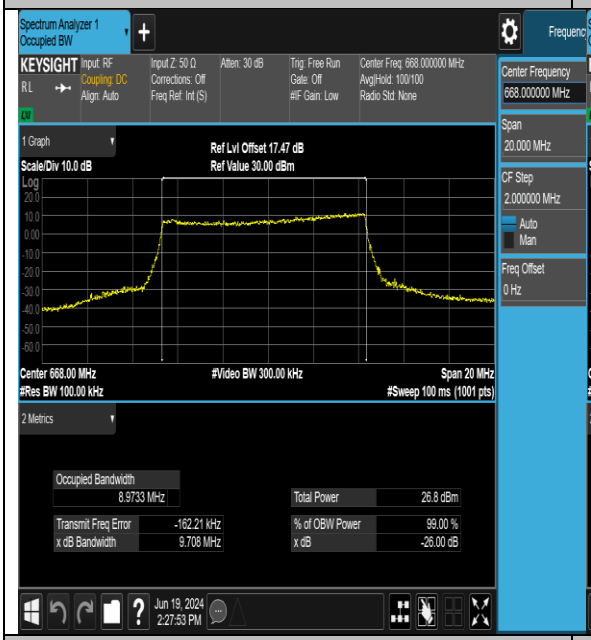

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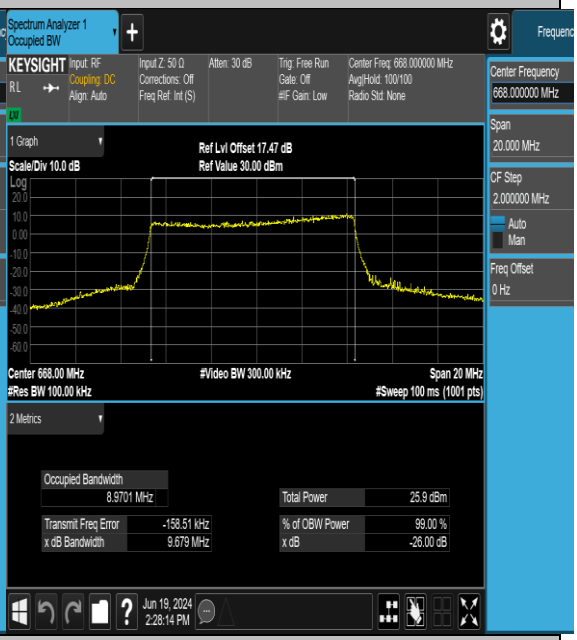

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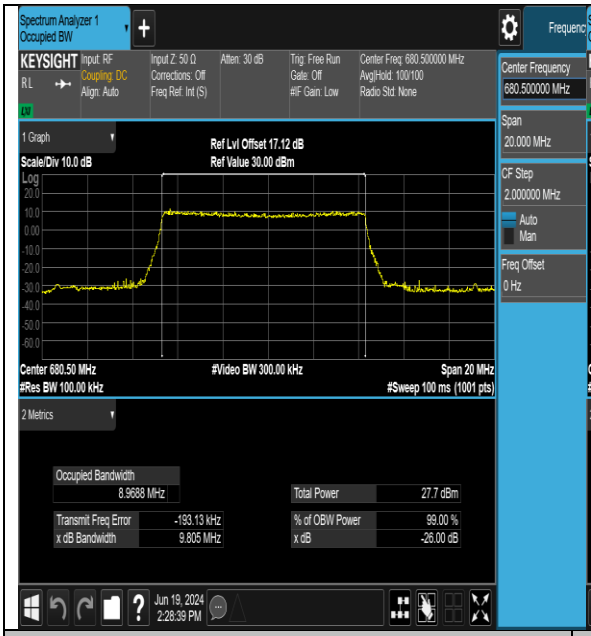

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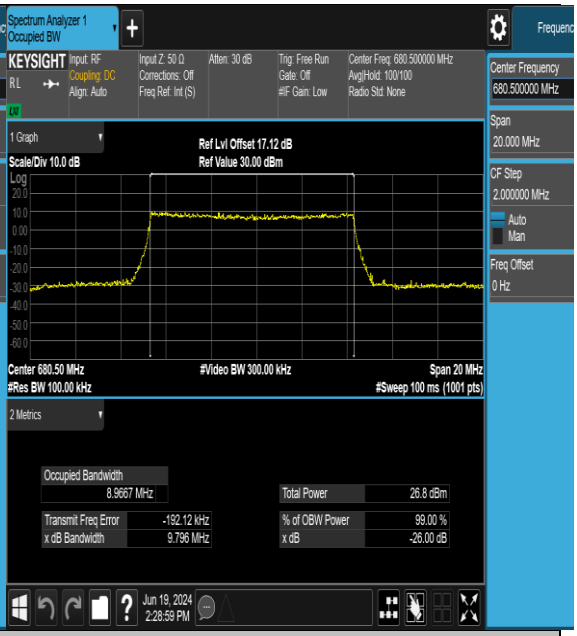

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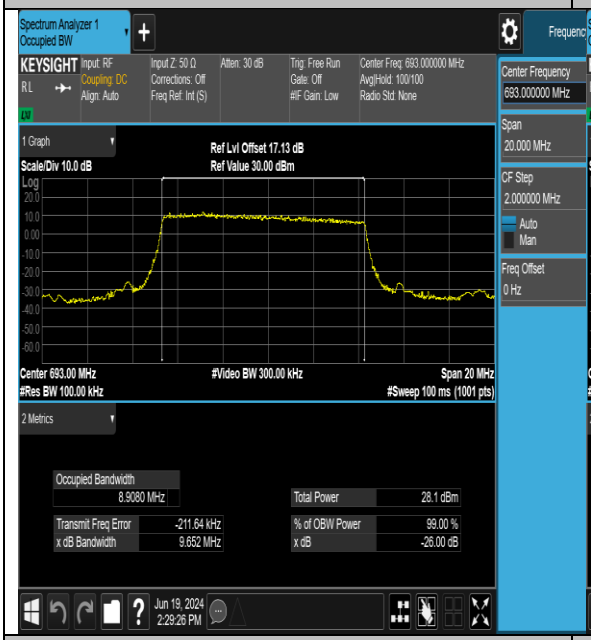

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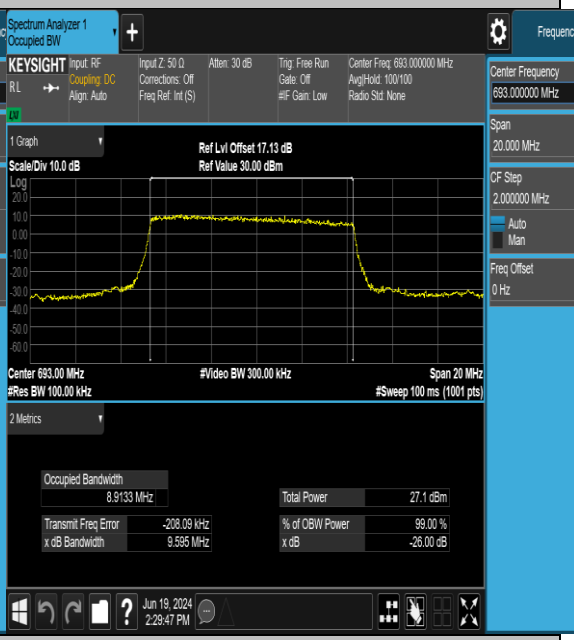

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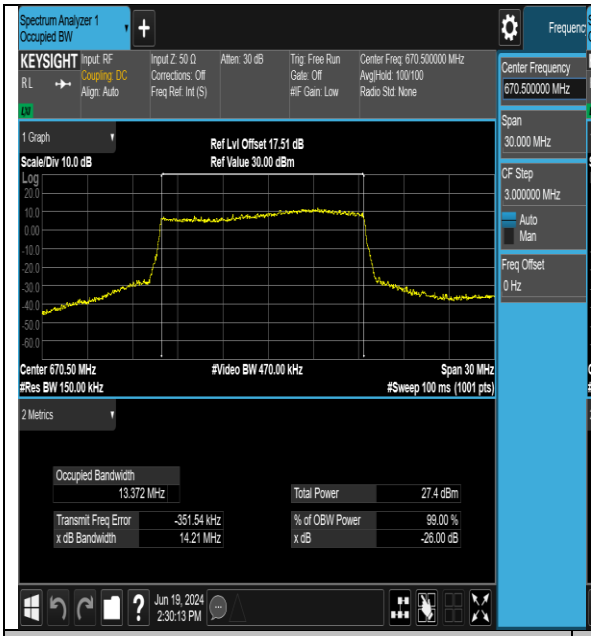

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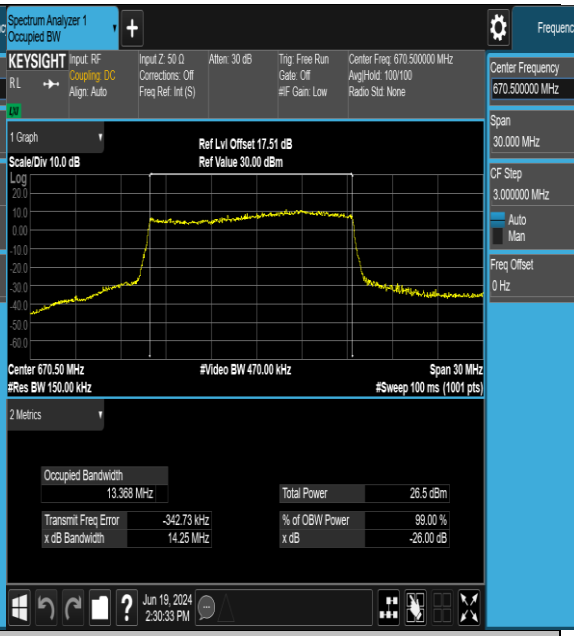

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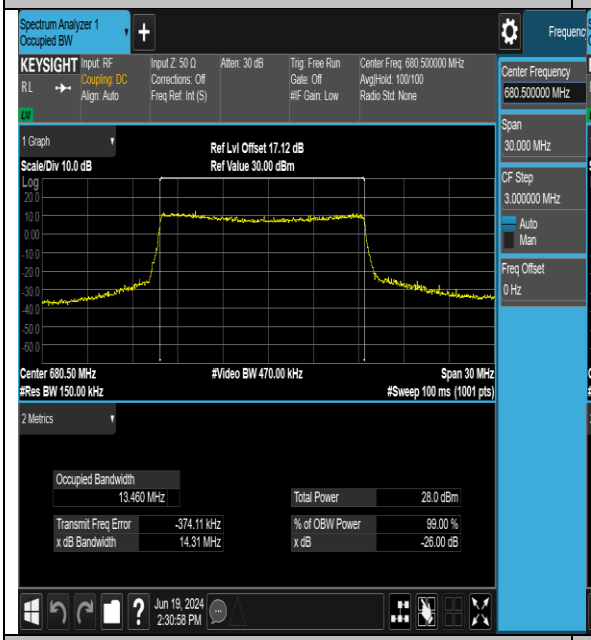

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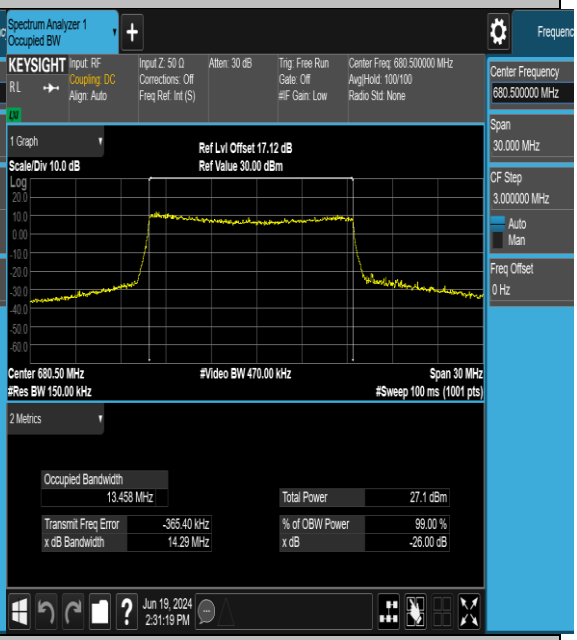

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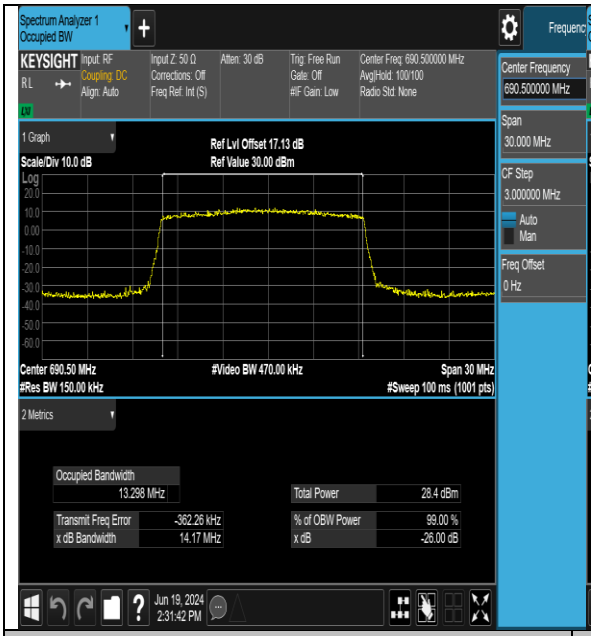

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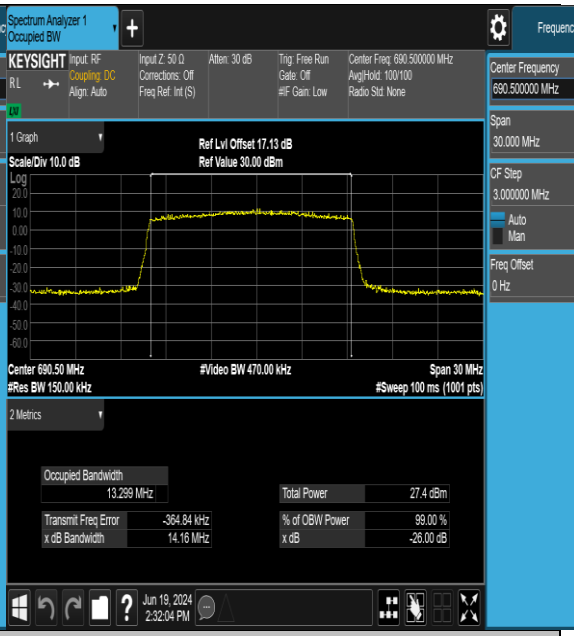

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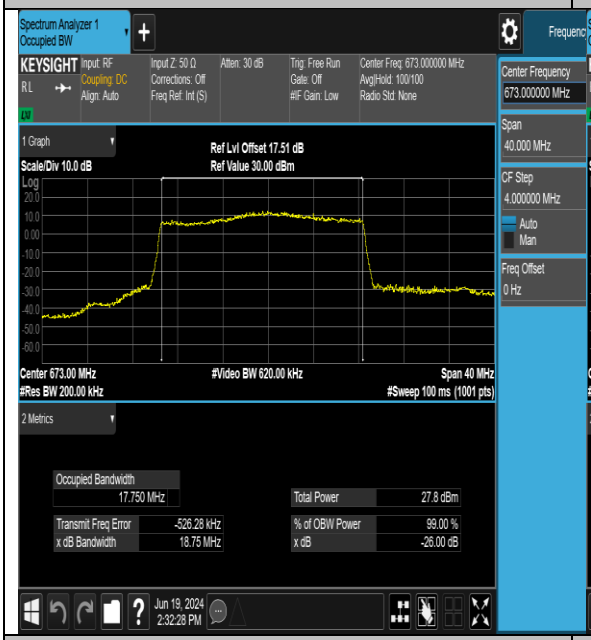

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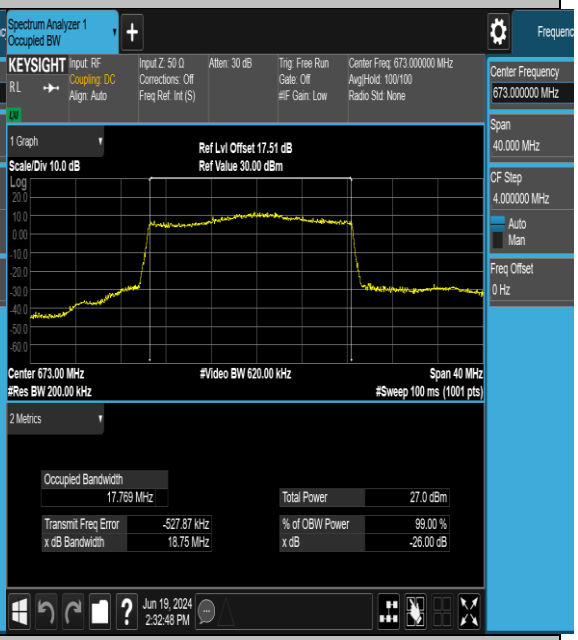

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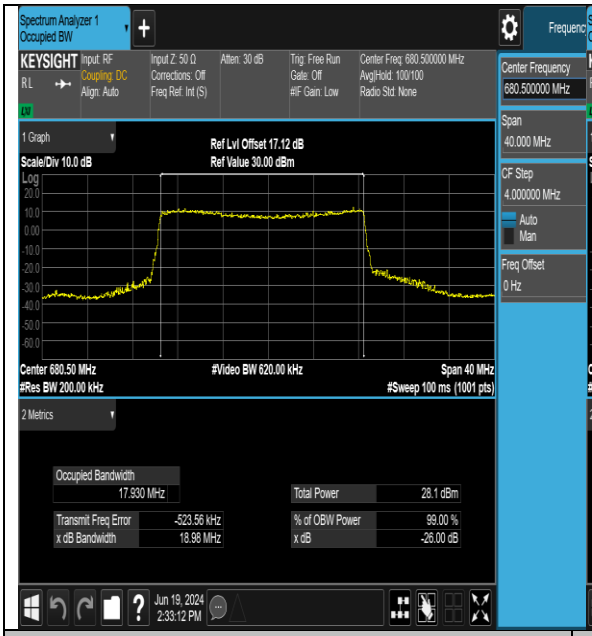

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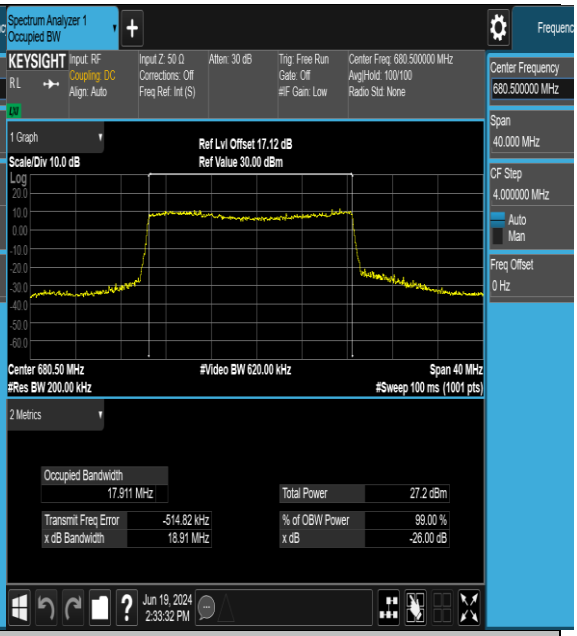

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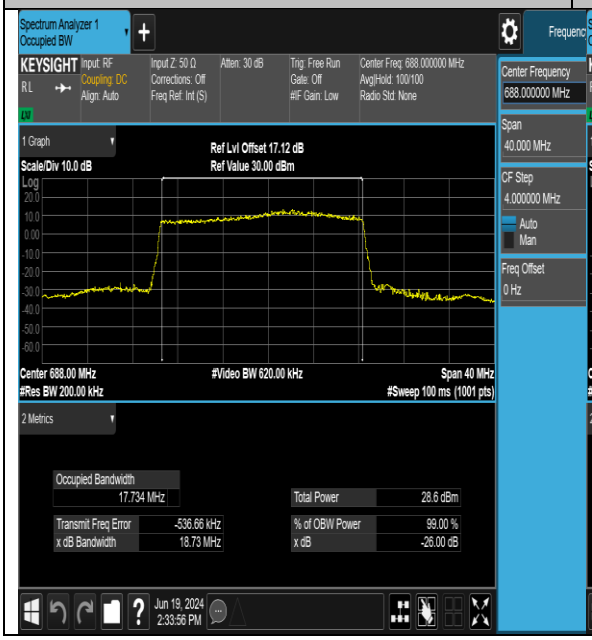
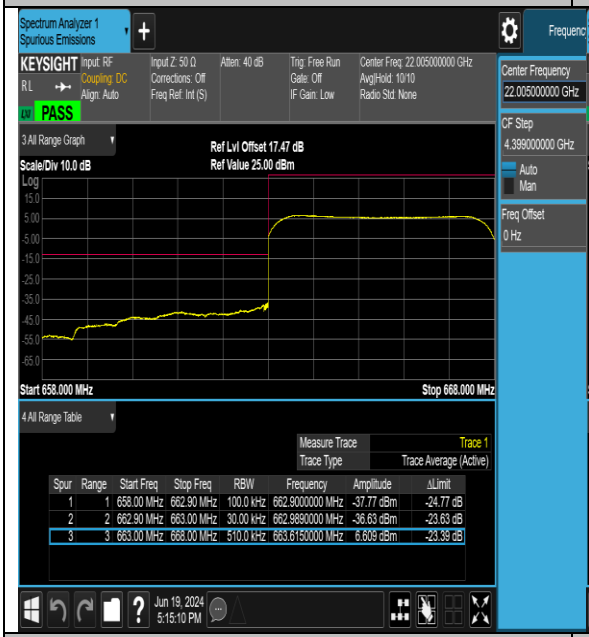
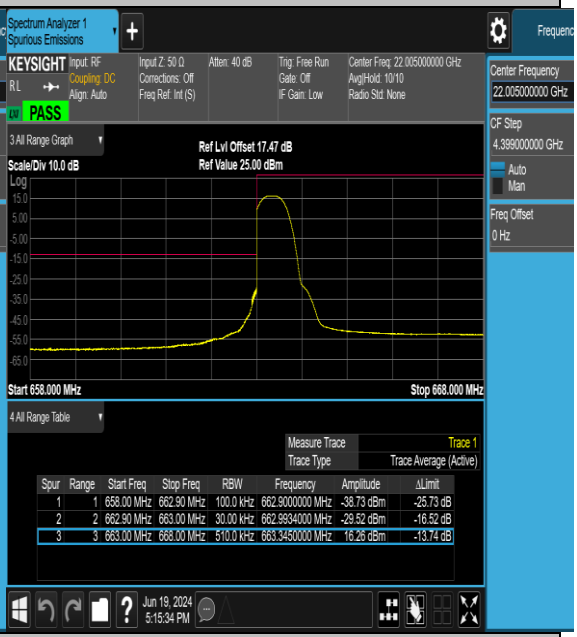
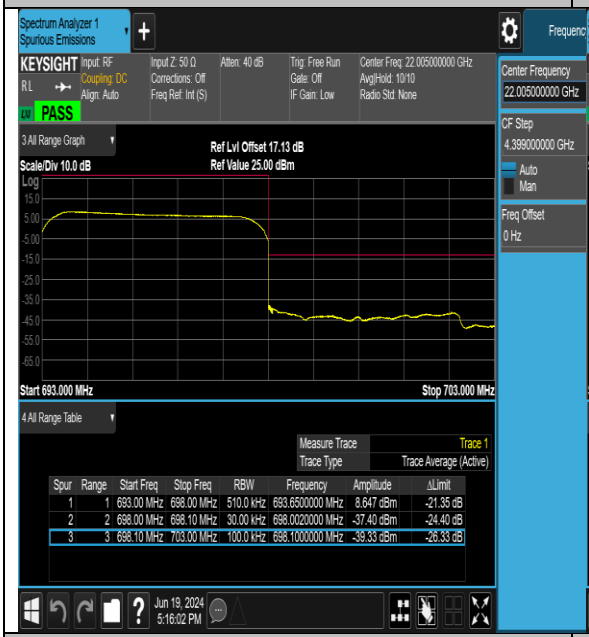
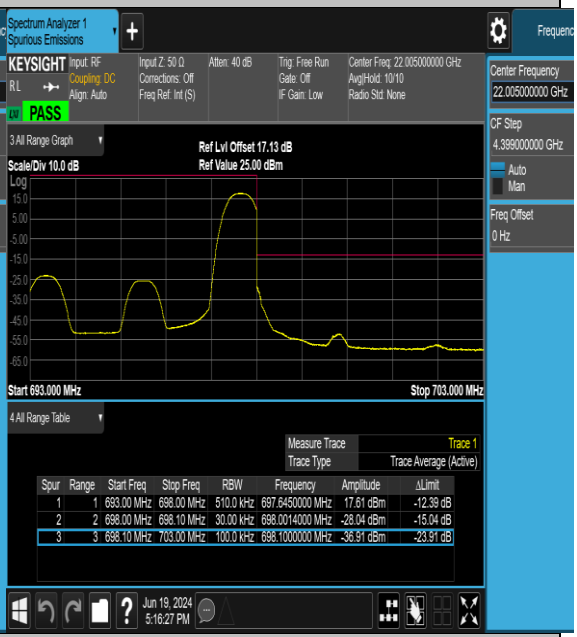


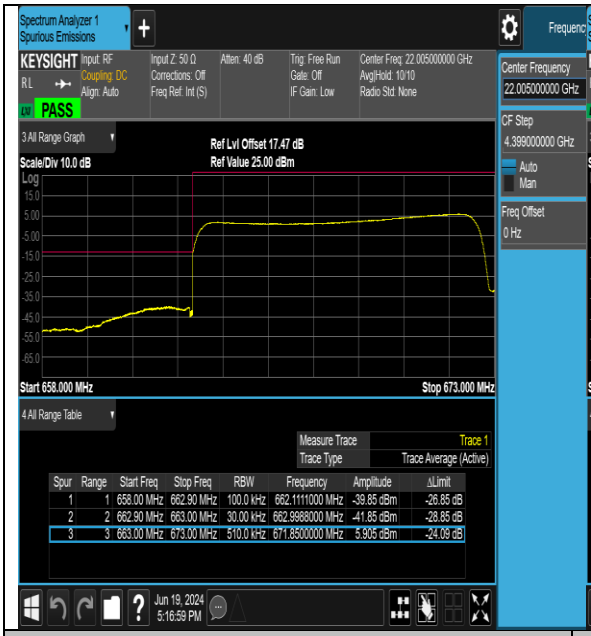
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	PASS
DC_4A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_4A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

NTNV_DC_4A_n71A_PC3_15_5+5_M+L_TID9__#1	NTNV_DC_4A_n71A_PC3_15_5+5_M+L_TID7__#1
	
NTNV_DC_4A_n71A_PC3_15_5+5_M+H_TID9__#1	NTNV_DC_4A_n71A_PC3_15_5+5_M+H_TID8__#1
	
NTNV_DC_4A_n71A_PC3_15_5+10_M+L_TID9__#1	NTNV_DC_4A_n71A_PC3_15_5+10_M+L_TID7__#1



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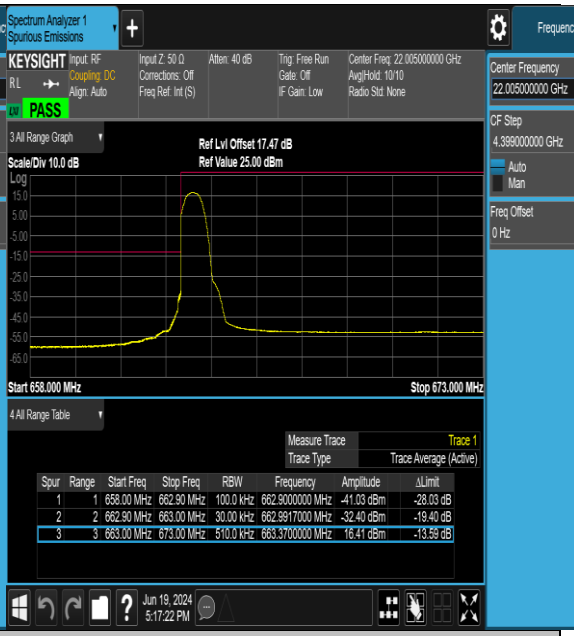

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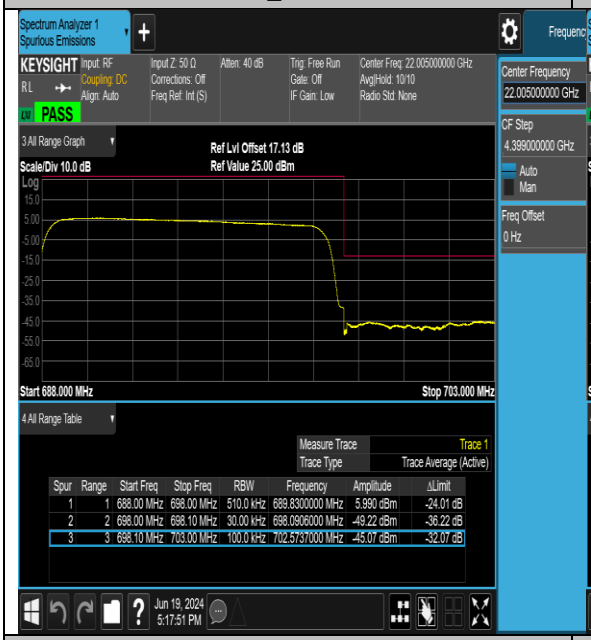

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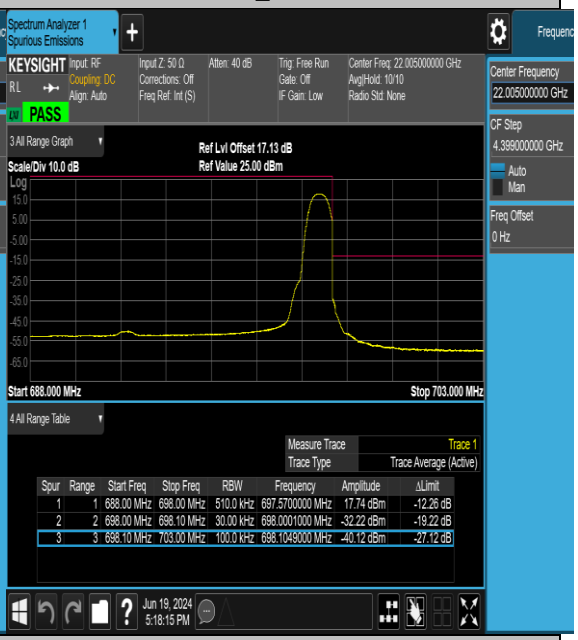

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