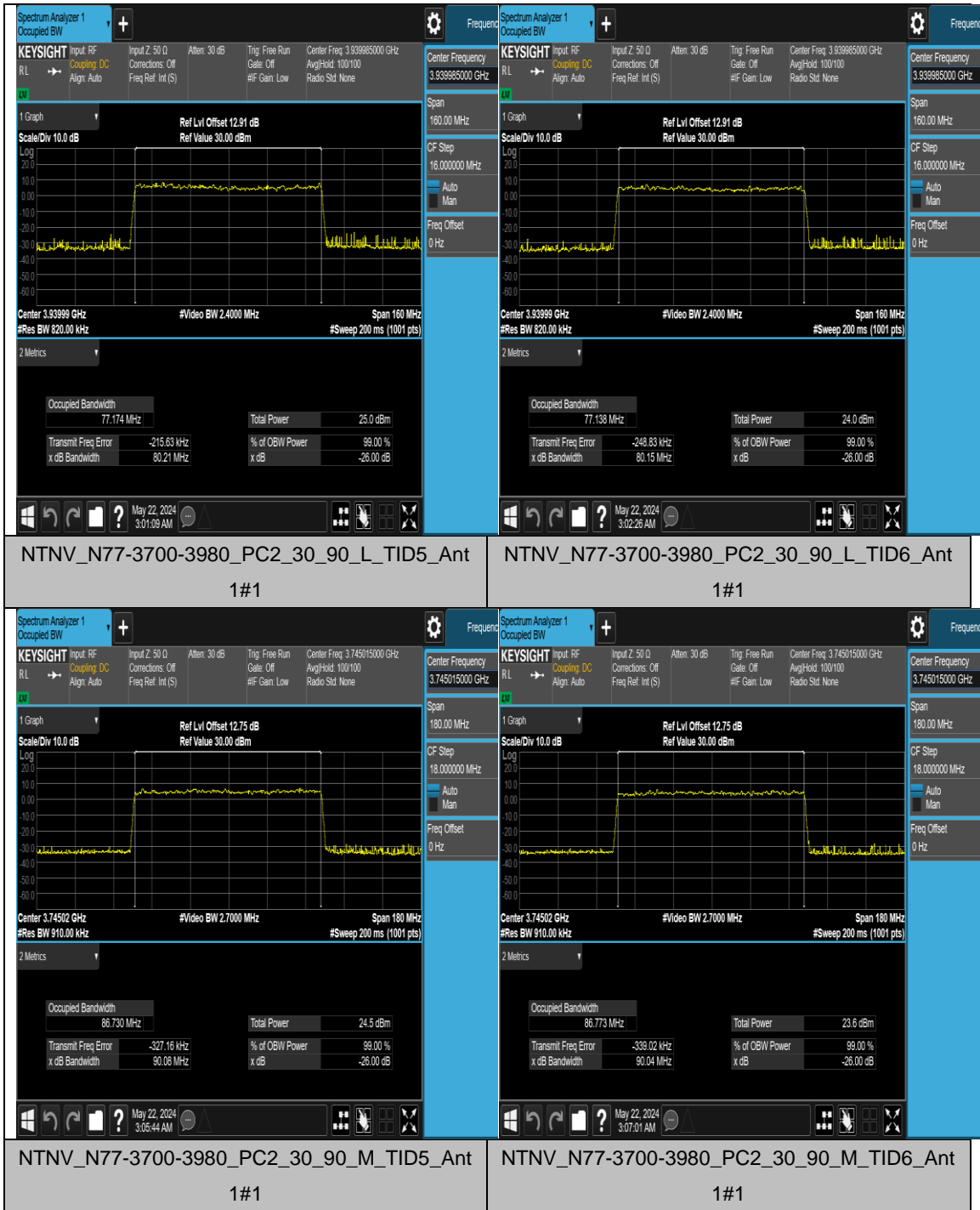


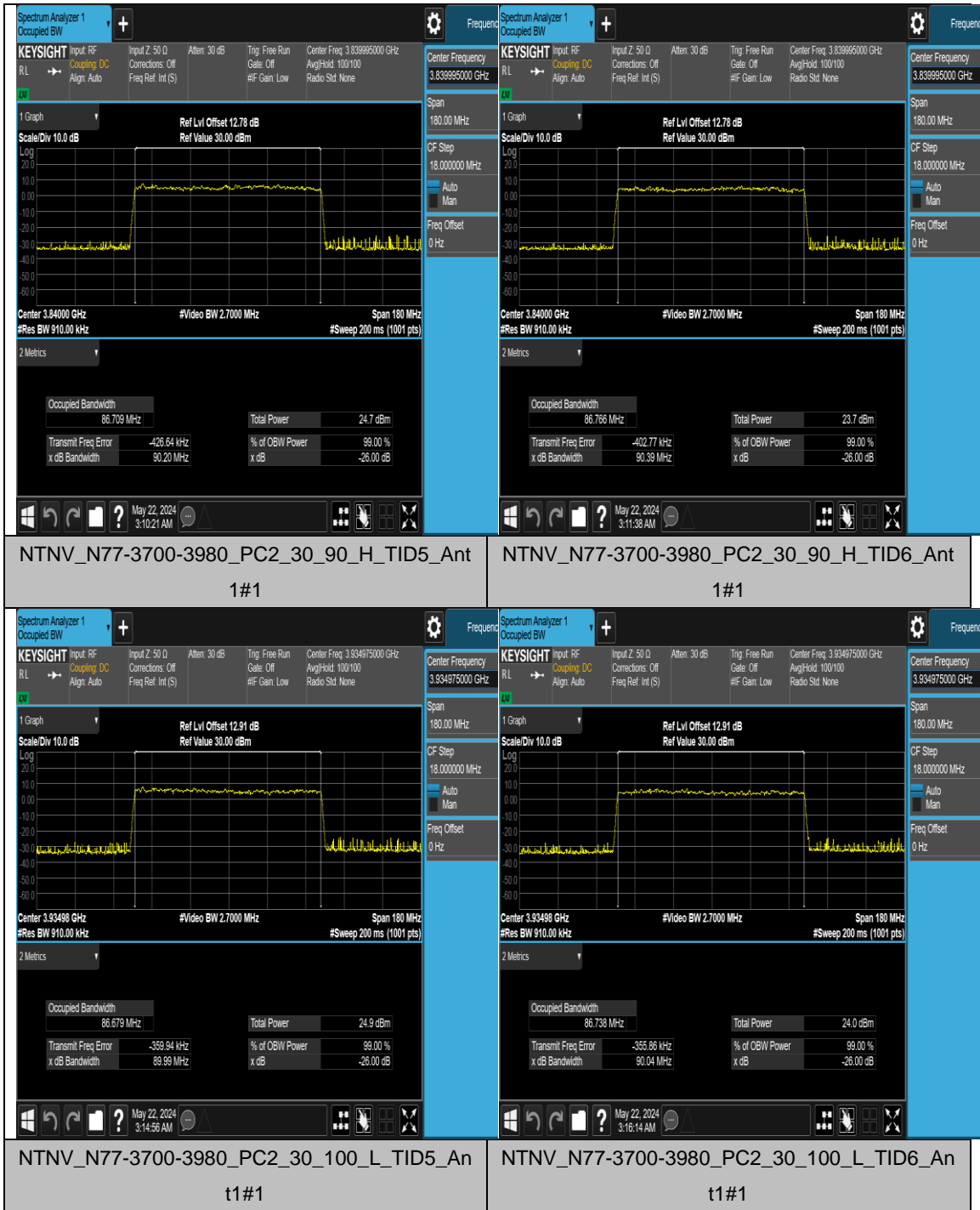
NTNV_N77-3700-3980_PC2_30_80_M_TID5_Ant
1#1

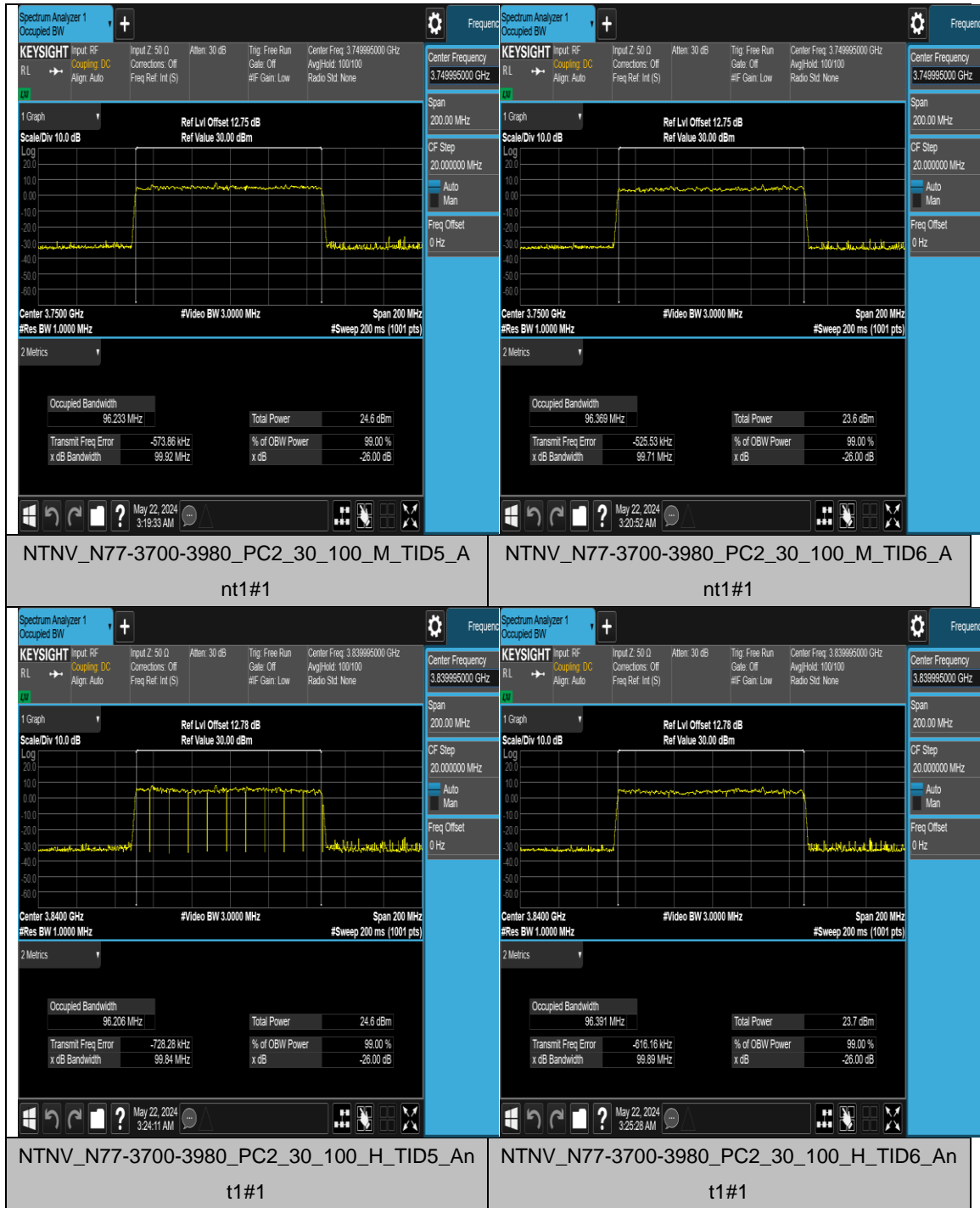
NTNV_N77-3700-3980_PC2_30_80_M_TID6_Ant
1#1

NTNV_N77-3700-3980_PC2_30_80_H_TID5_Ant
1#1

NTNV_N77-3700-3980_PC2_30_80_H_TID6_Ant
1#1







NTNV_N77-3700-3980_PC2_30_100_M_TID5_A

nt1#1

NTNV_N77-3700-3980_PC2_30_100_M_TID6_A

nt1#1

NTNV_N77-3700-3980_PC2_30_100_H_TID5_An

t1#1

NTNV_N77-3700-3980_PC2_30_100_H_TID6_An

t1#1



Appendix D: Band Edge for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
N77-3700-3980	30	10	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	10	DFT-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	10	DFT-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	10	DFT-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	15	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	15	DFT-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	15	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	15	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	15	DFT-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	20	DFT-16QAM	L	Outer_Full	PASS

3980						
N77-3700-3980	30	20	DFT-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	20	DFT-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	30	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	30	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	30	DFT-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	30	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	30	DFT-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	40	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	40	DFT-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	40	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	40	DFT-16QAM	H	Edge_1RB_Right	PASS

3980						
N77-3700-3980	30	50	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	50	DFT-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	60	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	60	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	70	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	70	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	70	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	80	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	80	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	80	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	90	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	90	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	90	DFT-QPSK	H	Outer_Full	PASS

3980						
N77-3700-3980	30	90	DFT-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	PASS

Test Graphs

