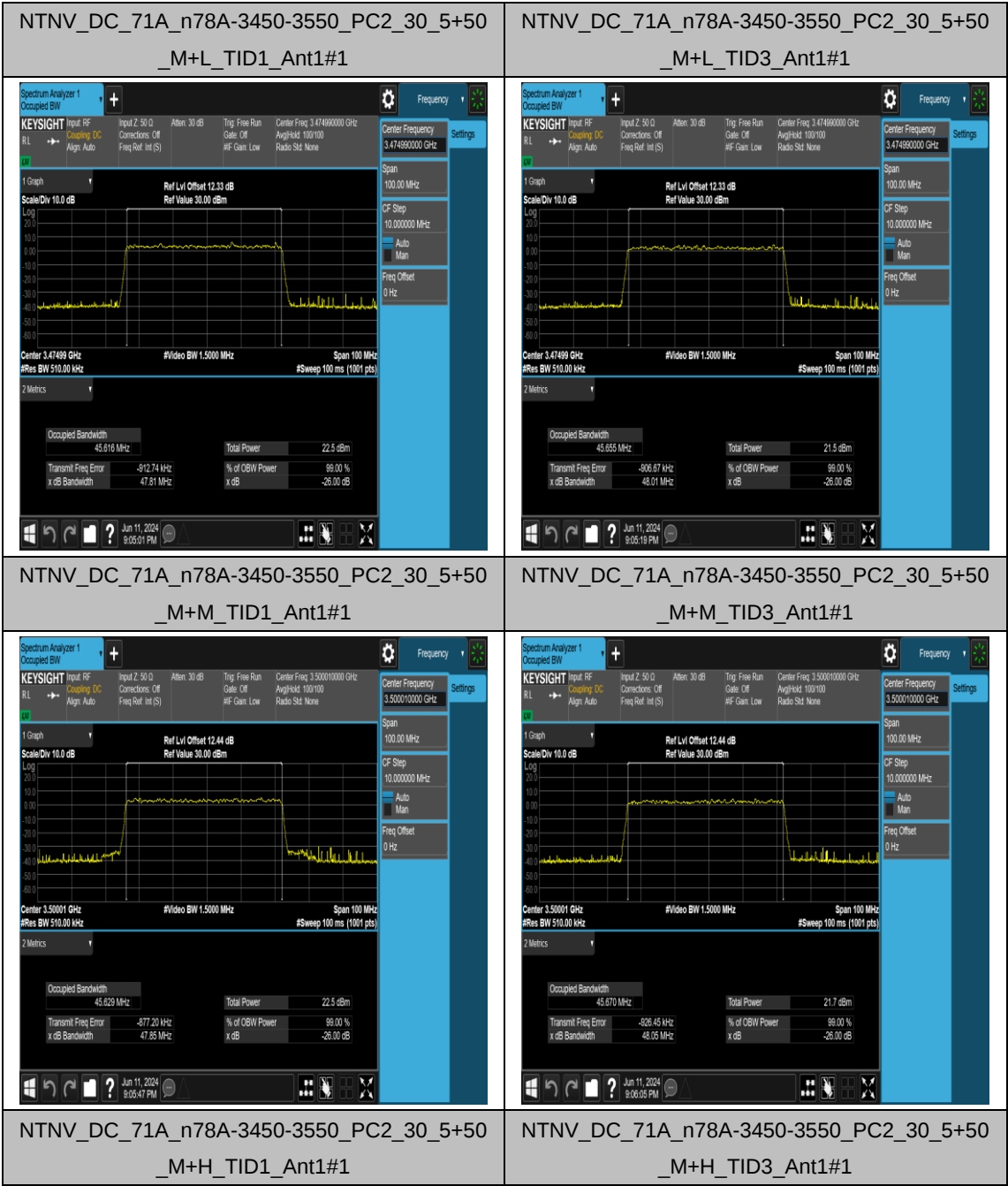
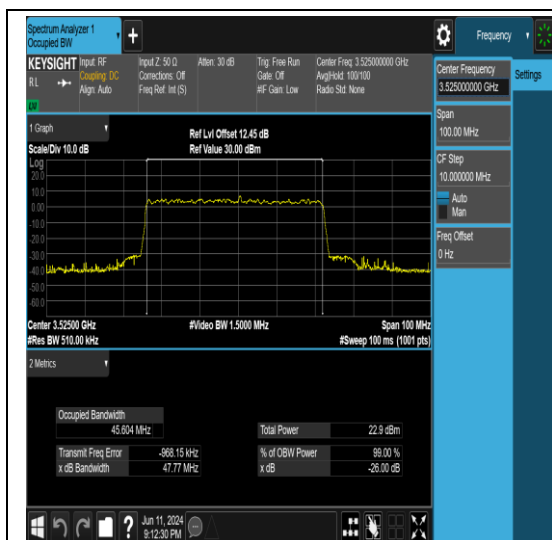
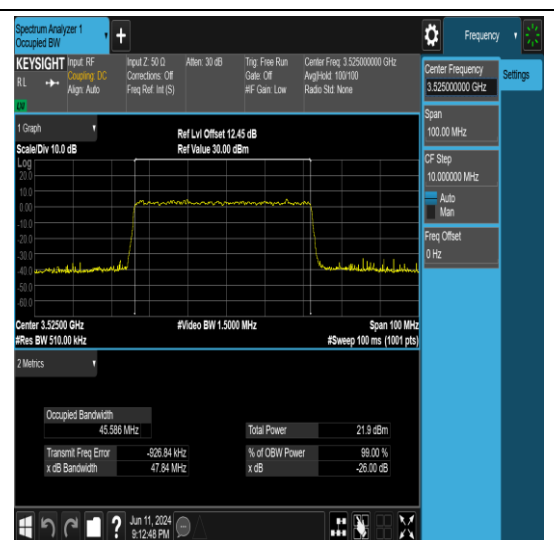


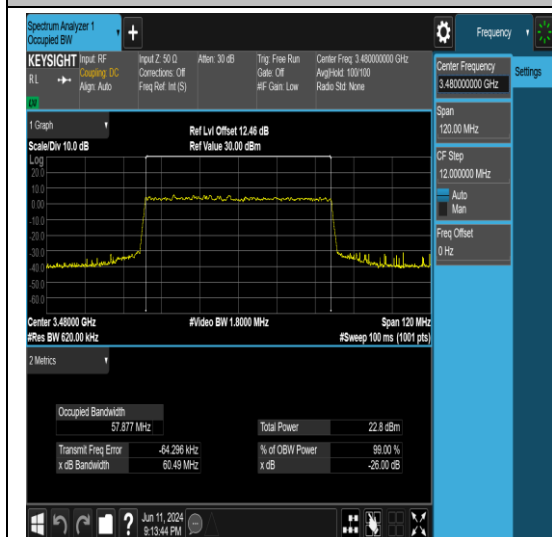




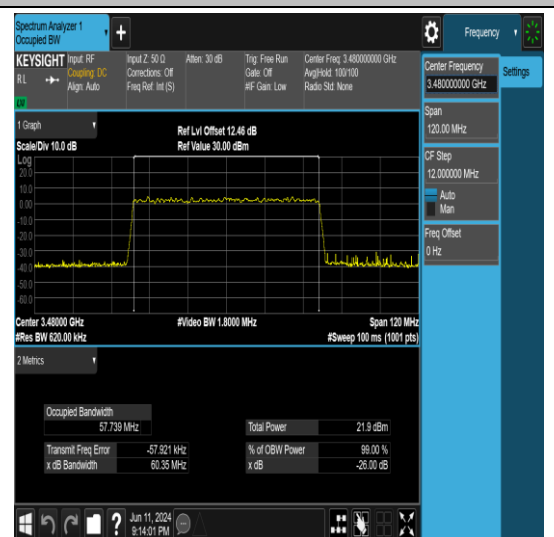
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60
_M+L_TID1_Ant1#1



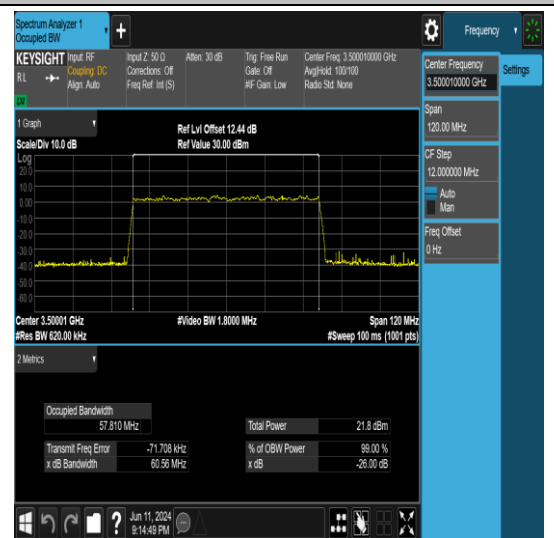
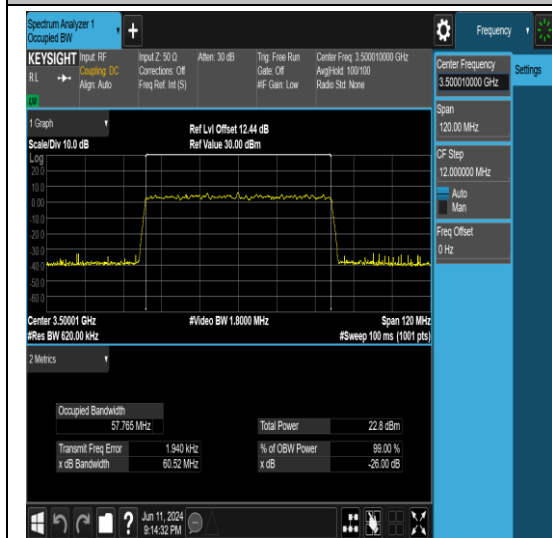
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_M+L_TID3_Ant1#1

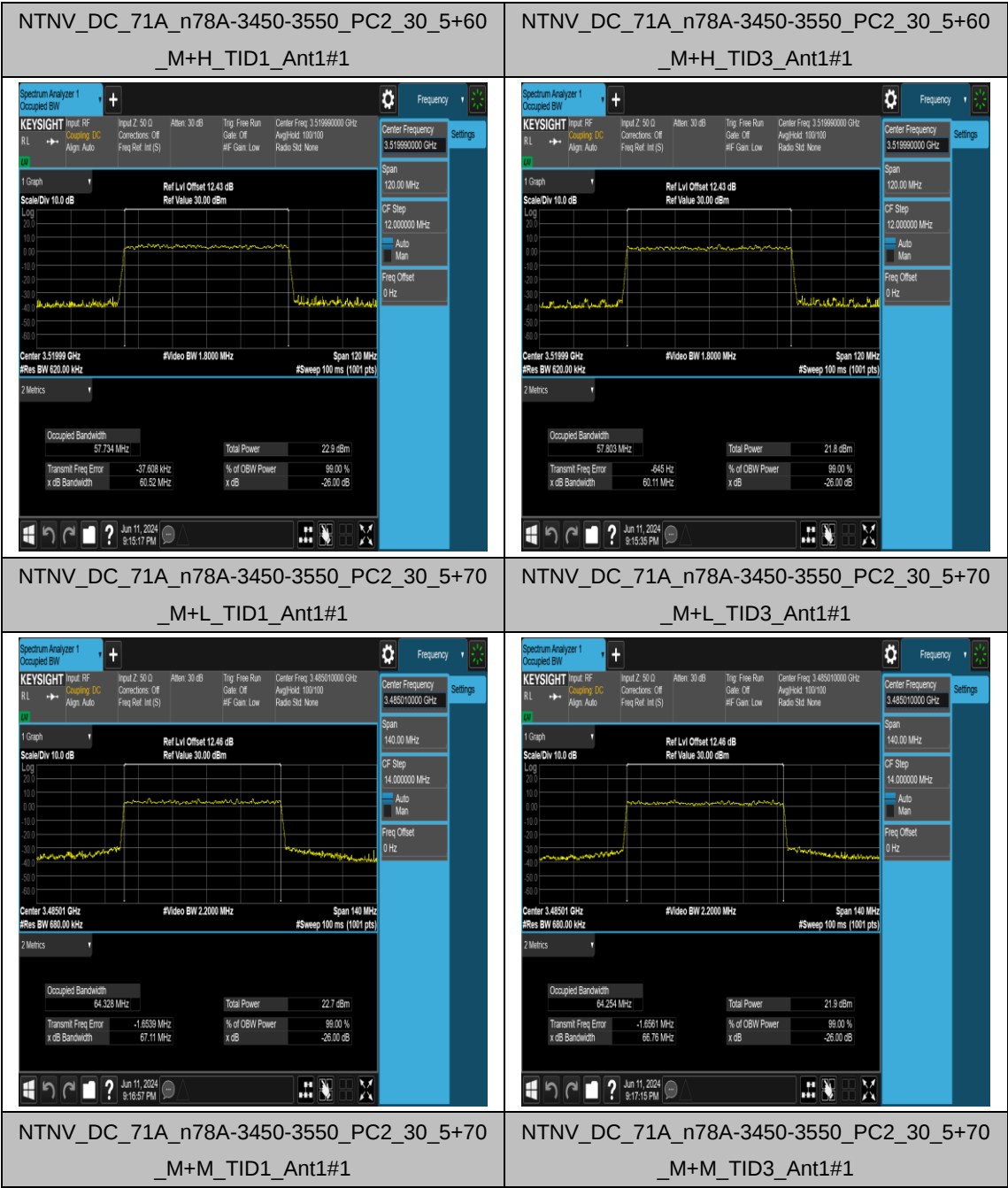


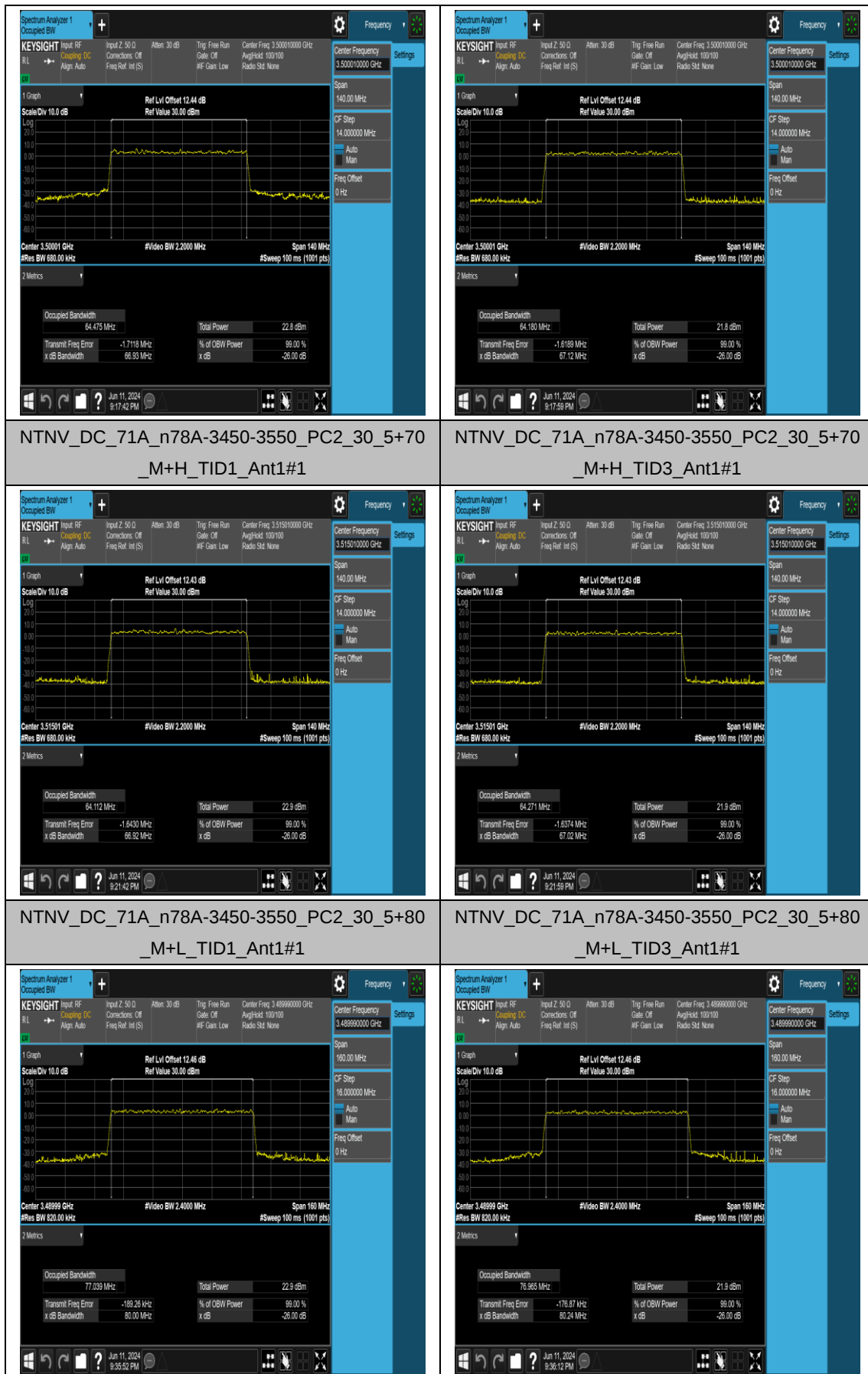
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_M+M_TID1_Ant1#1

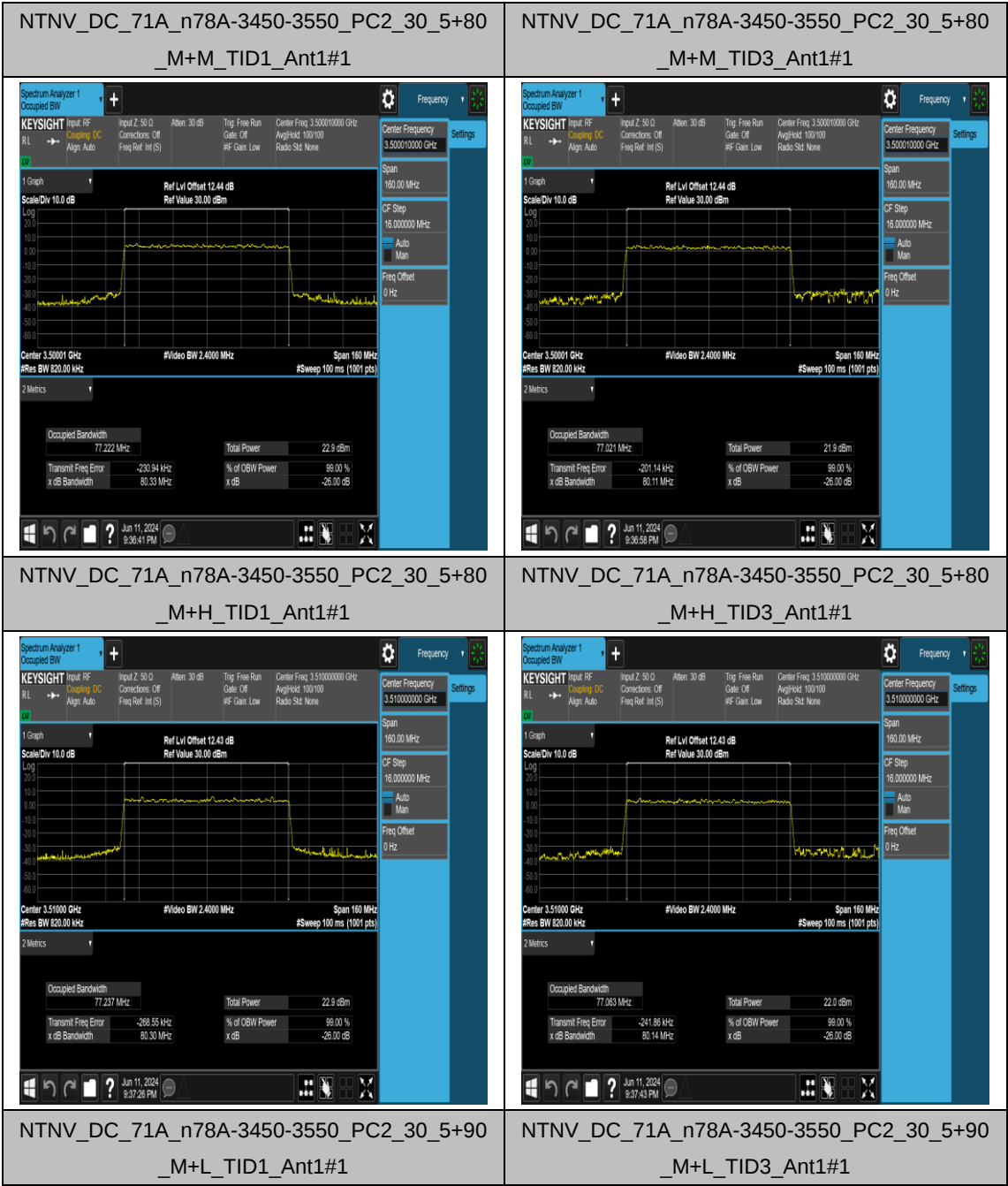


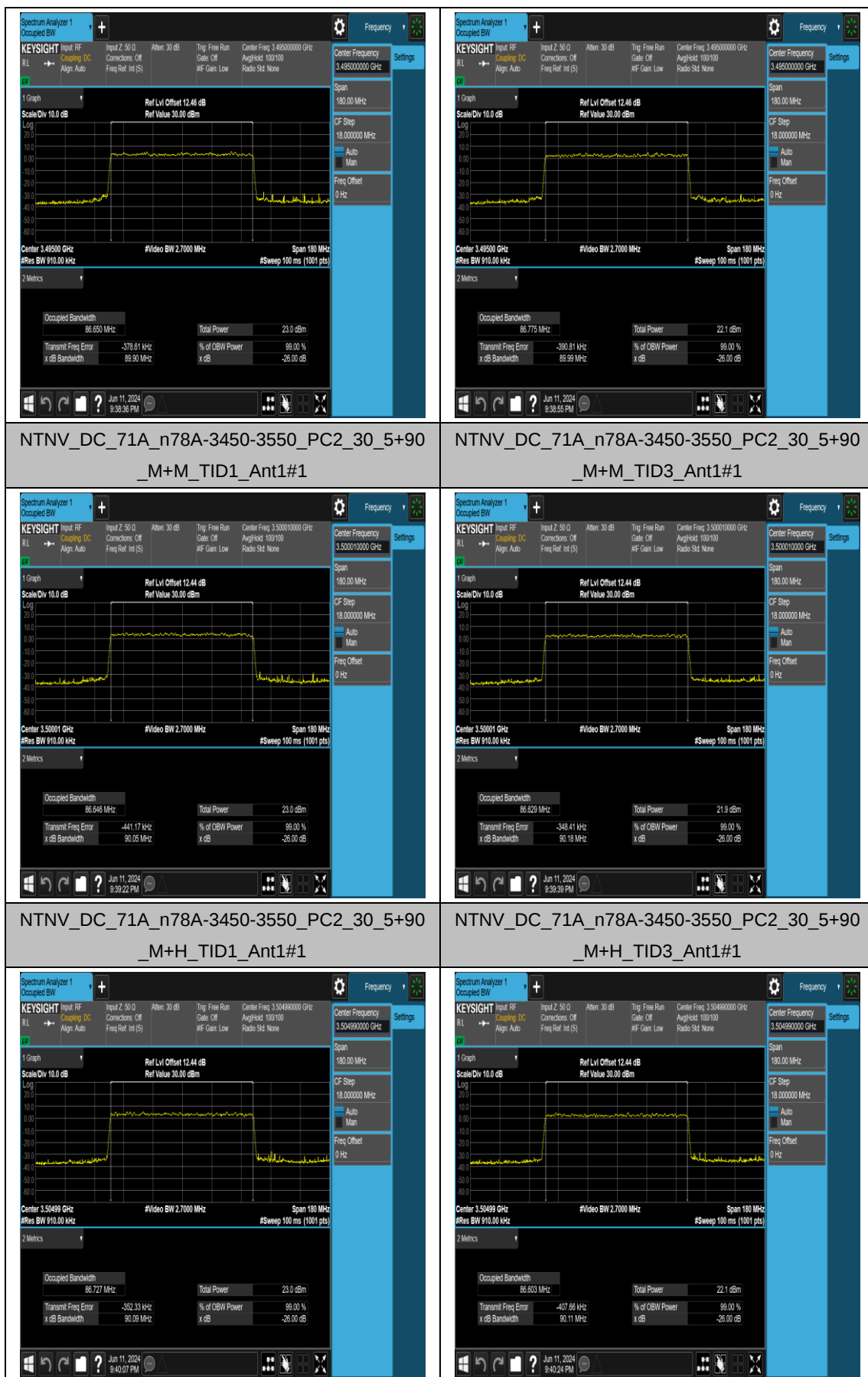
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_M+M_TID3_Ant1#1

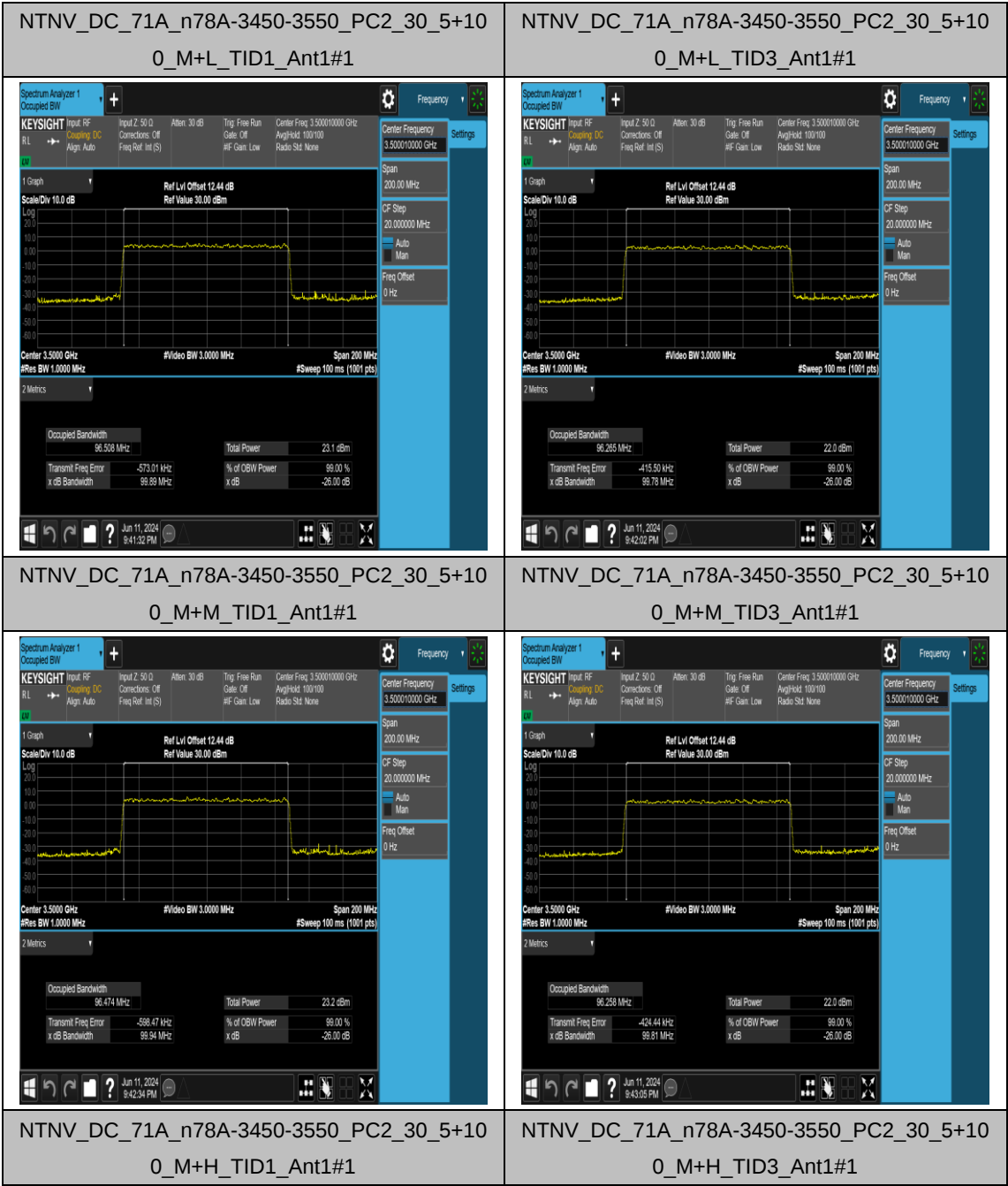














Appendix D: Band Edge for NSA

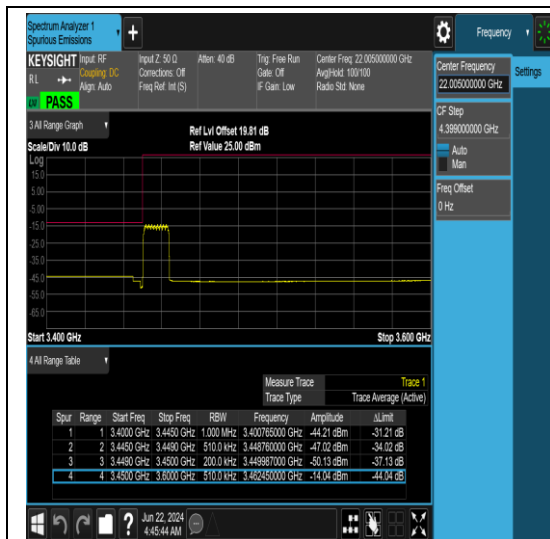
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	-44.20	31.20	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-44.22	31.22	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	-43.54	30.54	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-43.99	30.99	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	-44.21	31.21	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-44.21	31.21	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	-43.72	30.72	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-42.79	29.79	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	-44.21	31.21	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-44.20	31.20	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	-43.82	30.82	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-43.27	30.27	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	-44.13	31.13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	-44.03	31.03	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	-43.82	30.82	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	-42.84	29.84	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	-44.14	31.14	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	-44.08	31.08	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	-43.91	30.91	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	-43.06	30.06	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	-44.01	31.01	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	-43.72	30.72	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	-43.79	30.79	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	-42.51	29.51	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	-43.99	30.99	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	-43.83	30.83	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	-43.81	30.81	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	-42.65	29.65	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	-44.03	31.03	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	-43.69	30.69	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	-43.59	30.59	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	-41.88	28.88	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	-44.01	31.01	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	-43.46	30.46	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	-43.63	30.63	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	-41.21	28.21	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	-42.24	29.24	PASS

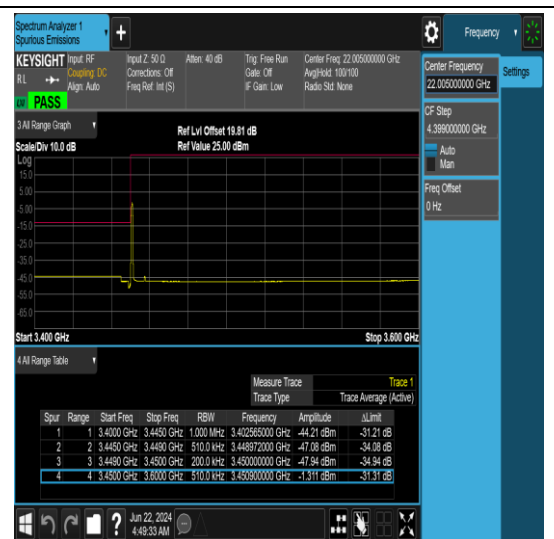
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	-37.34	24.34	PASS
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Test Graphs

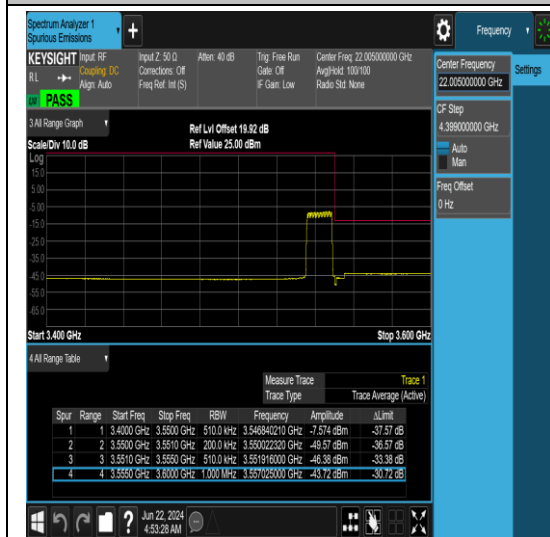




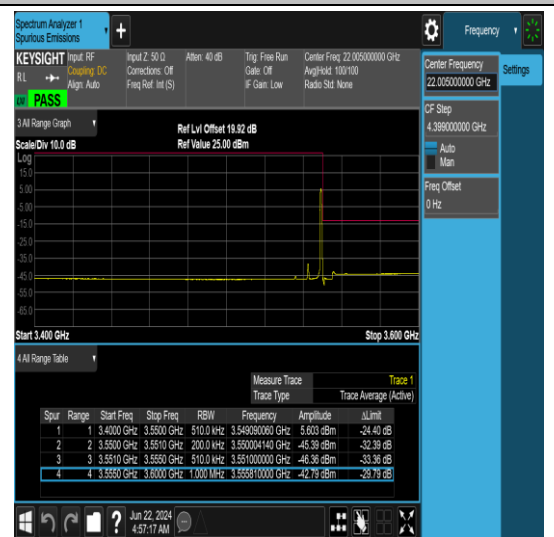
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15
_M+H_TID9_#1



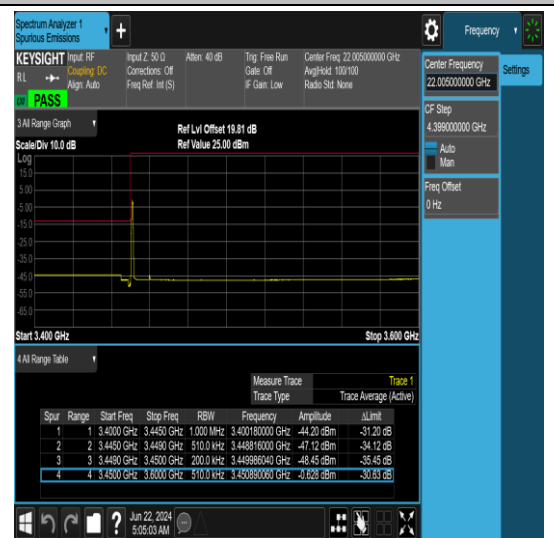
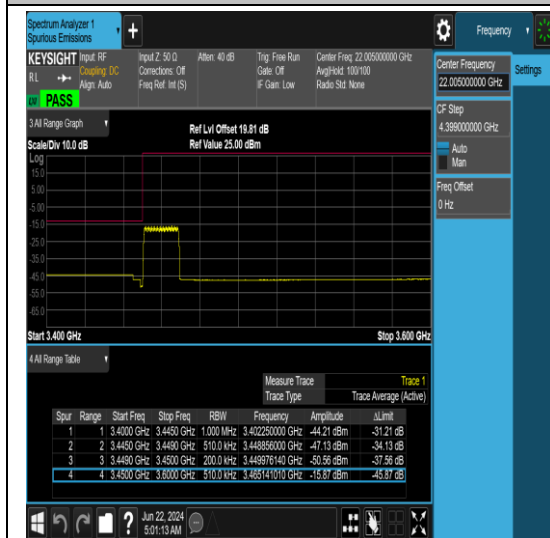
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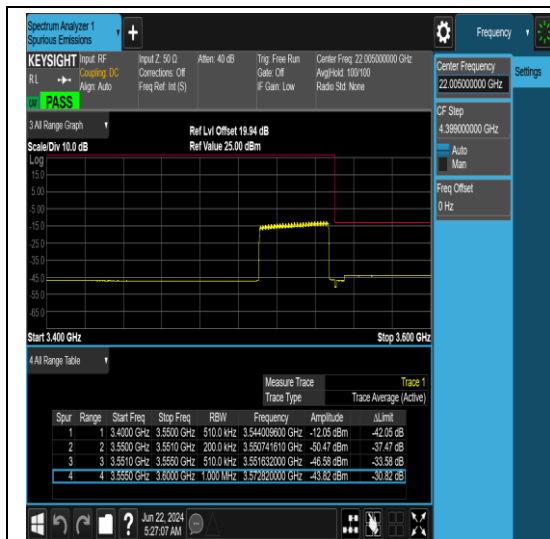
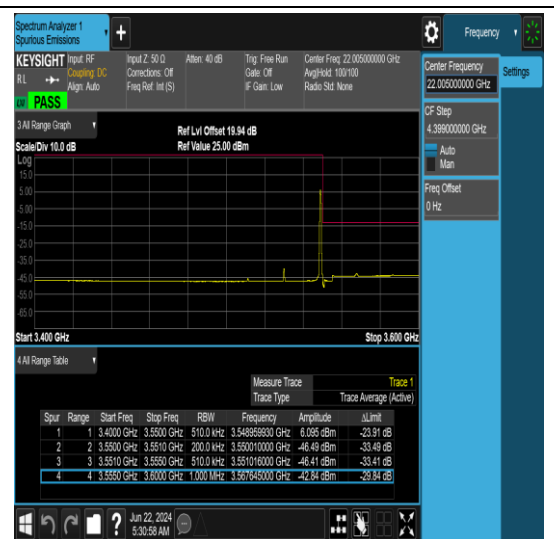
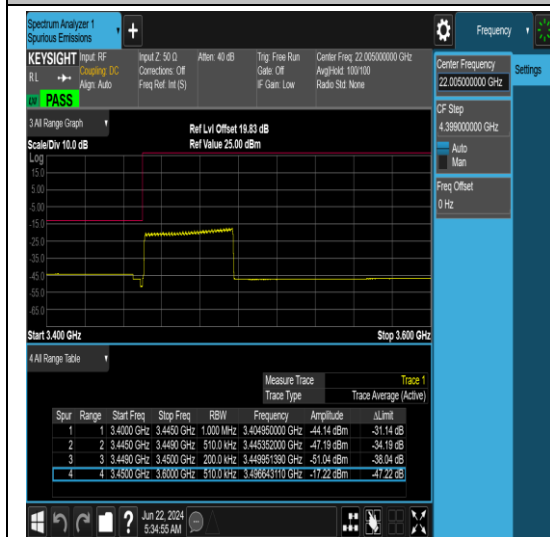
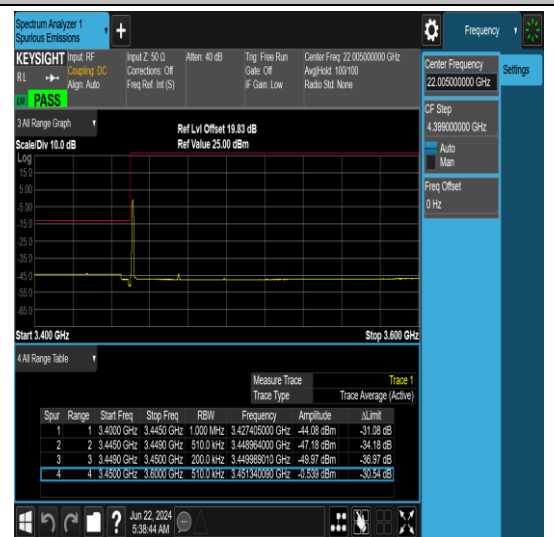
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_M+L_TID9_#1



NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20
_M+L_TID7_#1





NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50
_M+L_TID9_#1NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50
_M+L_TID7_#1NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50
_M+H_TID9_#1NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50
_M+H_TID8_#1