

Appendix for 71A_N2A

Product Name: 5G Indoor CPE

Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	20.82	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Inner_Full	21.48	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	20.95	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	20.79	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	19.74	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Inner_Full	20.68	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	19.52	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	19.50	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	20.50	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Inner_Full	21.35	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	20.47	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	20.09	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	19.38	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Inner_Full	20.48	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	19.42	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	19.35	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	18.85	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Inner_Full	19.79	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	18.69	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	18.48	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	17.96	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Inner_Full	18.94	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	17.35	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	17.16	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	20.70	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Inner_Full	21.52	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.03	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	20.67	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	19.68	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Inner_Full	20.67	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.14	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.74	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	20.53	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Inner_Full	21.29	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.76	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.35	PC3	PASS

DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	19.46	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Inner_Full	20.51	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.71	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.46	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	18.91	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Inner_Full	19.71	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.25	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	18.76	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	17.96	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Inner_Full	19.06	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18.56	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	17.76	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	20.76	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Inner_Full	21.64	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	20.98	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	20.53	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	19.72	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Inner_Full	20.69	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.63	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.75	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	20.67	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Inner_Full	21.55	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.18	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.58	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	19.67	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Inner_Full	20.63	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	19.46	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	19.57	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	19.36	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Inner_Full	20.14	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.27	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	18.91	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	18.30	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Inner_Full	19.28	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	18.36	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	17.99	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	20.89	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Inner_Full	21.69	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	20.91	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	20.60	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	19.91	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Inner_Full	20.76	PC3	PASS

DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.88	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.64	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	20.55	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Inner_Full	21.41	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.90	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	19.94	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	19.53	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Inner_Full	20.65	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.99	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.05	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	19.53	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Inner_Full	20.26	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	20.09	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18.95	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	18.44	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Inner_Full	19.43	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.17	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	17.95	PC3	PASS

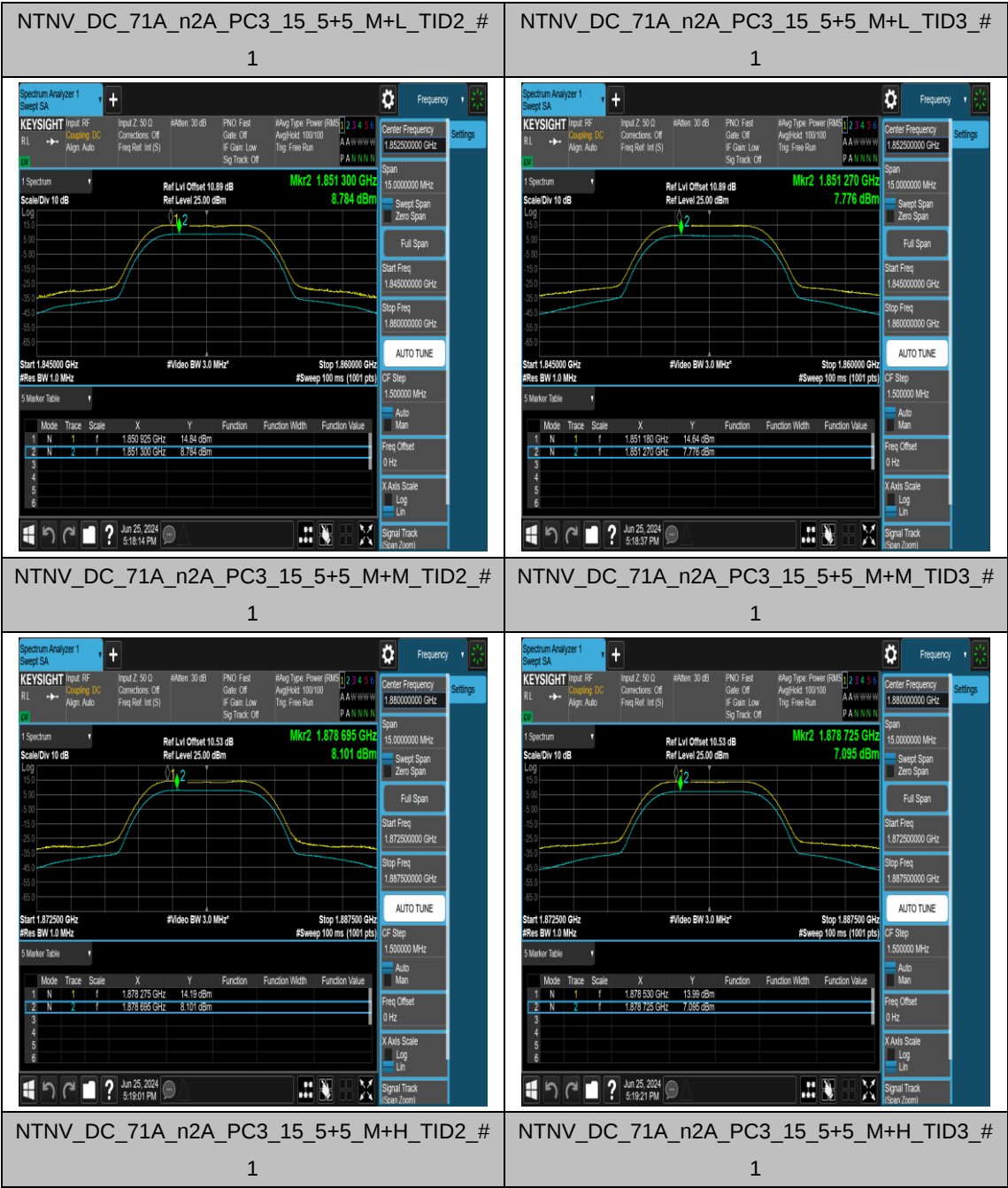
Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	100%	0.00	6.06	≤13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	100%	0.00	6.86	≤13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	100%	0.00	6.09	≤13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	100%	0.00	6.89	≤13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	100%	0.00	6.10	≤13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	100%	0.00	6.85	≤13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.06	≤13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.35	≤13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.00	≤13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.36	≤13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	6.96	≤13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.40	≤13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.48	≤13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.30	≤13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.61	≤13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.27	≤13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.54	≤13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.47	≤13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.55	≤13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.46	≤13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.60	≤13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.56	≤13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.73	≤13	PASS

Test Graphs



NTNV_DC_71A_n2A_PC3_15_5+5_M+M_TID2_#

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NTNV_DC_71A_n2A_PC3_15_5+5_M+M_TID3_#

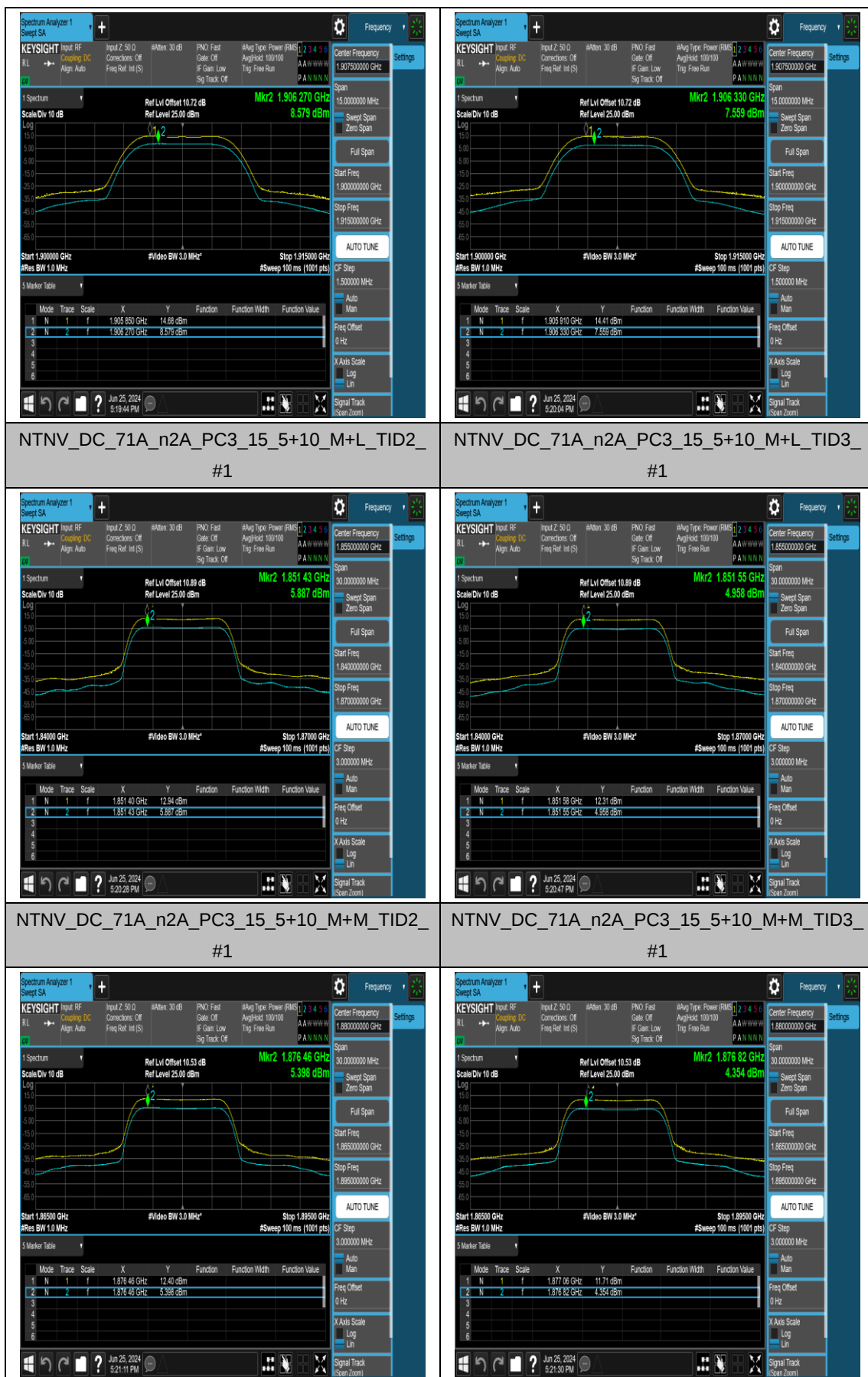
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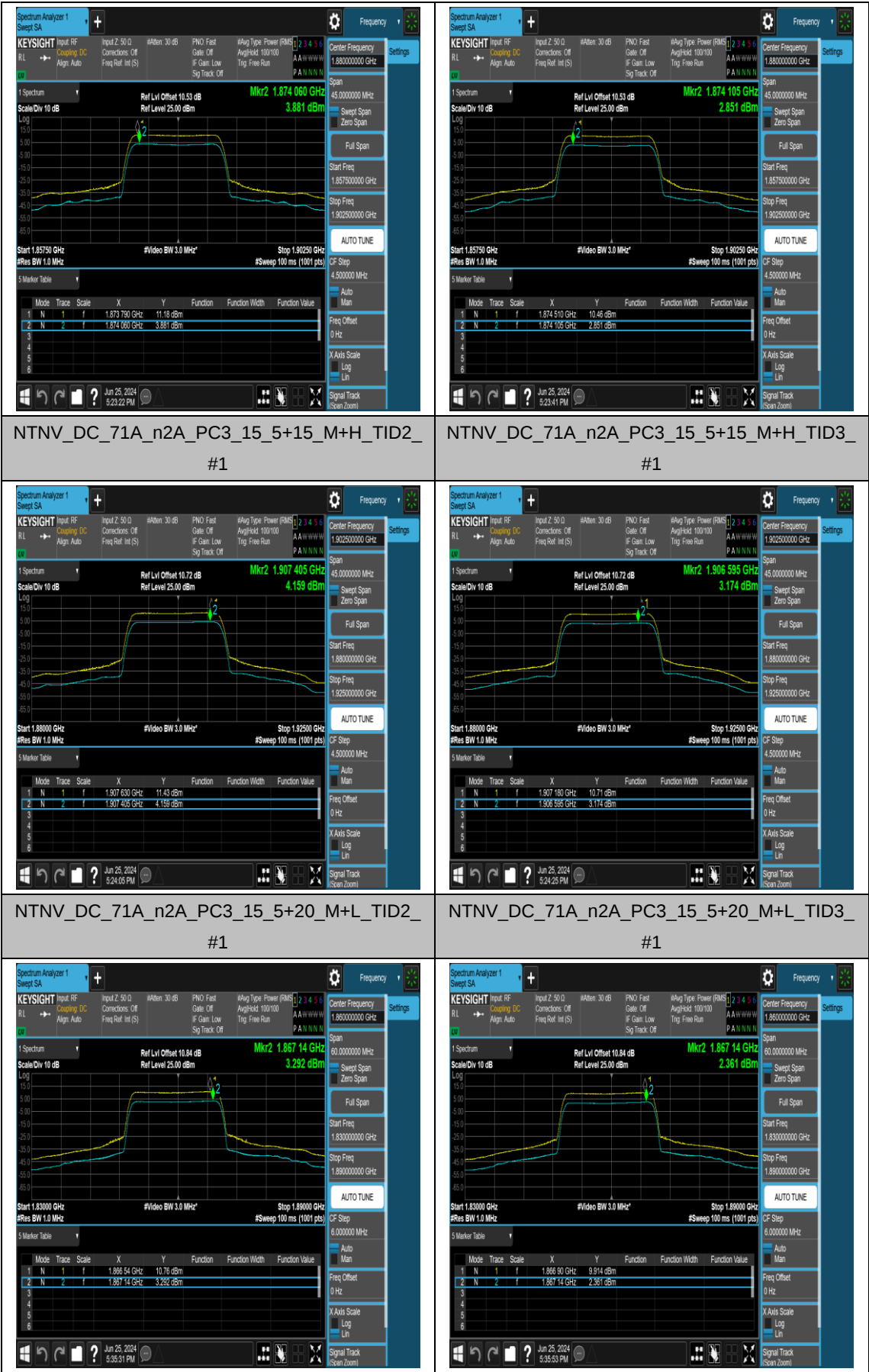
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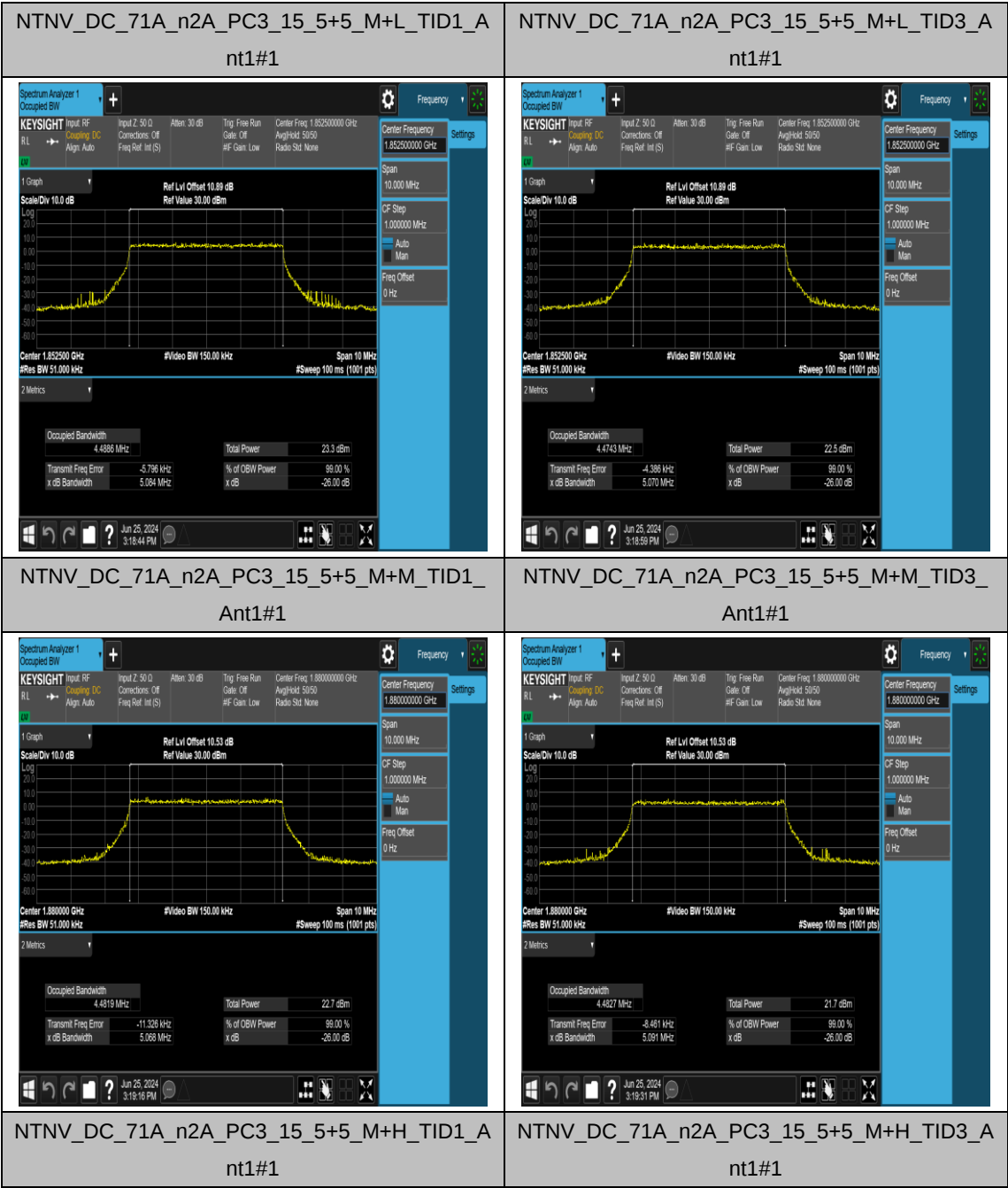
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

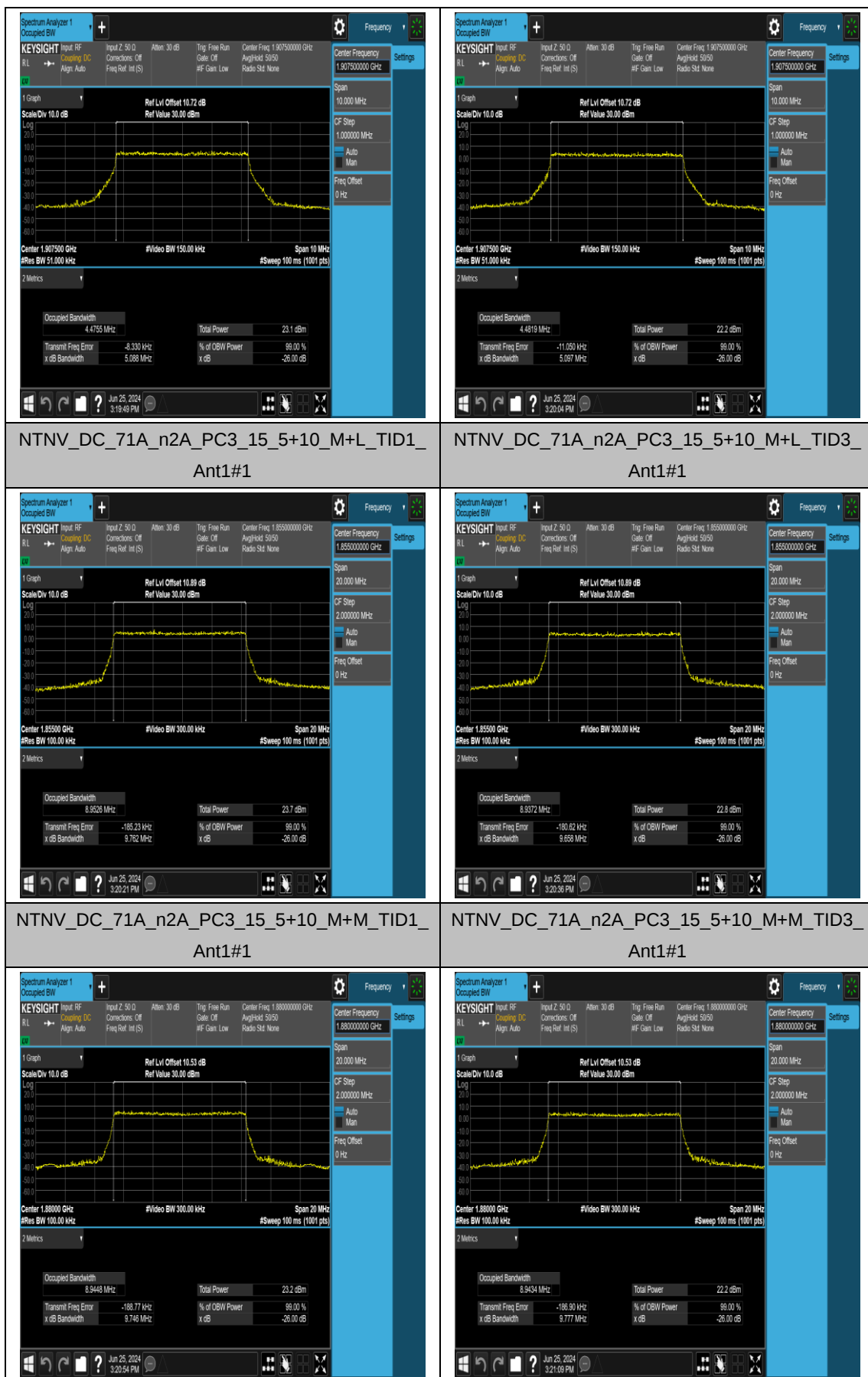
NSA

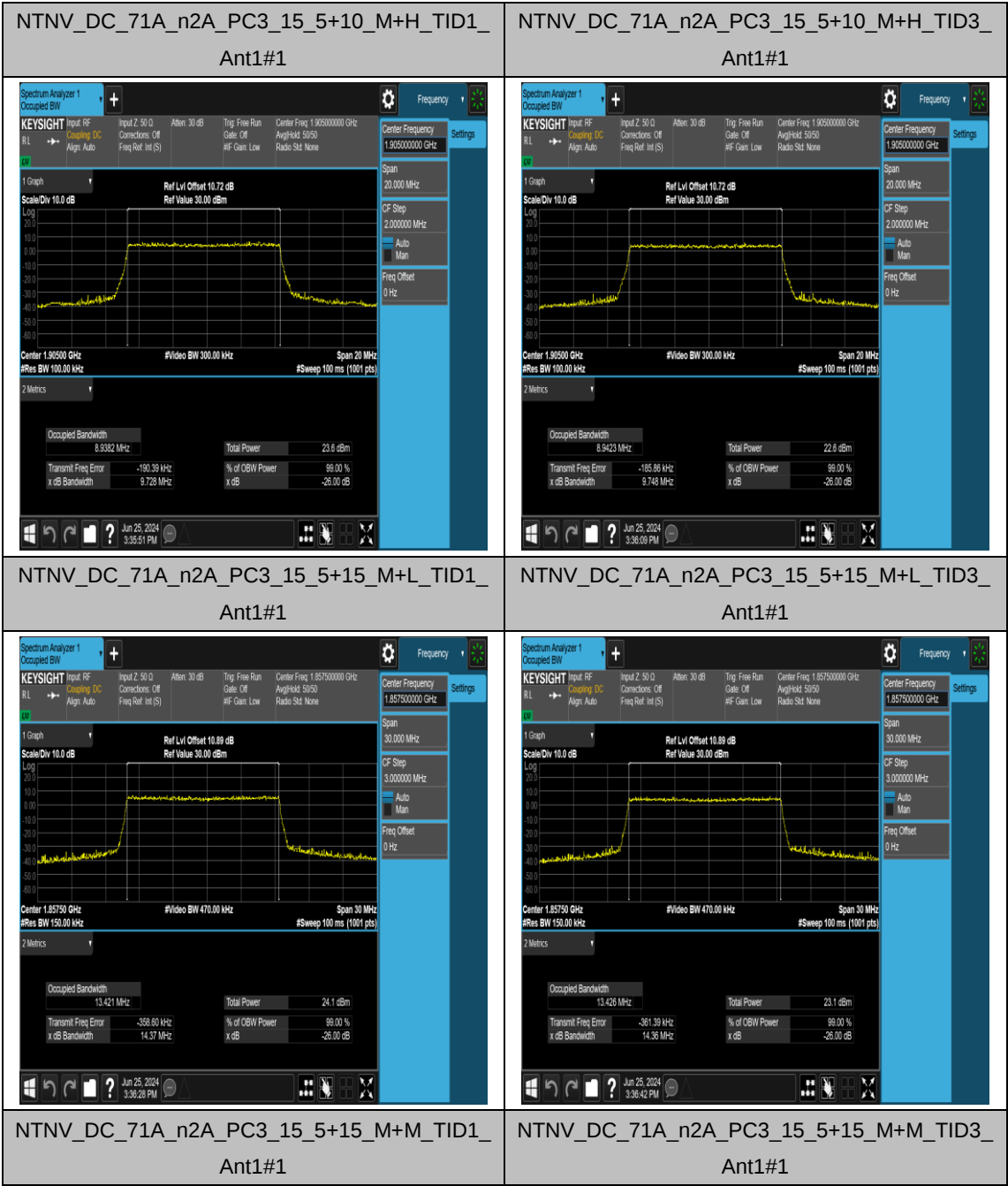
Test Result

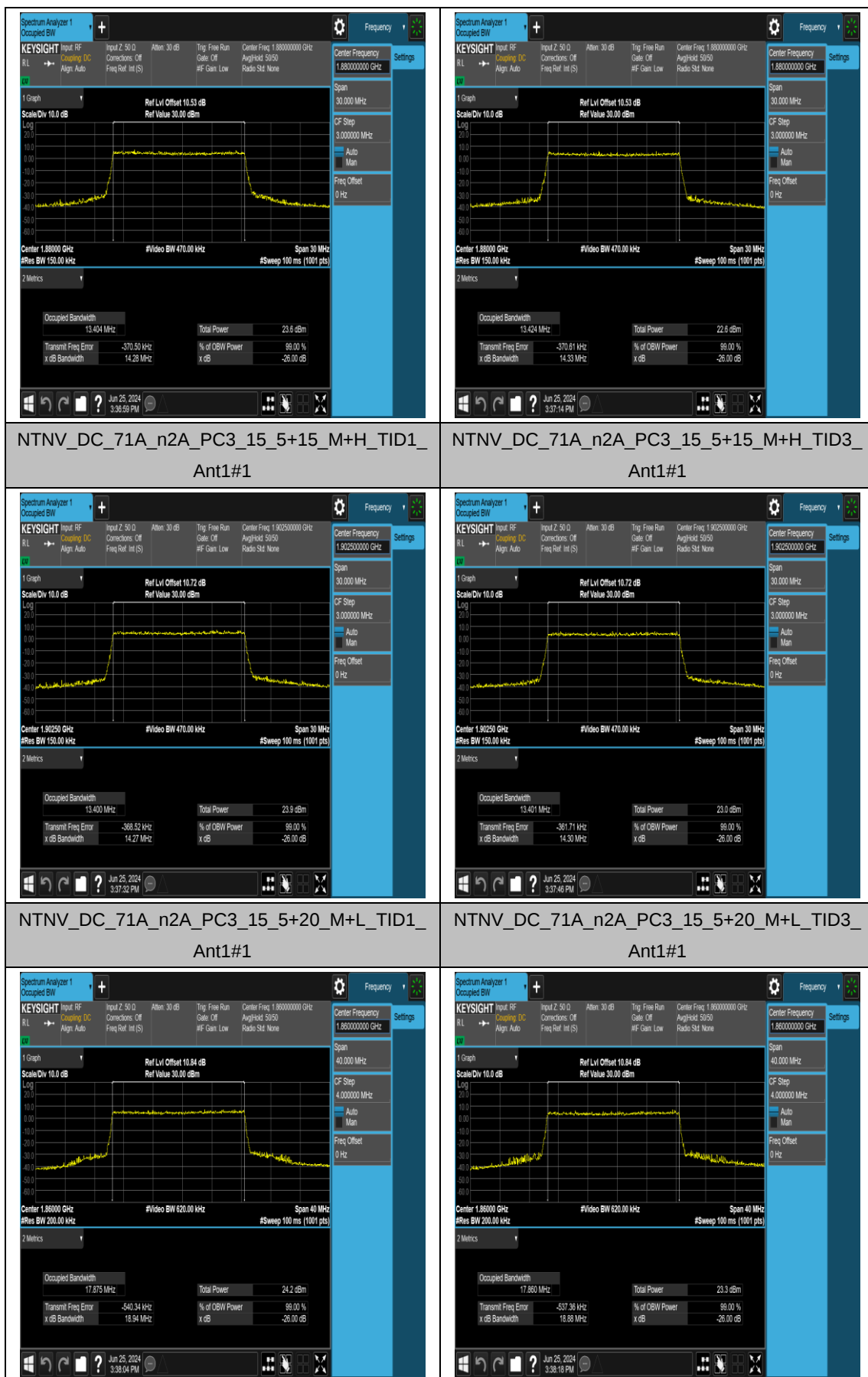
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	4.4886	5.084	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	4.4743	5.070	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.4819	5.068	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	4.4827	5.091	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	4.4755	5.088	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	4.4819	5.097	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	8.9526	9.762	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	8.9372	9.658	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	8.9448	9.746	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	8.9434	9.777	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	8.9382	9.728	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	8.9423	9.748	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	13.421	14.37	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	13.426	14.36	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	13.404	14.28	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	13.424	14.33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	13.400	14.27	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	13.401	14.30	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	17.875	18.94	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	17.860	18.88	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	17.872	18.93	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	17.897	18.98	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	17.869	18.94	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	17.887	18.84	PASS

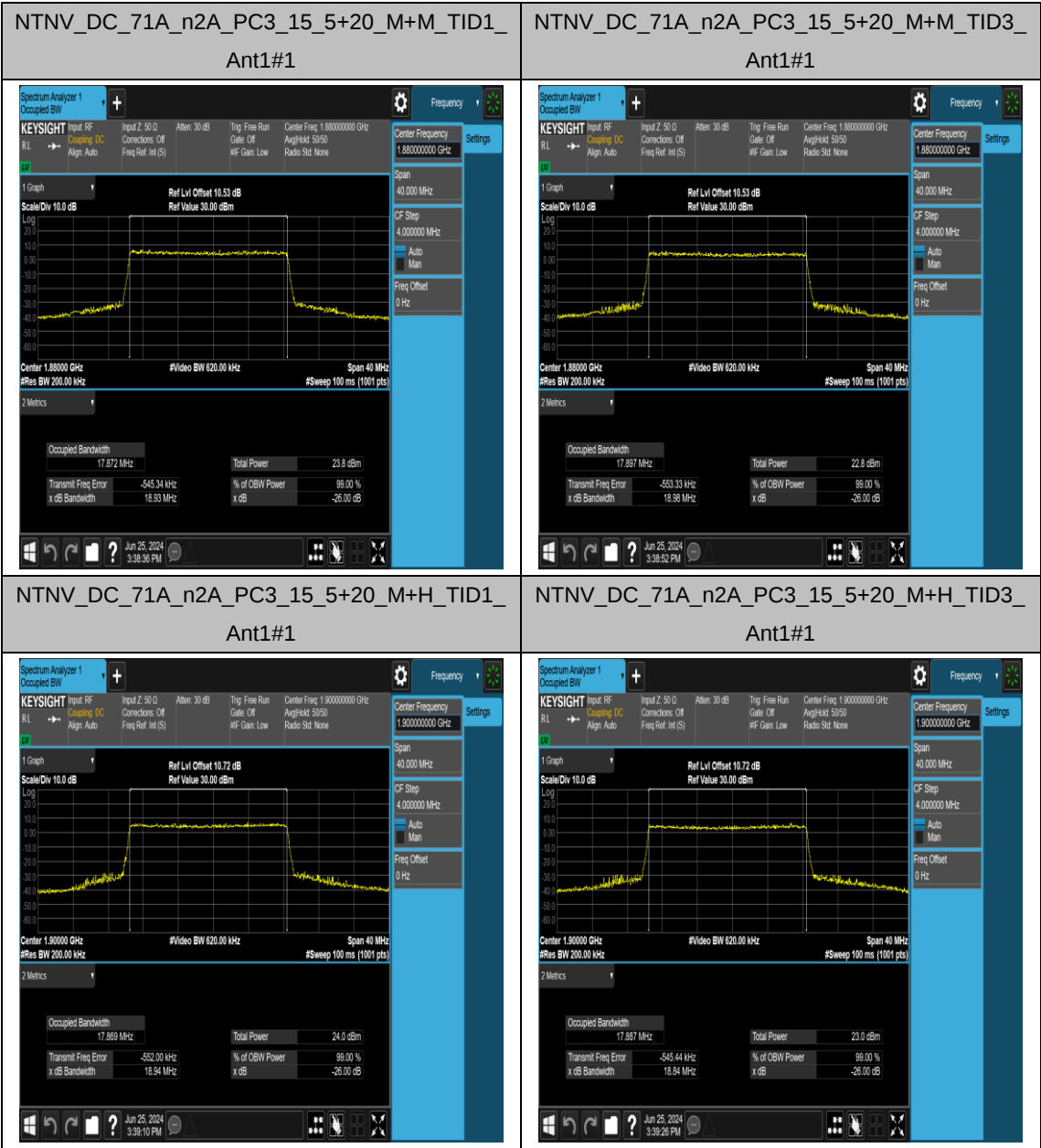
Test Graphs









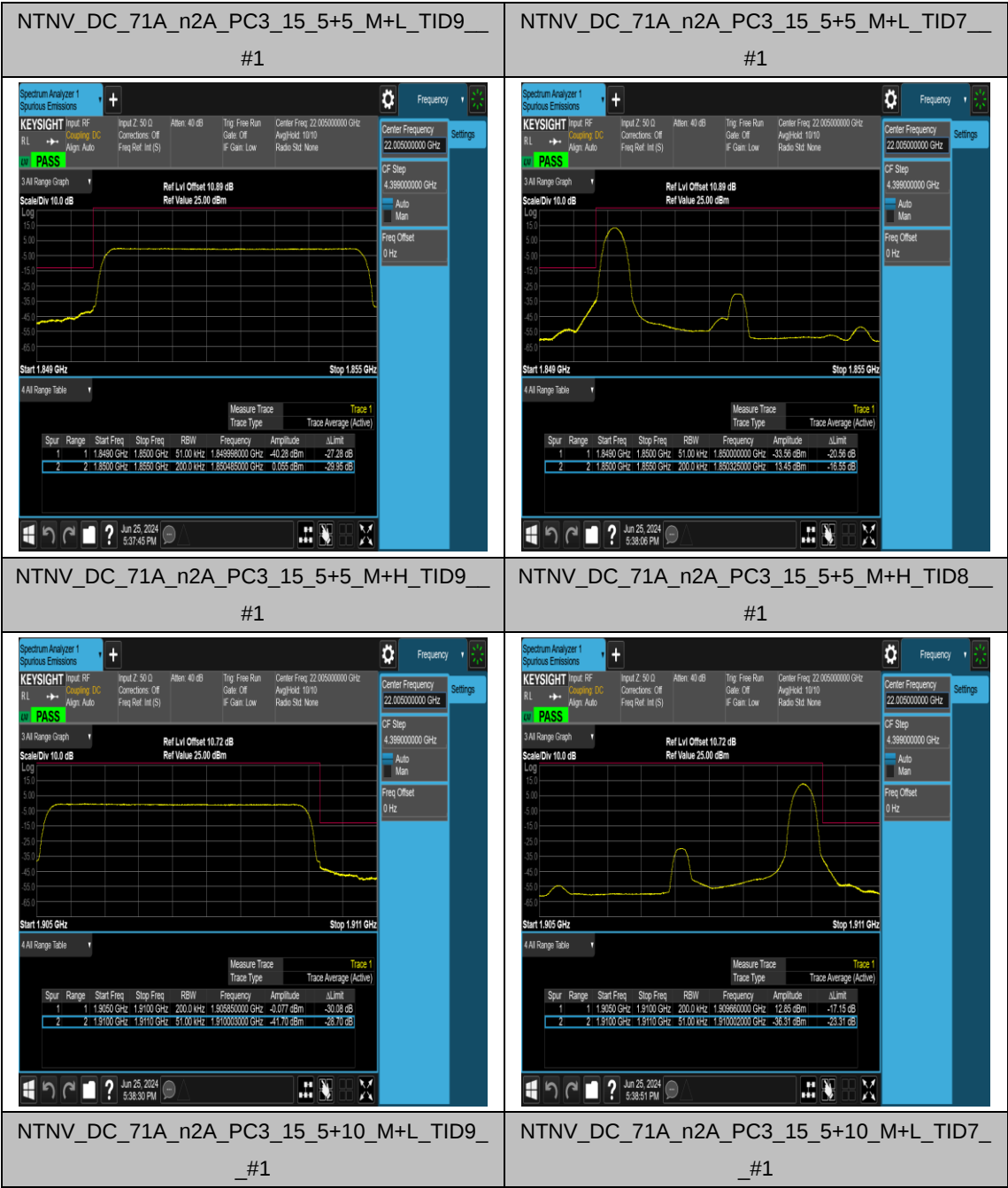


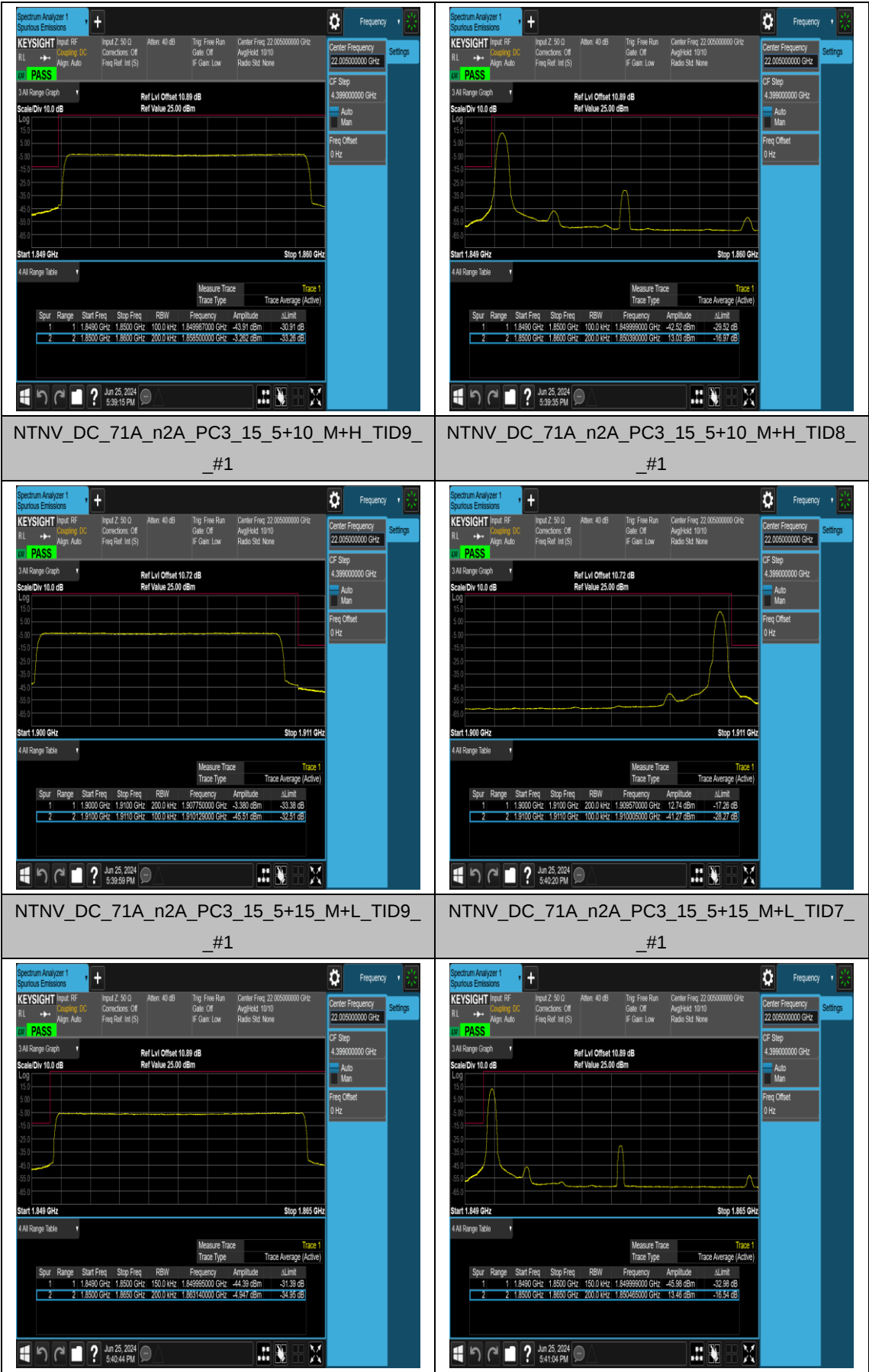
Appendix D: Band Edge for NSA

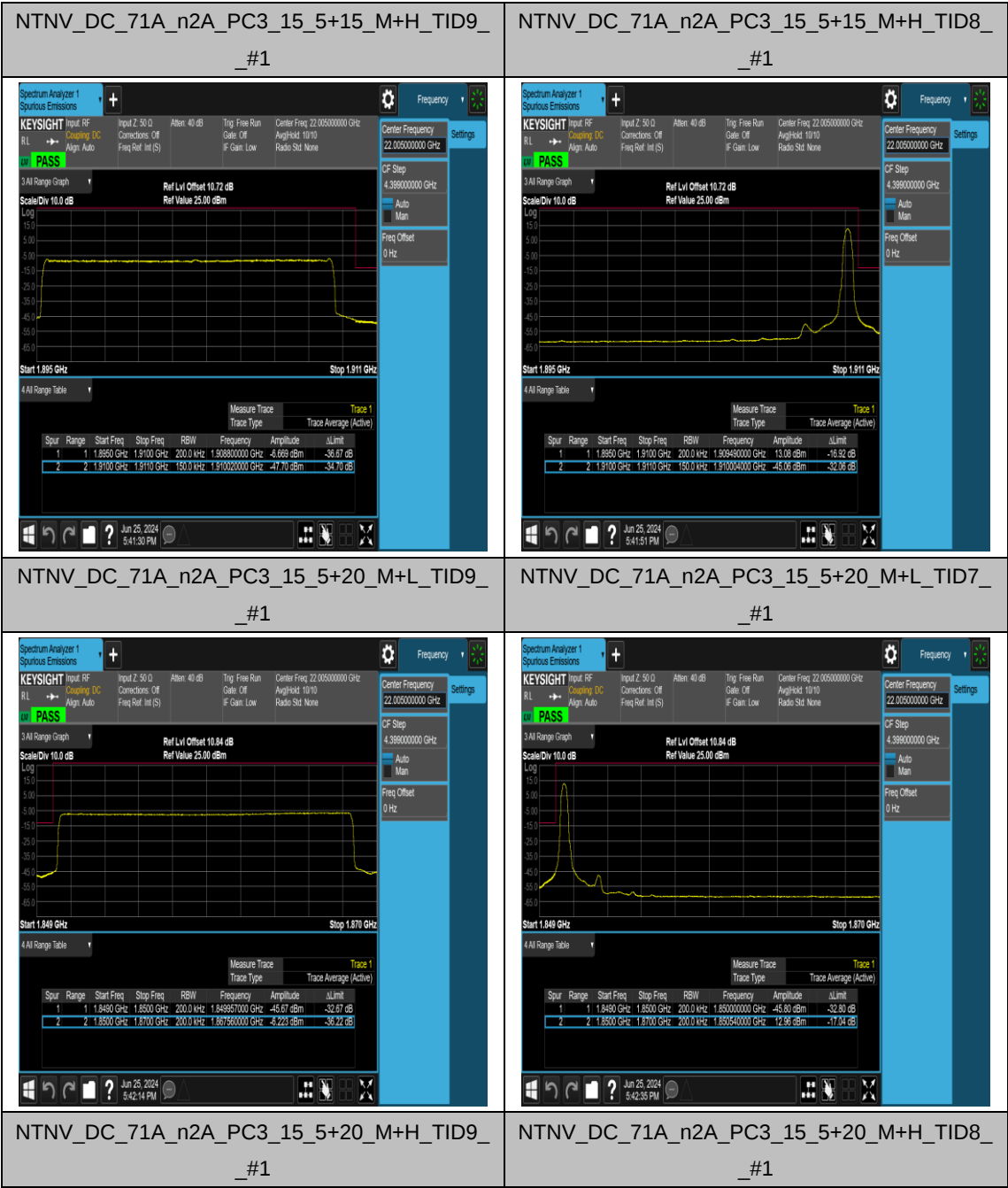
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	-40.28	27.28	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	-33.56	20.56	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	-41.70	28.70	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	-36.31	23.31	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	-43.91	30.91	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-42.52	29.52	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	-45.51	32.51	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-41.27	28.27	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	-44.39	31.39	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-45.98	32.98	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	-47.70	34.70	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-45.06	32.06	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	-45.67	32.67	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-45.80	32.80	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	-47.10	34.10	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-46.35	33.35	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-72.87	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.57	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	30	1000	-54.03	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	1000	3000	-34.19	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	3000	12000	-53.44	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	12000	20000	-55.26	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-72.51	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.88	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.37	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.69	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-53.49	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-55.29	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.47	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.58	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-58.25	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.00	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-53.49	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-55.26	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-72.22	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.15	30	-77.53	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	1000	3000	-33.69	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	3000	12000	-53.51	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	12000	20000	-55.31	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-73.08	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.77	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.11	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-34.18	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-53.33	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-55.30	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-72.06	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.71	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-58.51	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-31.89	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-49.56	-13	PASS

DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-55.32	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-72.97	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.45	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	30	1000	-55.32	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	1000	3000	-34.67	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	3000	12000	-53.57	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	12000	20000	-55.32	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-71.72	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.01	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.50	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-34.17	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-46.72	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-55.32	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.10	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.57	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.58	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-53.35	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-55.21	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-72.83	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.15	30	-82.63	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	30	1000	-55.74	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.40	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	3000	12000	-52.98	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	12000	20000	-55.40	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-72.39	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-78.40	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-55.14	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-47.67	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-53.61	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-55.37	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-71.40	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-79.24	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-58.88	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-47.71	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-53.46	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-55.28	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.11	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.11	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	30	1000	-55.25	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	1000	3000	-33.67	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	3000	12000	-45.48	-13	PASS

DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	12000	20000	-55.35	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.62	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.32	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.71	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-34.21	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-53.60	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-55.08	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.52	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.34	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.56	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.34	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.38	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-55.37	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-72.89	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.15	30	-81.52	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	30	1000	-57.68	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	1000	3000	-35.59	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.08	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	12000	20000	-55.25	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-72.68	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-77.74	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-55.22	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-47.73	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-53.37	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-55.44	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-71.77	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-79.28	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-56.36	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.46	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-53.52	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-55.15	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-72.27	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-77.28	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-59.26	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-29.46	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	3000	12000	-53.46	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	12000	20000	-55.22	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-72.50	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-77.27	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.23	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.76	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-45.94	-13	PASS

DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-55.31	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.92	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.61	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.39	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-33.97	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.43	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-55.22	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-71.95	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-79.23	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-54.49	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-35.13	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	3000	12000	-53.54	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	12000	20000	-55.35	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-72.74	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-77.93	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.21	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-33.42	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-53.51	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-55.36	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-72.44	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.84	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-56.45	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-33.55	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-53.62	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-55.40	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-72.34	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-80.61	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-56.31	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-34.55	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	3000	12000	-53.49	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	12000	20000	-55.35	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-72.62	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.35	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-58.38	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-33.51	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-53.42	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-55.31	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.07	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.95	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-58.07	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.93	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-53.55	-13	PASS

DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-55.30	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-72.08	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-80.30	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-55.19	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-47.70	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	3000	12000	-45.51	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	12000	20000	-55.13	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-72.70	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-79.32	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-59.22	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-34.33	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-53.51	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-55.37	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-72.36	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-78.49	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.99	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-47.66	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-53.59	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-55.32	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-71.83	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.14	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-56.23	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-33.31	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	3000	12000	-53.49	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	12000	20000	-55.31	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.43	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.67	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-25.58	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.65	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-53.50	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-55.27	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.60	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.10	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.05	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.51	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-53.48	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-55.33	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-72.60	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.96	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-54.84	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-33.42	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	3000	12000	-53.60	-13	PASS

DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	12000	20000	-55.27	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-72.05	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.93	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-55.70	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-45.78	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-53.48	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-55.36	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-71.78	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.49	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.46	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-33.15	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-45.44	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-55.28	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-71.93	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.91	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-55.84	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-33.32	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	3000	12000	-53.53	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	12000	20000	-55.40	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-72.24	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.13	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.80	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-33.76	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-53.57	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-55.41	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-71.58	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.22	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.29	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-53.55	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-55.33	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-71.88	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-79.19	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-58.46	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-36.37	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	3000	12000	-47.06	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	12000	20000	-55.15	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-70.73	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-77.37	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.68	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-33.38	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-45.80	-13	PASS

DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-55.27	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-73.06	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-77.89	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-55.37	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-47.63	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-53.59	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-55.38	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-72.25	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.63	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-55.47	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-33.39	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	3000	12000	-53.56	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	12000	20000	-55.08	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-73.07	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.40	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-58.88	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-30.44	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-53.54	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-55.40	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-71.21	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.11	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.07	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.98	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-53.56	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-55.21	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-70.26	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-80.71	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-56.77	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-47.73	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	3000	12000	-53.51	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	12000	20000	-55.26	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-72.92	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-77.14	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-55.00	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-47.70	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-53.34	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-55.27	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-72.70	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-78.85	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-55.69	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-45.02	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-53.49	-13	PASS

DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-55.32	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-71.98	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-78.18	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-55.26	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-34.03	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	3000	12000	-53.44	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	12000	20000	-55.37	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.49	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-78.96	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.60	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.74	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-53.09	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-55.24	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.56	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.06	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-53.57	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-55.37	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-72.92	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-80.31	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-58.58	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-37.00	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	3000	12000	-53.63	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	12000	20000	-55.15	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-72.42	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-78.25	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.92	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-46.26	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-53.44	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-55.14	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-71.46	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-77.38	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-57.74	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-41.29	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-53.36	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-55.19	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-73.04	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.61	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-53.73	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-30.39	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-53.62	-13	PASS

DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	12000	20000	-55.28	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-72.09	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.17	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.13	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-43.45	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-53.51	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-55.38	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.40	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.84	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.64	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-33.62	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-53.47	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-55.31	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-72.62	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-81.85	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-54.26	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-37.84	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	3000	12000	-53.60	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	12000	20000	-55.11	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-72.53	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-77.89	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-59.52	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-44.34	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-53.48	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-55.24	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-72.14	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.71	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-56.20	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-47.64	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-46.83	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-55.42	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-72.33	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.90	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-55.52	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-47.78	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-53.63	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-55.36	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-72.59	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.08	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.66	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-33.13	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-53.55	-13	PASS

DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-55.17	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-71.81	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.77	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.35	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-34.51	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-53.50	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-55.26	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-71.73	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-81.63	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-55.30	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-47.68	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	3000	12000	-53.60	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	12000	20000	-55.27	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-71.75	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-79.40	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-58.05	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-47.71	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-53.52	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-55.29	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-72.70	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-78.98	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-55.63	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-33.17	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-53.51	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-55.25	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-72.11	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.79	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-54.62	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-33.35	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-53.53	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-55.30	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-71.59	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.50	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.41	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.64	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-53.56	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-55.25	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.29	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.78	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.80	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.73	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-53.67	-13	PASS