

Appendix for 14A_N30A

Product Name: 5G Indoor CPE

Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	22.20	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Inner_Full	22.29	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	22.13	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.52	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	22.25	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Inner_Full	22.41	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	22.36	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	22.53	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	22.31	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Inner_Full	22.28	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	22.51	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	22.35	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	22.33	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Inner_Full	22.36	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	22.23	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	22.06	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	22.39	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Inner_Full	22.35	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	22.33	PC3	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	22.37	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	22.34	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Inner_Full	22.45	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	22.14	PC3	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	22.86	PC3	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Outer_Full	22.27	PC3	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Inner_Full	22.28	PC3	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.30	PC3	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.28	PC3	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	22.33	PC3	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Inner_Full	22.21	PC3	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.28	PC3	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.21	PC3	PASS

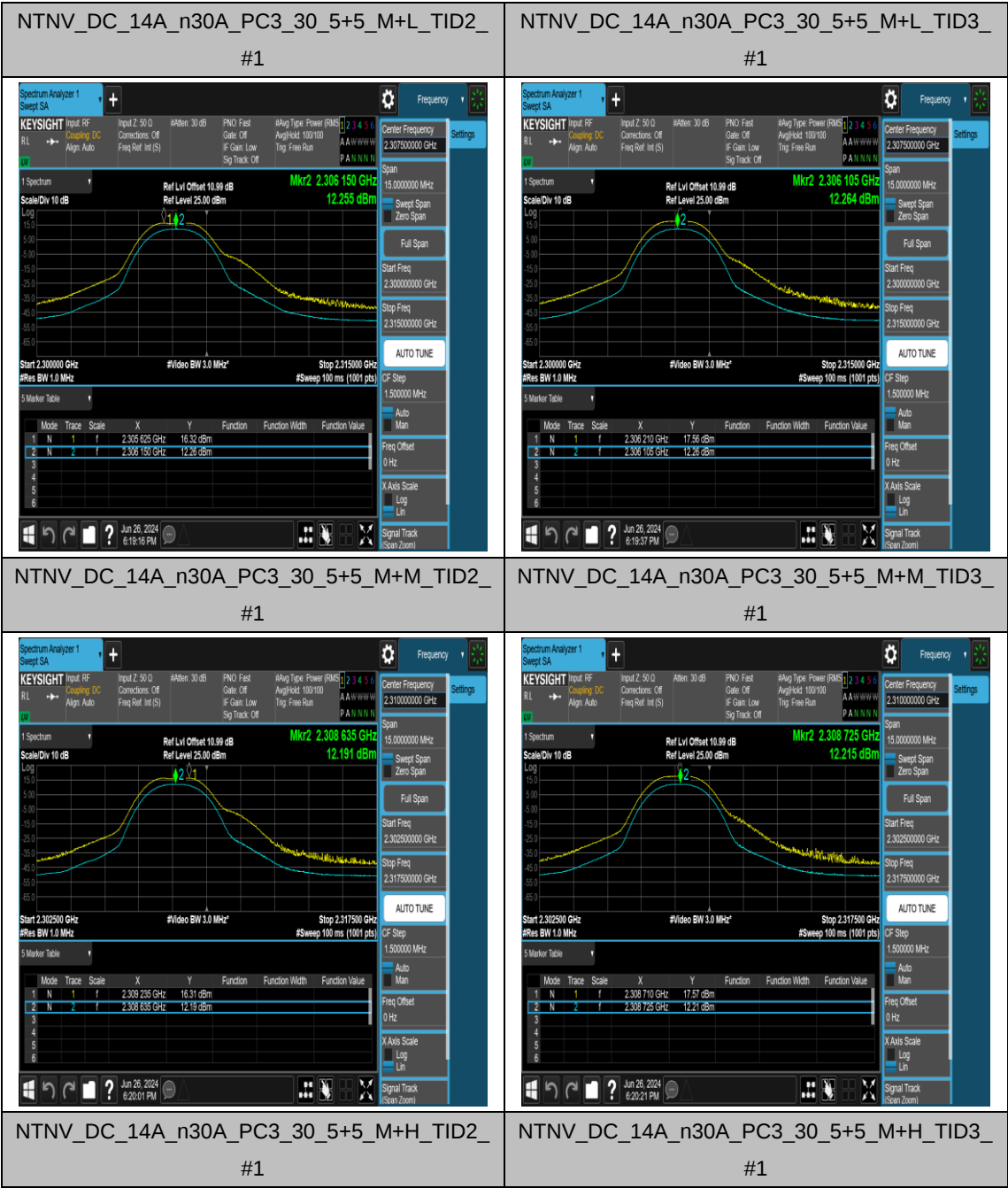
Appendix B: Peak-to-Average Ratio for NSA

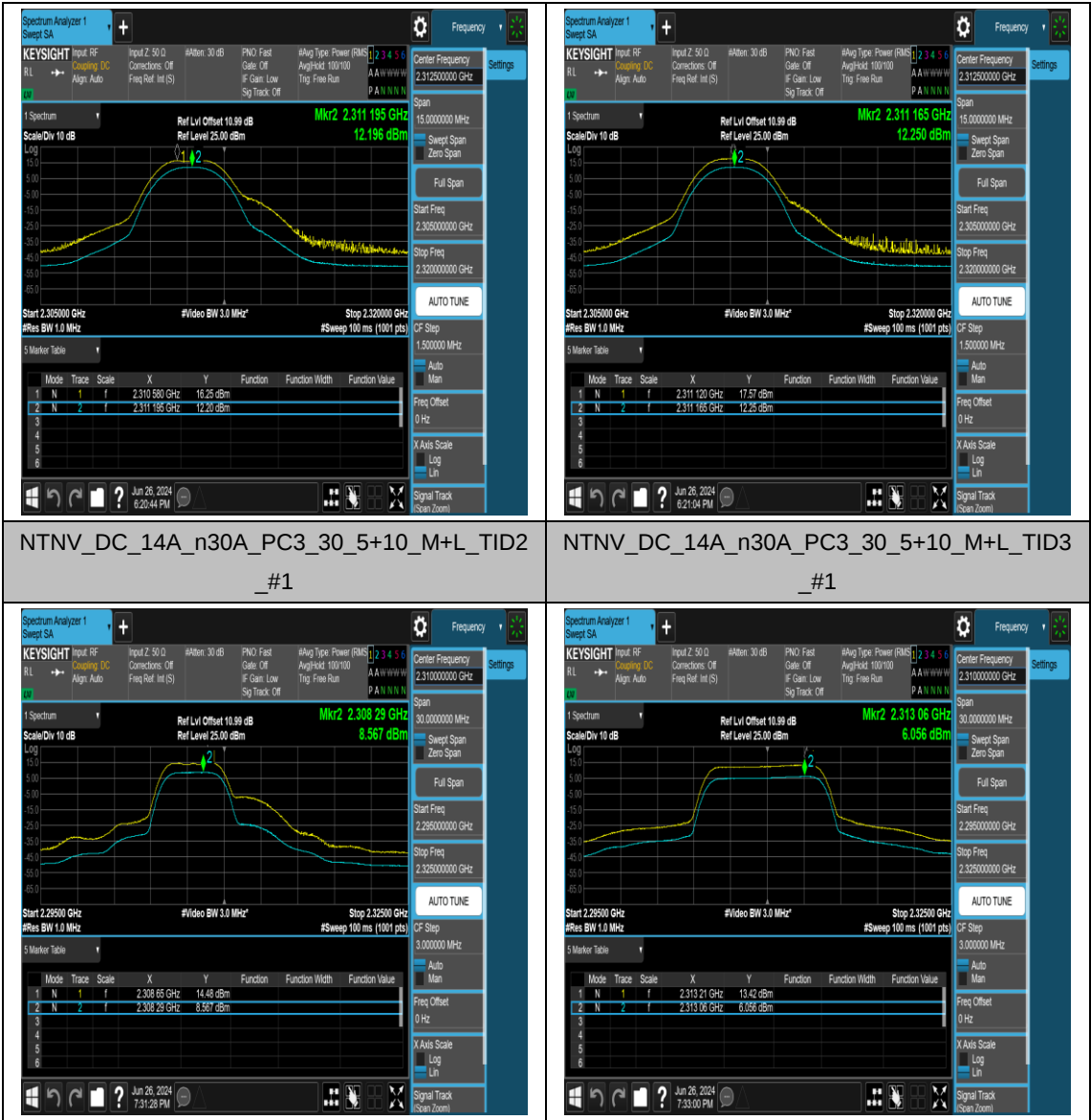
Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	100%	0.00	4.06	≤13	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	100%	0.00	5.30	≤13	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	100%	0.00	4.12	≤13	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	100%	0.00	5.35	≤13	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	100%	0.00	4.05	≤13	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	100%	0.00	5.32	≤13	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	5.91	≤13	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.36	≤13	PASS

Test Graphs



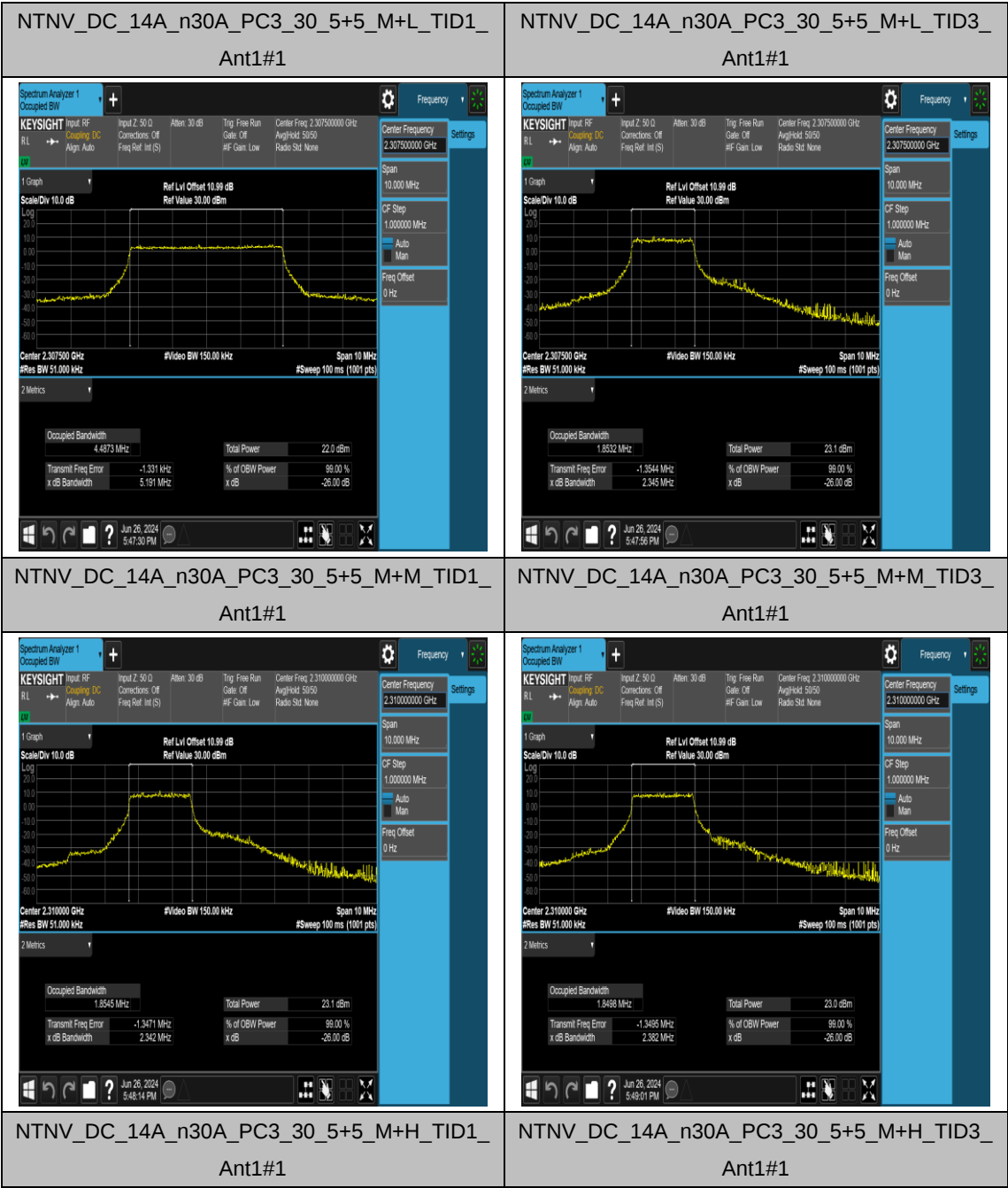


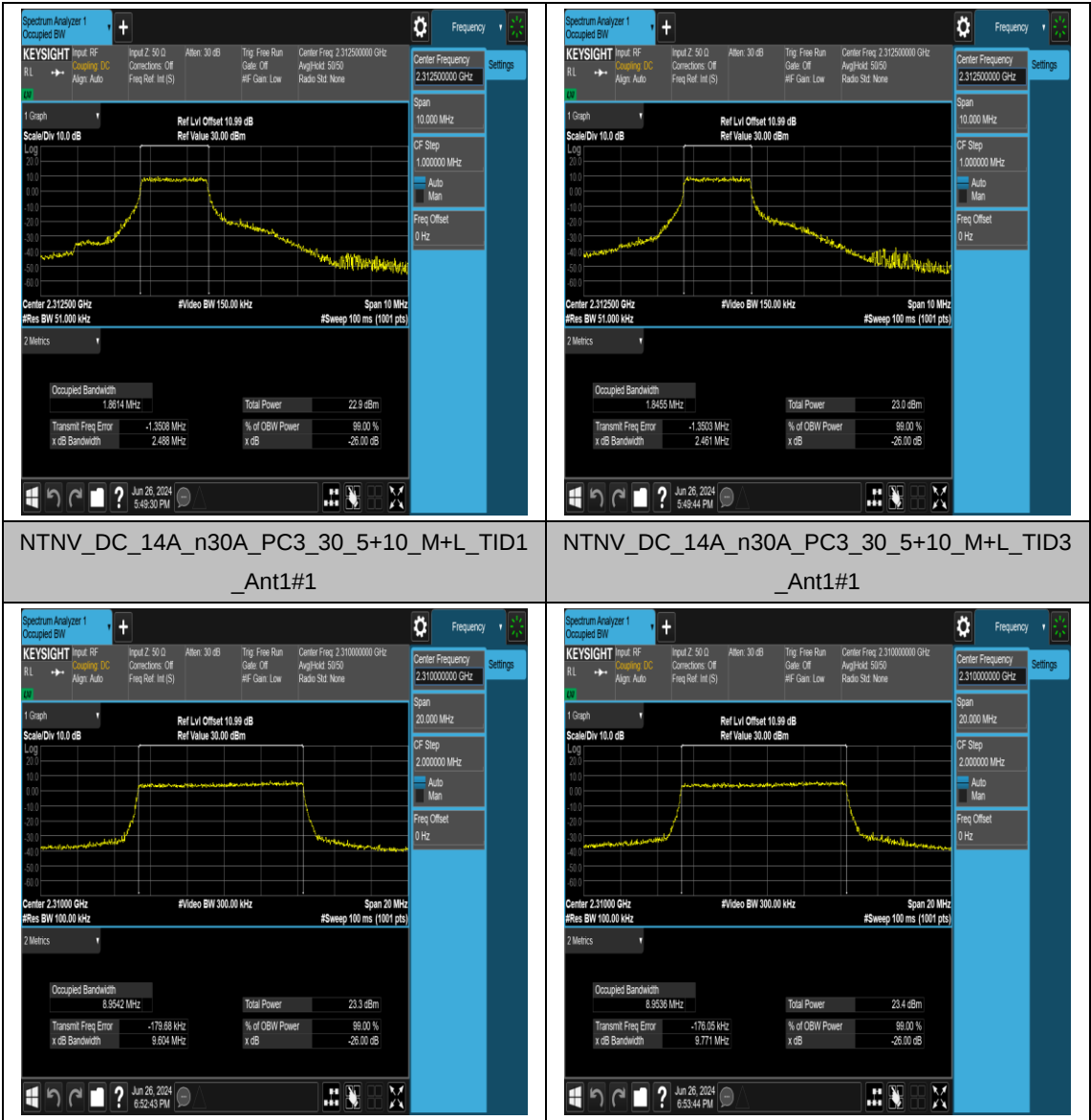
Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	4.4873	5.191	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	1.8532	2.345	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	1.8545	2.342	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	1.8498	2.382	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	1.8614	2.488	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	1.8455	2.461	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Outer_Full	8.9542	9.604	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	8.9536	9.771	PASS

Test Graphs



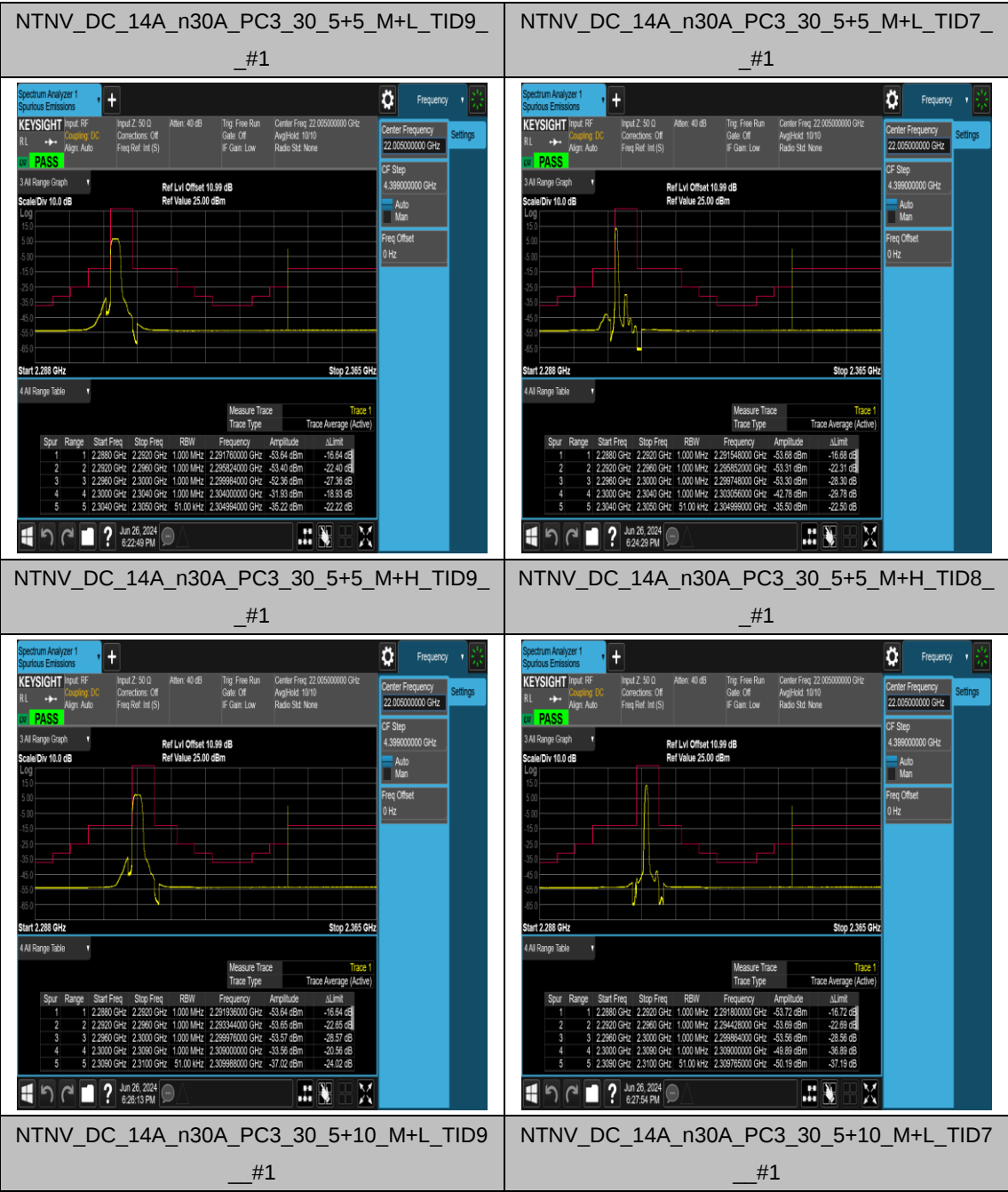


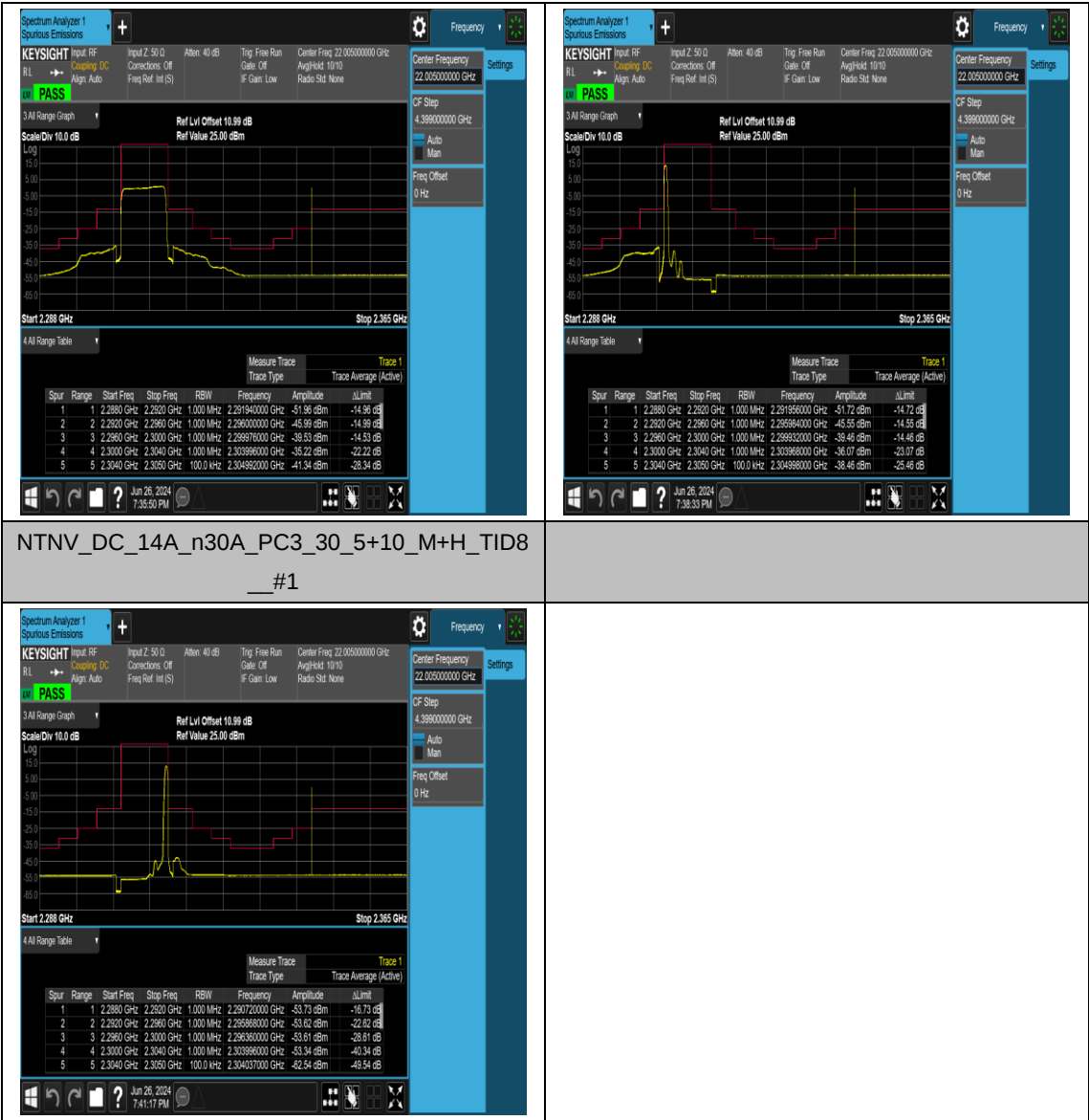
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	-53.37	16.37	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	-53.42	16.42	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	-53.41	16.41	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	-53.45	16.45	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Outer_Full	-50.44	13.44	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-39.46	14.46	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-53.49	16.49	PASS

Test Graphs





Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-71.94	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	0.15	30	-72.54	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	30	1000	-52.75	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	1000	3000	-57.66	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	3000	12000	-53.61	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Outer_Full	12000	20000	-55.36	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-75.88	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-80.79	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.22	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-57.69	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-53.59	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-55.21	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.77	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.58	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.20	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-54.58	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-53.68	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-55.34	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-72.39	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	0.15	30	-72.04	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	30	1000	-52.46	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	1000	3000	-57.69	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	3000	12000	-53.66	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Outer_Full	12000	20000	-55.44	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-71.71	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.25	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-53.03	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-57.77	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-53.63	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-55.28	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-71.98	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.09	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-57.14	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-56.49	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-53.54	-40	PASS

DC_14A_n30A	30	5+5	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-55.35	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-72.96	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.97	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	30	1000	-52.97	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	1000	3000	-51.99	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	3000	12000	-53.40	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Outer_Full	12000	20000	-55.32	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-71.82	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.58	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.10	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-57.73	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-46.16	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-55.35	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-72.16	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.80	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-50.47	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-57.77	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-53.58	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-55.38	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-73.61	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	0.15	30	-73.10	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	30	1000	-54.99	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	1000	3000	-50.74	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	3000	12000	-53.57	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Outer_Full	12000	20000	-55.24	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-72.47	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.24	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.03	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-52.37	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-53.60	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-55.10	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-72.52	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.22	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-51.50	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-57.63	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-53.59	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-55.31	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-72.56	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.36	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	30	1000	-52.44	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	1000	3000	-57.62	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	3000	12000	-53.55	-40	PASS

DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Outer_Full	12000	20000	-55.42	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.63	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.88	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.28	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.71	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-53.63	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-55.32	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.34	-70	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.71	-60	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-57.33	-50	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-57.80	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-53.60	-40	PASS
DC_14A_n30A	30	5+5	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-55.38	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-72.65	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	0.15	30	-71.85	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	30	1000	-55.25	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	1000	3000	-57.69	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	3000	12000	-53.56	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Outer_Full	12000	20000	-55.26	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-72.52	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.05	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-52.80	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-57.72	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-53.54	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-55.39	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-71.59	-70	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.98	-60	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.76	-50	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-55.31	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-53.63	-40	PASS
DC_14A_n30A	30	5+5	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-55.39	-40	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-57.75	-40	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-71.25	-70	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.80	-60	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-50.75	-50	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-56.81	-40	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-53.68	-40	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-55.15	-40	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.95	-70	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.24	-60	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.27	-50	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-57.64	-40	PASS

DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.62	-40	PASS
DC_14A_n30A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-55.30	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-73.13	-70	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-73.87	-60	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-50.42	-50	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-57.76	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	3000	12000	-53.56	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Outer_Full	12000	20000	-55.36	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-73.01	-70	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.31	-60	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.50	-50	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-47.21	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-53.49	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-55.43	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-71.65	-70	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.54	-60	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-55.92	-50	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-47.03	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-53.59	-40	PASS
DC_14A_n30A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-55.35	-40	PASS

Test Graphs

