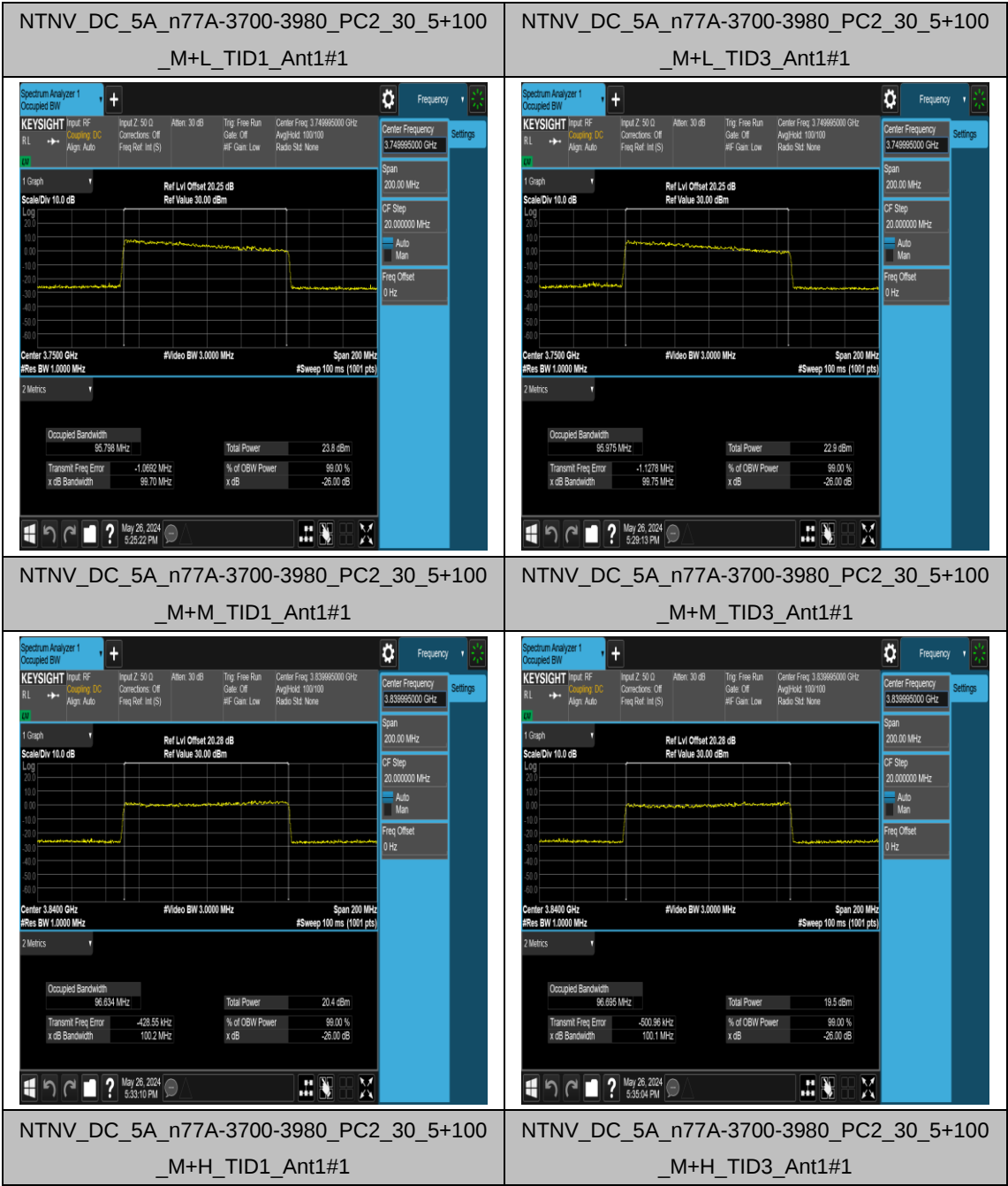






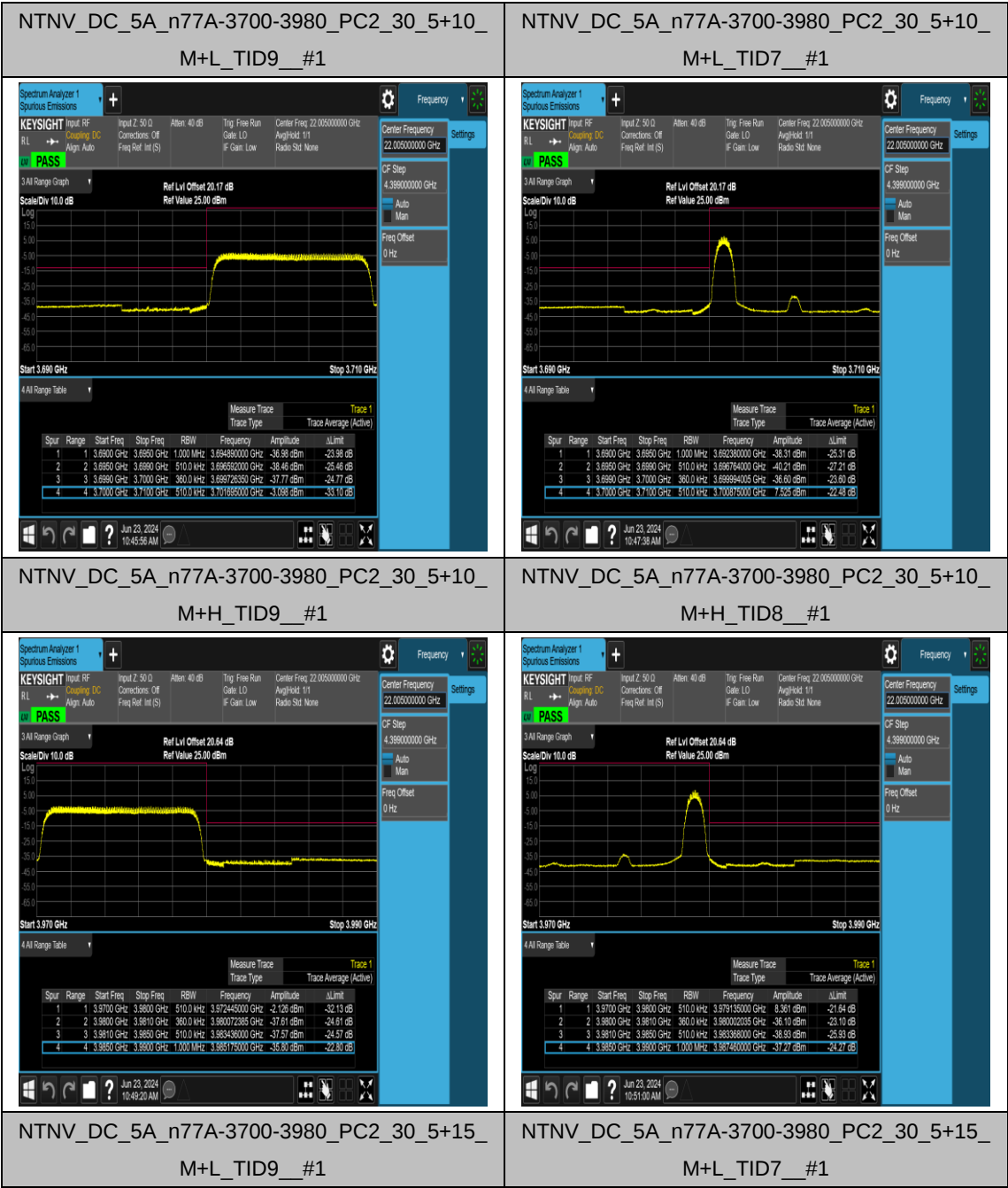
Appendix D: Band Edge for NSA

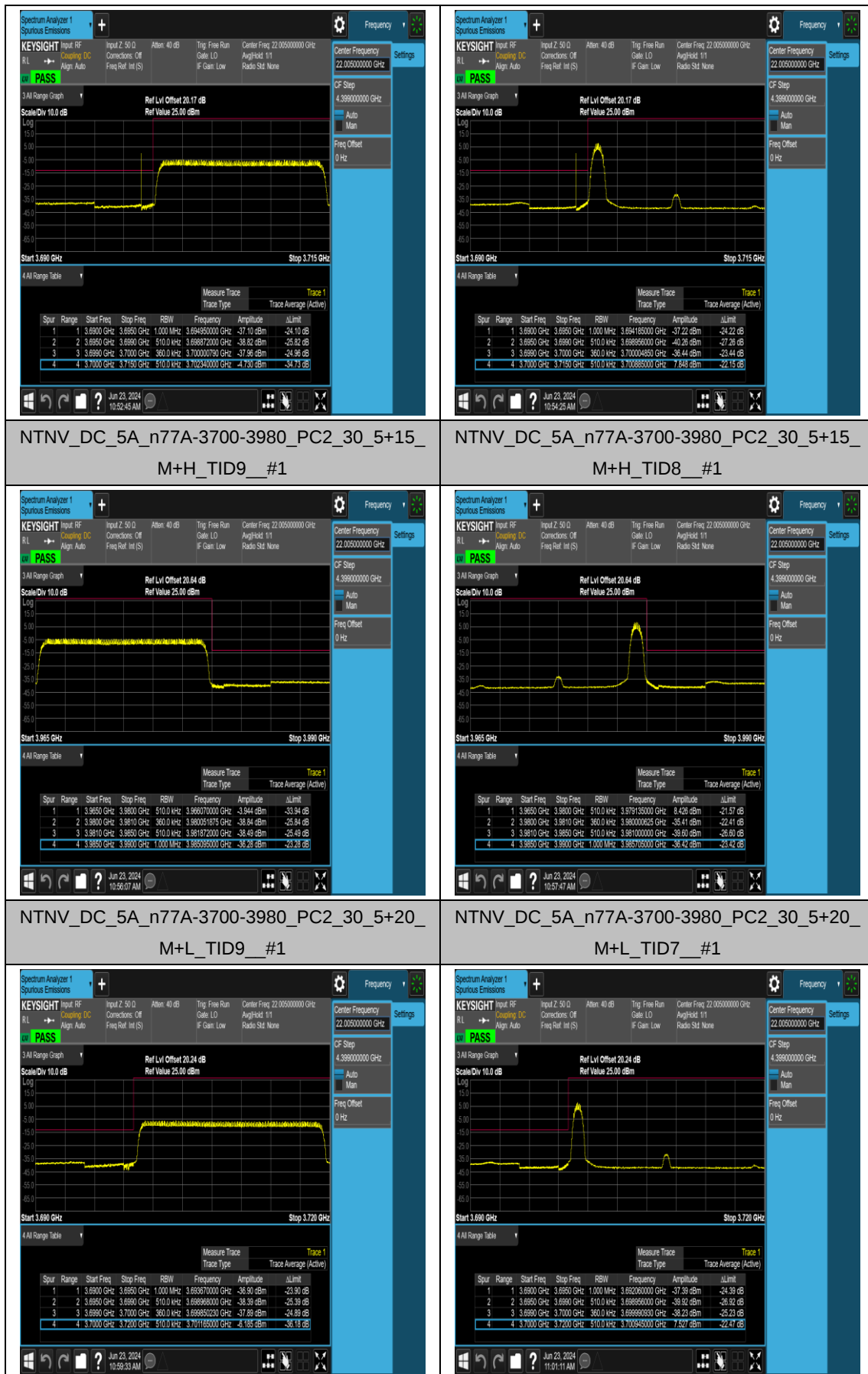
Test Result

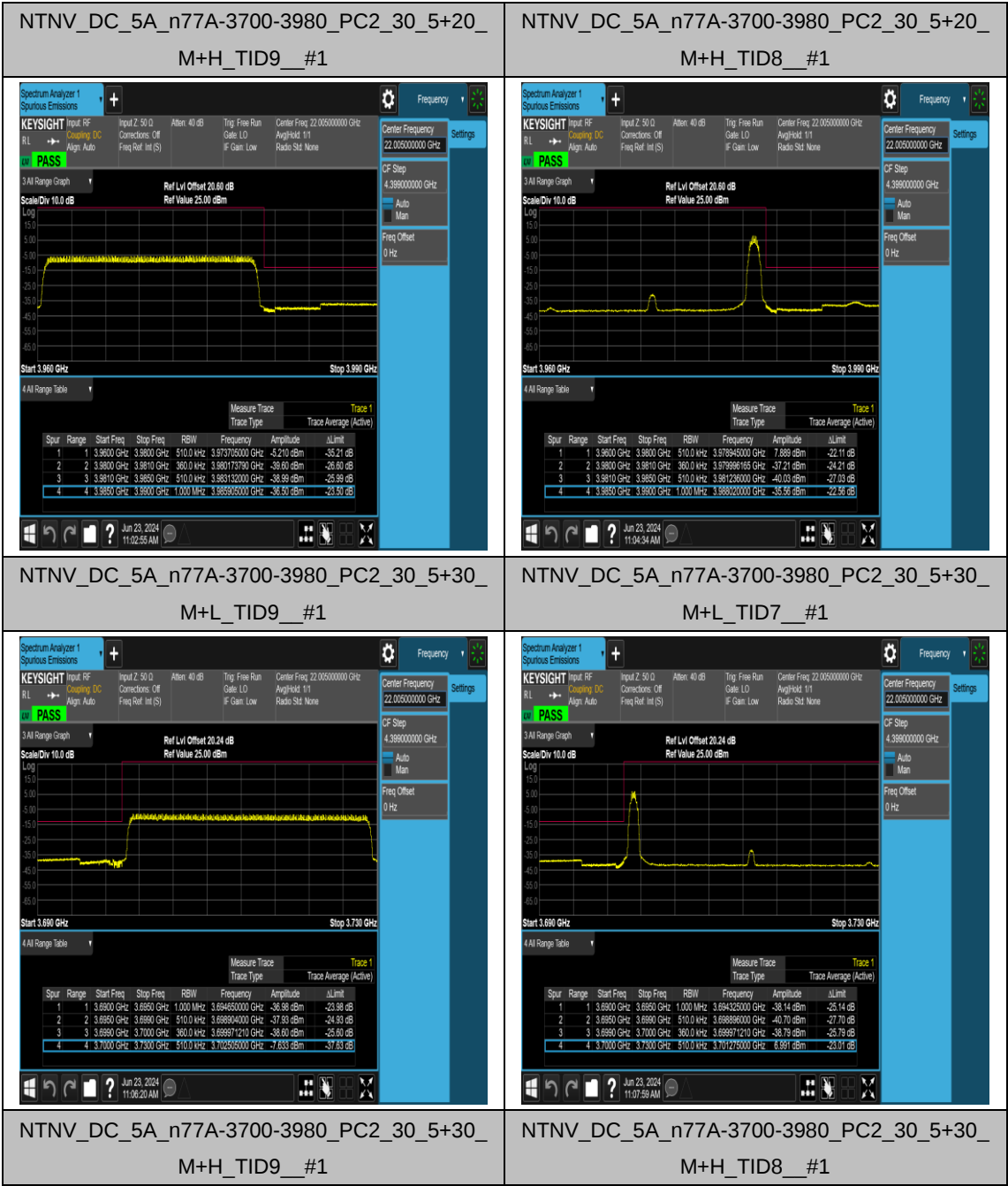
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_5A_n77A-3700-3980	30	5+10	CP-QPSK	M+L	Outer_Full	-36.98	23.98	PASS
DC_5A_n77A-3700-3980	30	5+10	CP-QPSK	M+L	Edge_1RB_Left	-36.60	23.60	PASS
DC_5A_n77A-3700-3980	30	5+10	CP-QPSK	M+H	Outer_Full	-35.80	22.80	PASS
DC_5A_n77A-3700-3980	30	5+10	CP-QPSK	M+H	Edge_1RB_Right	-36.10	23.10	PASS
DC_5A_n77A-3700-3980	30	5+15	CP-QPSK	M+L	Outer_Full	-37.10	24.10	PASS
DC_5A_n77A-3700-3980	30	5+15	CP-QPSK	M+L	Edge_1RB_Left	-36.44	23.44	PASS
DC_5A_n77A-3700-3980	30	5+15	CP-QPSK	M+H	Outer_Full	-36.28	23.28	PASS
DC_5A_n77A-3700-3980	30	5+15	CP-QPSK	M+H	Edge_1RB_Right	-35.41	22.41	PASS
DC_5A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	-36.90	23.90	PASS
DC_5A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	-37.39	24.39	PASS
DC_5A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	-36.50	23.50	PASS
DC_5A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	-35.56	22.56	PASS
DC_5A_n77A-3700-3980	30	5+30	CP-QPSK	M+L	Outer_Full	-36.98	23.98	PASS
DC_5A_n77A-3700-3980	30	5+30	CP-QPSK	M+L	Edge_1RB_Left	-38.14	25.14	PASS
DC_5A_n77A-3700-3980	30	5+30	CP-QPSK	M+H	Outer_Full	-36.96	23.96	PASS
DC_5A_n77A-3700-3980	30	5+30	CP-QPSK	M+H	Edge_1RB_Right	-37.23	24.23	PASS
DC_5A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	-37.33	24.33	PASS
DC_5A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	-38.21	25.21	PASS
DC_5A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	-37.07	24.07	PASS
DC_5A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	-37.53	24.53	PASS
DC_5A_n77A-3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	-37.88	24.88	PASS
DC_5A_n77A-3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	-38.18	25.18	PASS
DC_5A_n77A-3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	-37.33	24.33	PASS
DC_5A_n77A-3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	-37.69	24.69	PASS
DC_5A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	-38.06	25.06	PASS
DC_5A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	-38.13	25.13	PASS
DC_5A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	-37.31	24.31	PASS
DC_5A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	-37.50	24.50	PASS
DC_5A_n77A-3700-3980	30	5+70	CP-QPSK	M+L	Outer_Full	-37.45	24.45	PASS
DC_5A_n77A-3700-3980	30	5+70	CP-QPSK	M+L	Edge_1RB_Left	-38.29	25.29	PASS
DC_5A_n77A-3700-3980	30	5+70	CP-QPSK	M+H	Outer_Full	-37.05	24.05	PASS
DC_5A_n77A-3700-3980	30	5+70	CP-QPSK	M+H	Edge_1RB_Right	-37.83	24.83	PASS
DC_5A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	-37.85	24.85	PASS
DC_5A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	-38.17	25.17	PASS
DC_5A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	-37.47	24.47	PASS
DC_5A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	-37.71	24.71	PASS

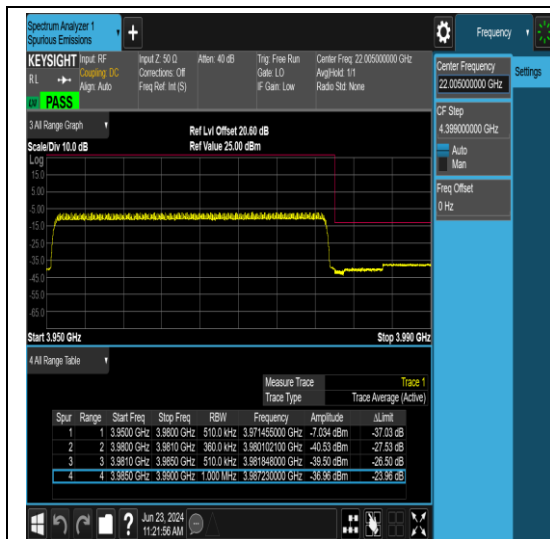
DC_5A_n77A-3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	-37.75	24.75	PASS
DC_5A_n77A-3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	-38.15	25.15	PASS
DC_5A_n77A-3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	-37.23	24.23	PASS
DC_5A_n77A-3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	-37.84	24.84	PASS
DC_5A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Outer_Full	-37.88	24.88	PASS
DC_5A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	-38.24	25.24	PASS
DC_5A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	-37.51	24.51	PASS
DC_5A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	-37.48	24.48	PASS

Test Graphs



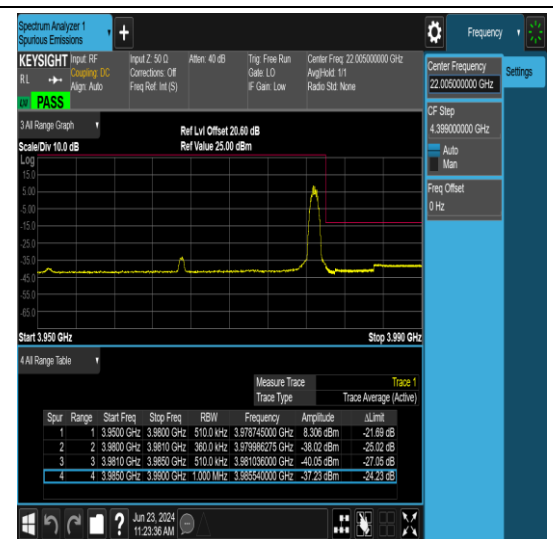






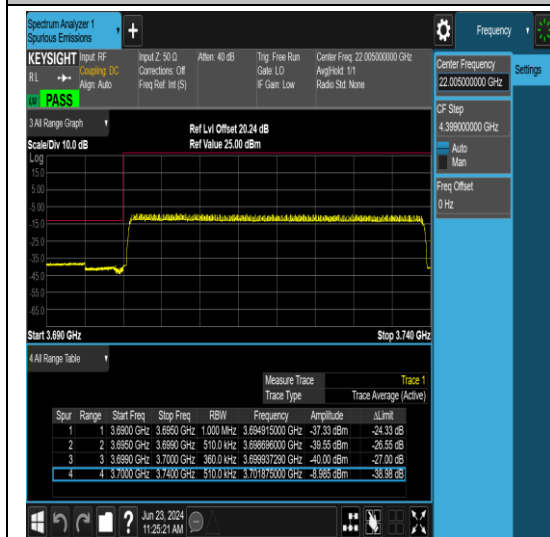
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_

M+L_TID9_#1



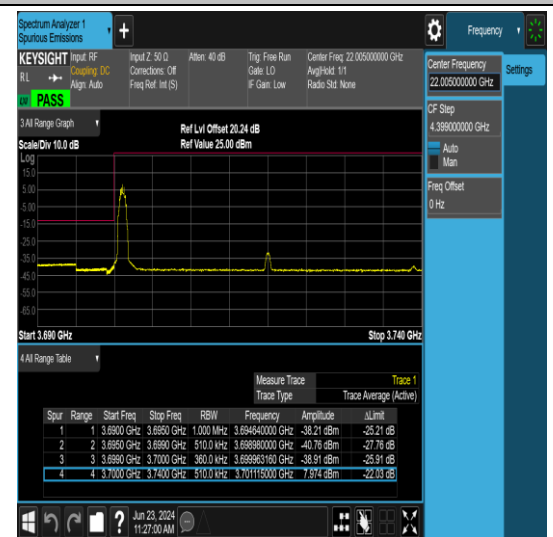
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_

M+L_TID7_#1



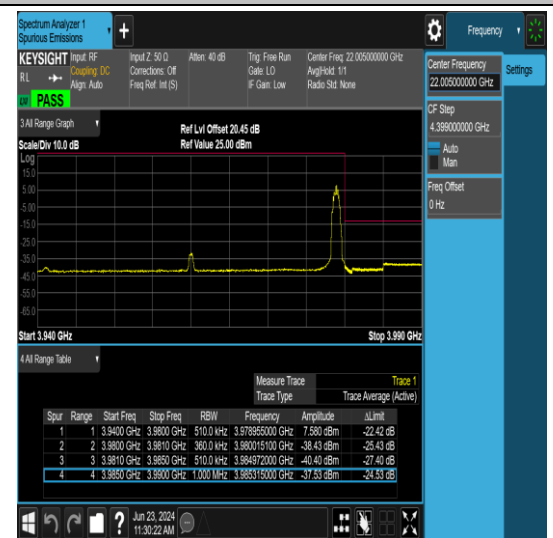
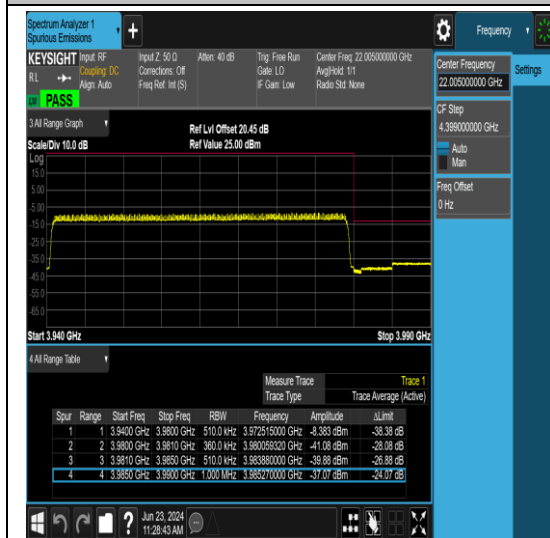
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_

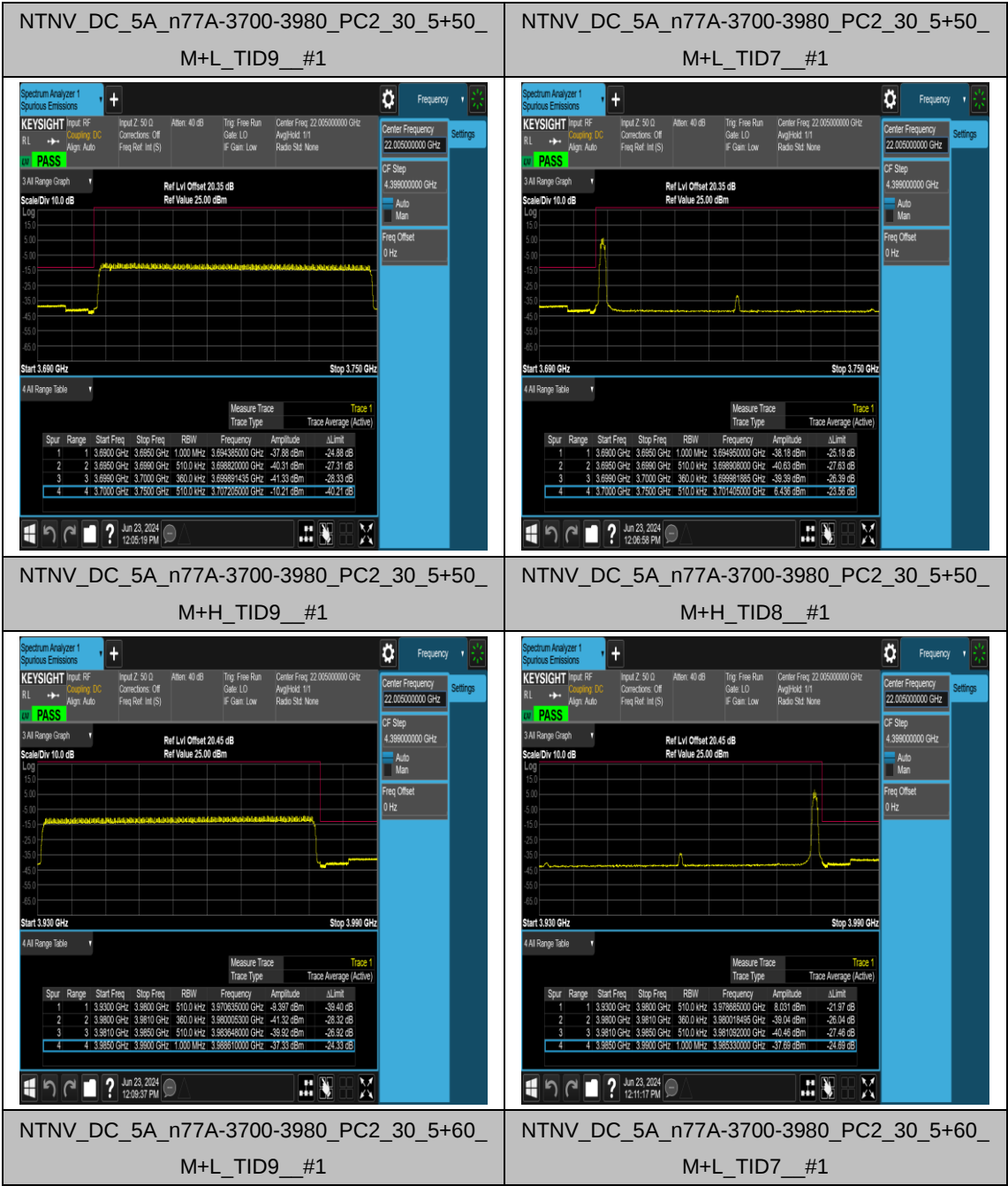
M+H_TID9_#1

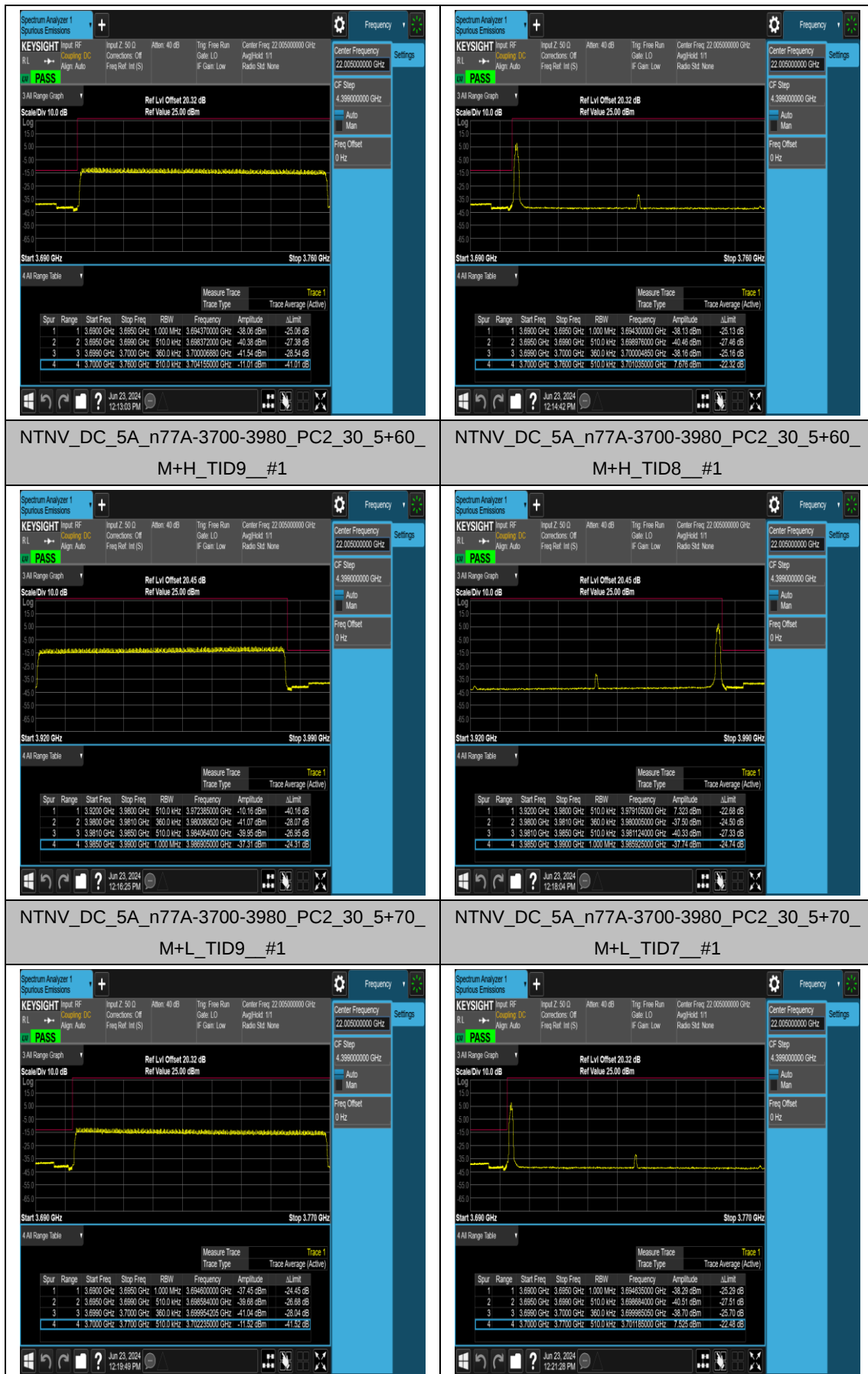


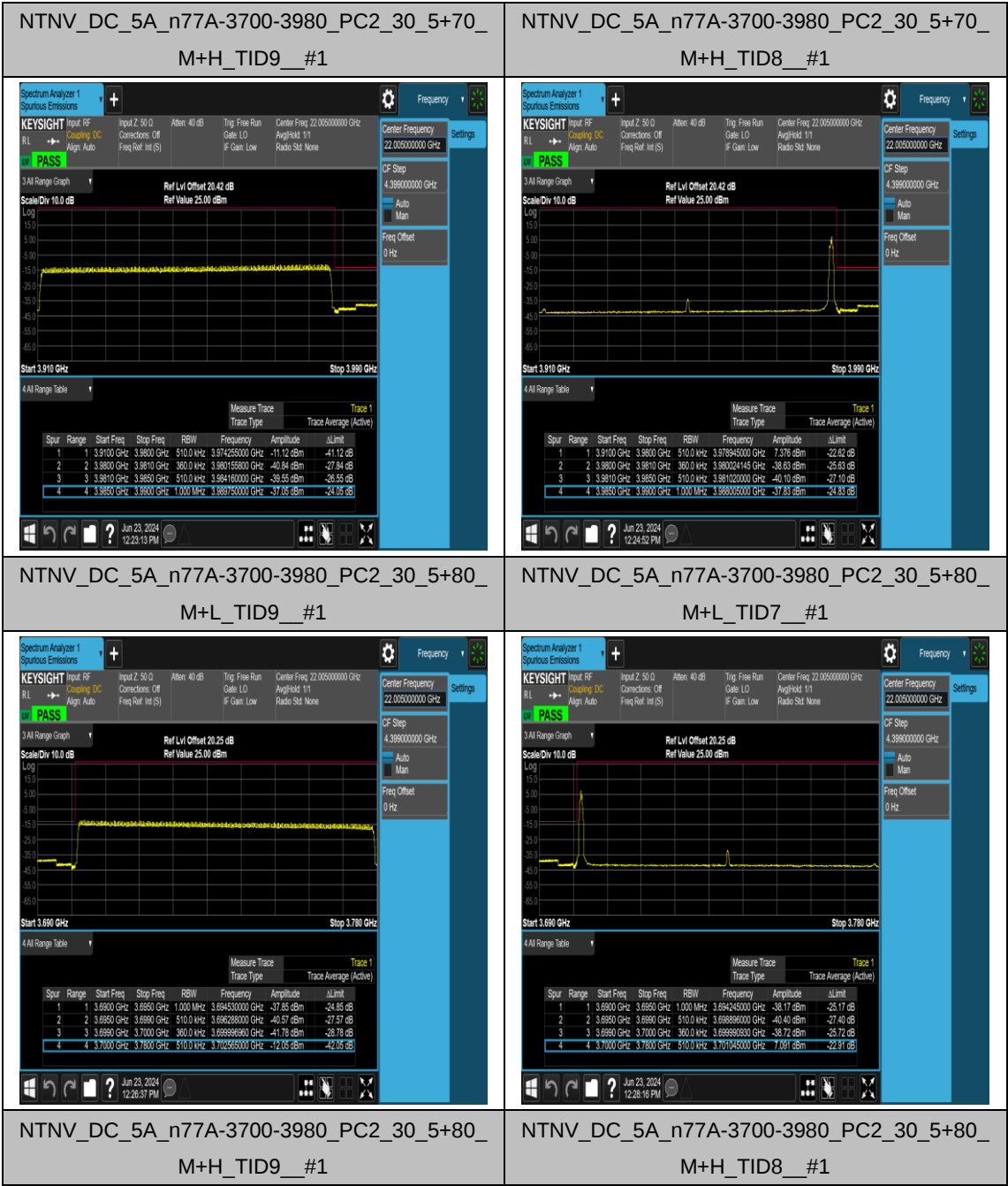
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_

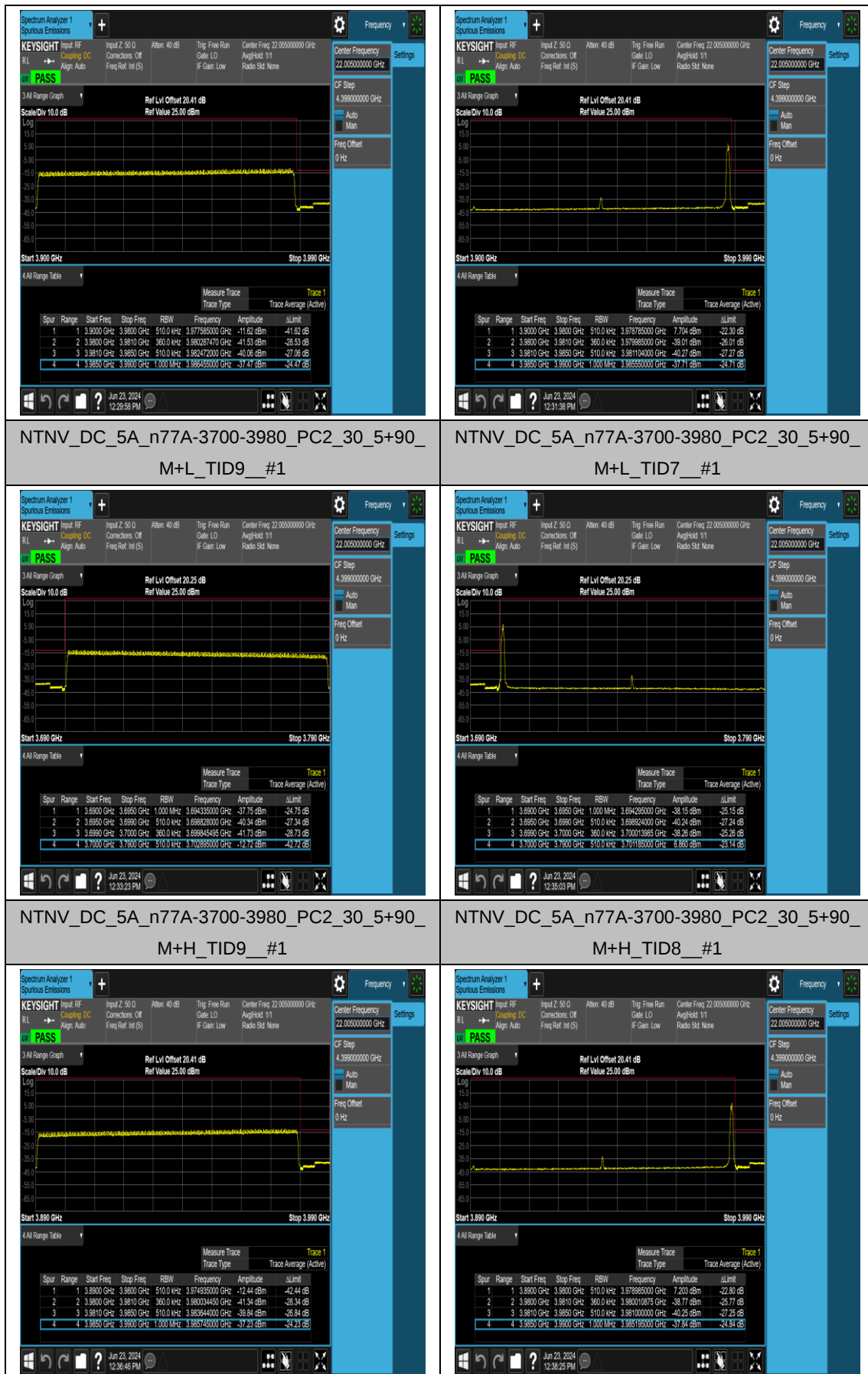
M+H_TID8_#1

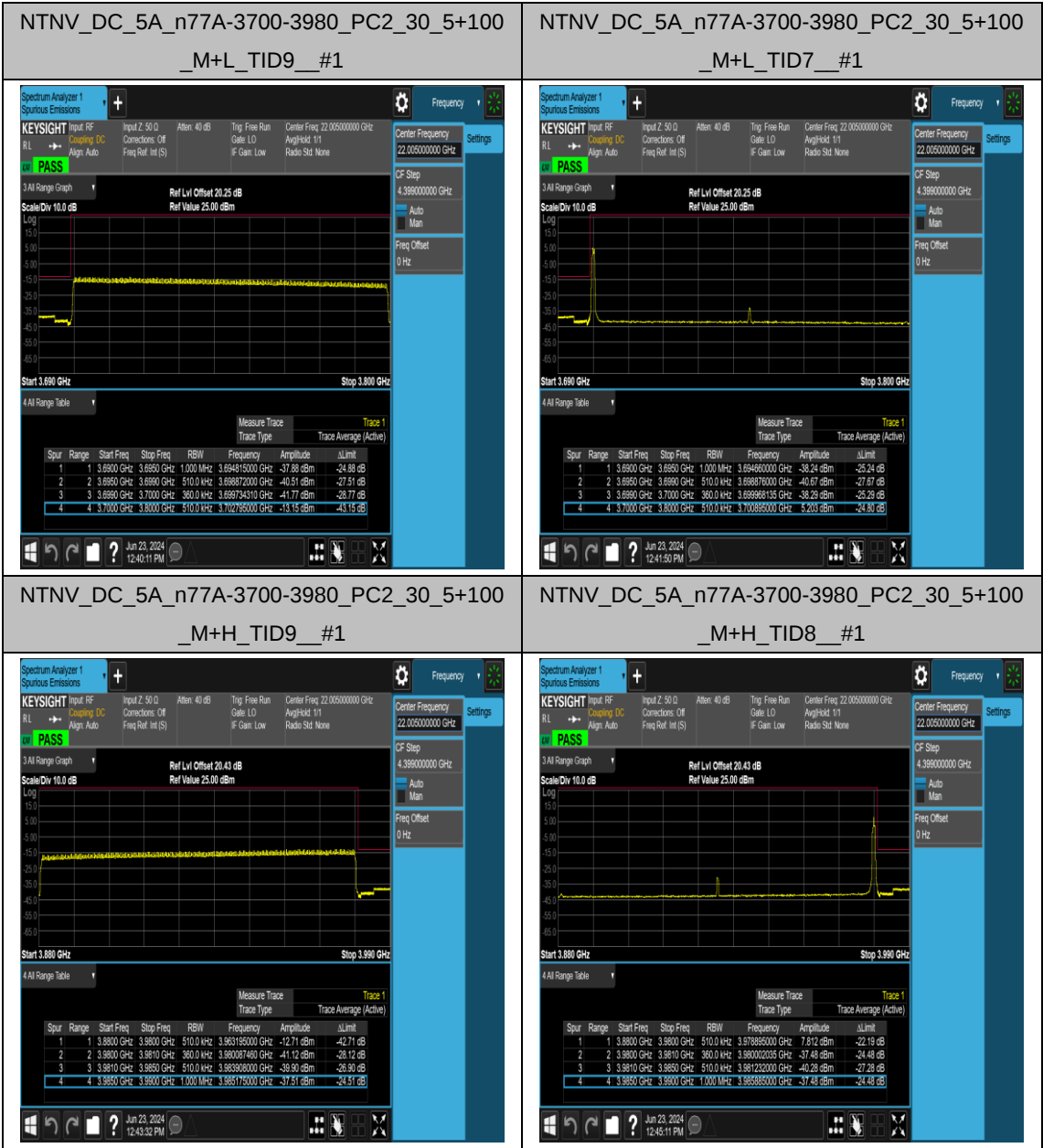












Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.84	-43	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.91	-33	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-44.91	-23	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.22	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.48	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.81	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.70	-43	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-63.38	-33	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.65	-23	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.40	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.98	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.24	-43	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.52	-33	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-45.00	-23	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.20	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.49	-13	PASS
DC_5A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.94	-13	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-55.26	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.45	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-45.17	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.50	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-56.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-66.75	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.70	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.61	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.36	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-45.14	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.35	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-54.35	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.43	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-45.10	-23	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.59	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-27.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.11	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.01	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-44.94	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.58	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-27.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.47	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.21	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-45.12	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-27.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-55.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.54	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.89	-13	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-55.71	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-66.38	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.97	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.53	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.59	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-45.13	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.35	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.60	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.43	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.56	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.52	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.19	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-64.08	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-44.85	-23	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.52	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.96	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.51	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.44	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.97	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.37	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-45.04	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.38	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.35	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.40	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.82	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.92	-13	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-55.42	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.75	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-45.11	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.68	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.99	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-45.20	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.50	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-45.10	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.61	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.31	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.47	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.70	-23	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-56.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.99	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-45.08	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.52	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-54.62	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.99	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-45.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.93	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.16	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-44.64	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.59	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.89	-13	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.21	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-45.02	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.09	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-45.05	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.55	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-44.69	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-54.90	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.54	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-44.87	-23	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-56.10	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.80	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-54.92	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.28	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.94	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.63	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.84	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-41.57	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.97	-13	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.57	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-40.54	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.36	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-54.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-44.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.64	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.71	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-45.07	-23	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-55.17	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.93	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-45.04	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.09	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-45.08	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.46	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.72	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.35	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.91	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.97	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.90	-13	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.54	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.77	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-55.02	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.51	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-44.61	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.36	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-54.51	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-45.08	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.33	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-54.66	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-45.07	-23	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.38	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-54.82	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.43	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-45.04	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.26	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.88	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-44.93	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.53	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.74	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.95	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.61	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.95	-13	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-56.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-28.98	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-56.23	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.97	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.62	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-54.98	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.00	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.78	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.60	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.36	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.60	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-44.97	-23	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.55	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.27	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.98	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-45.11	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-27.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.79	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-45.10	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.20	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-66.36	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-44.82	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.78	-13	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.36	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-66.50	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.95	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.47	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.43	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-43.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.20	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.59	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-44.88	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.33	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.61	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.59	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.76	-23	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.00	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.16	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-27.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-55.09	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.49	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-44.60	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-55.68	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.62	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.83	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.92	-13	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-54.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-45.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.41	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-54.55	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.45	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-44.78	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.54	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-43.61	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.38	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.61	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.75	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.62	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-45.06	-23	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-55.48	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.91	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-45.13	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.33	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-56.18	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.81	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.73	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.16	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.66	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.46	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.93	-13	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.35	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-44.92	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.76	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.60	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-44.86	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-53.60	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.38	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.97	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.78	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-44.98	-23	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-54.42	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.41	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.58	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-66.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-44.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-22.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.40	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.03	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-44.65	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.36	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.78	-13	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.59	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.02	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-42.51	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.33	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.55	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.58	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-56.79	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-66.68	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.68	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-56.00	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.11	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.95	-23	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.41	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.76	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.78	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-44.91	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.90	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.31	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-45.14	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.58	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-44.76	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.41	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.95	-13	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.45	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.07	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-45.20	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.46	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-54.17	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-66.26	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-44.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-27.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-55.45	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.57	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.88	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.45	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.03	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-45.12	-23	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.11	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.80	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-44.73	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.45	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-63.82	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-24.85	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.35	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.52	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.39	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.83	-13	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.62	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-44.77	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.73	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-54.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.63	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.85	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-66.79	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-44.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.36	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.73	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-45.10	-23	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.67	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.51	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.95	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.92	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.96	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-54.99	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-63.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-44.81	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.86	-13	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-55.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.90	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-45.12	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.59	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.92	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-44.95	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.51	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.59	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.32	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.72	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-44.97	-23	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.72	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.50	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.11	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-44.95	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-27.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-56.19	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.76	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-44.98	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-27.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-56.03	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-66.14	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-27.08	-13	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.45	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.11	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.85	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-56.07	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.91	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-45.12	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.79	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-45.07	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.54	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.42	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.47	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.51	-23	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-22.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.46	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-66.01	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-45.10	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.41	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-56.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.75	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-38.41	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.56	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-27.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-56.03	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.06	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-44.95	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.60	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.92	-13	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-54.94	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.34	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-44.88	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.36	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.19	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.19	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-45.00	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.61	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.64	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-63.81	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-25.05	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-56.10	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.83	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-45.16	-23	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.59	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-54.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-66.18	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.73	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.56	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.26	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.64	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.78	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-44.57	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.55	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.89	-13	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-31.20	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.46	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.55	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.11	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-45.06	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.54	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-55.21	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.80	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-44.68	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.65	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-54.85	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-66.45	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.68	-23	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.54	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.11	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.27	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.83	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.19	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-44.56	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.35	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-63.50	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-45.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.94	-13	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.81	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.12	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.12	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.87	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-44.91	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.71	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.72	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.71	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.97	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.41	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-45.21	-23	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.63	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-54.32	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-64.03	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-45.04	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.53	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.43	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.50	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.81	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.57	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.82	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.67	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.74	-13	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-56.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	0.15	30	-66.36	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	30	1000	-44.85	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.33	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.50	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	26000	40000	-27.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-55.60	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.86	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.40	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.97	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-44.59	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.59	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.85	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-45.16	-23	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.86	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.20	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-45.03	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-27.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.21	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.09	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-44.68	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.52	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-55.71	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.90	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	30	1000	-44.55	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.46	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.79	-13	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-56.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.22	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.54	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.27	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.38	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.78	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-44.80	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.75	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.94	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-45.01	-23	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.94	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.21	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.70	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-54.40	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	0.15	30	-66.27	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	30	1000	-44.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	26000	40000	-27.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-54.66	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.98	-13	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.73	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.72	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-44.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.52	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-45.10	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.08	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.97	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.98	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-62.66	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.76	-23	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-54.94	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.59	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-55.50	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.07	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-45.08	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.53	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.96	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-56.22	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.76	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-42.62	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.55	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.94	-13	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-53.97	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.99	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-44.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.58	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.62	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.10	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-38.43	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.17	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.63	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.96	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-56.32	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-67.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-45.08	-23	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.59	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-54.31	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.12	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-27.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.23	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.12	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.38	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.89	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.92	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-44.81	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.35	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.92	-13	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.05	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.26	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-37.98	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.74	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.84	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-45.08	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.73	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.51	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-66.03	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-44.74	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.47	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.65	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.85	-23	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-55.27	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-44.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.00	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.21	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-45.03	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.04	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.82	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.94	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.57	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.95	-13	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.70	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.86	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-44.11	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-27.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-55.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.38	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-44.80	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.41	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-55.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.35	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.36	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.71	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-54.02	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.95	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-38.37	-23	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.52	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-54.93	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.63	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-39.34	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.72	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.60	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.50	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-45.19	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.32	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-27.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.31	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.57	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-44.93	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.42	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.93	-13	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-56.08	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.34	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-45.00	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.50	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-27.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-55.03	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.60	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-45.00	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.36	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.77	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-64.83	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-45.06	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.54	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.50	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-44.94	-23	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.29	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-64.08	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-44.97	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.49	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.87	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-45.12	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-22.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-63.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-44.71	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.50	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.95	-13	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-54.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.66	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-44.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.81	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-66.79	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-44.80	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.41	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.06	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.65	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-44.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-26.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.05	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-44.42	-23	PASS

DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-23.33	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-26.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.15	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.67	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-45.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-23.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-26.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-55.10	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.28	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	30	1000	-28.71	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	1000	6000	-23.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	6000	26000	-23.43	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	26000	40000	-26.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-53.71	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.83	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-44.93	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-23.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-23.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-26.70	-13	PASS

DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-55.57	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.33	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-43.33	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-23.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-23.44	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-26.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.86	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-44.61	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-23.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-23.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-26.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.08	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-42.35	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-23.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-23.40	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-26.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.23	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.03	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-45.12	-23	PASS

DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-23.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-26.68	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-55.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.48	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-44.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	6000	26000	-23.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	26000	40000	-26.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-55.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-66.25	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-44.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-23.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-23.39	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-26.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.49	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.09	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-45.05	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-23.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-23.31	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-26.86	-13	PASS

DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.89	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-44.95	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-23.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-23.30	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-26.69	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.51	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-45.03	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-23.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-23.47	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-44.97	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-23.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-26.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-55.66	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.71	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-44.94	-23	PASS

DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	1000	6000	-23.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	6000	26000	-23.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	26000	40000	-26.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-53.65	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.90	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-45.00	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-23.35	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-23.48	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-26.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-56.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.83	-33	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-45.03	-23	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-23.28	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-23.34	-13	PASS
DC_5A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-26.88	-13	PASS

Test Graphs

