

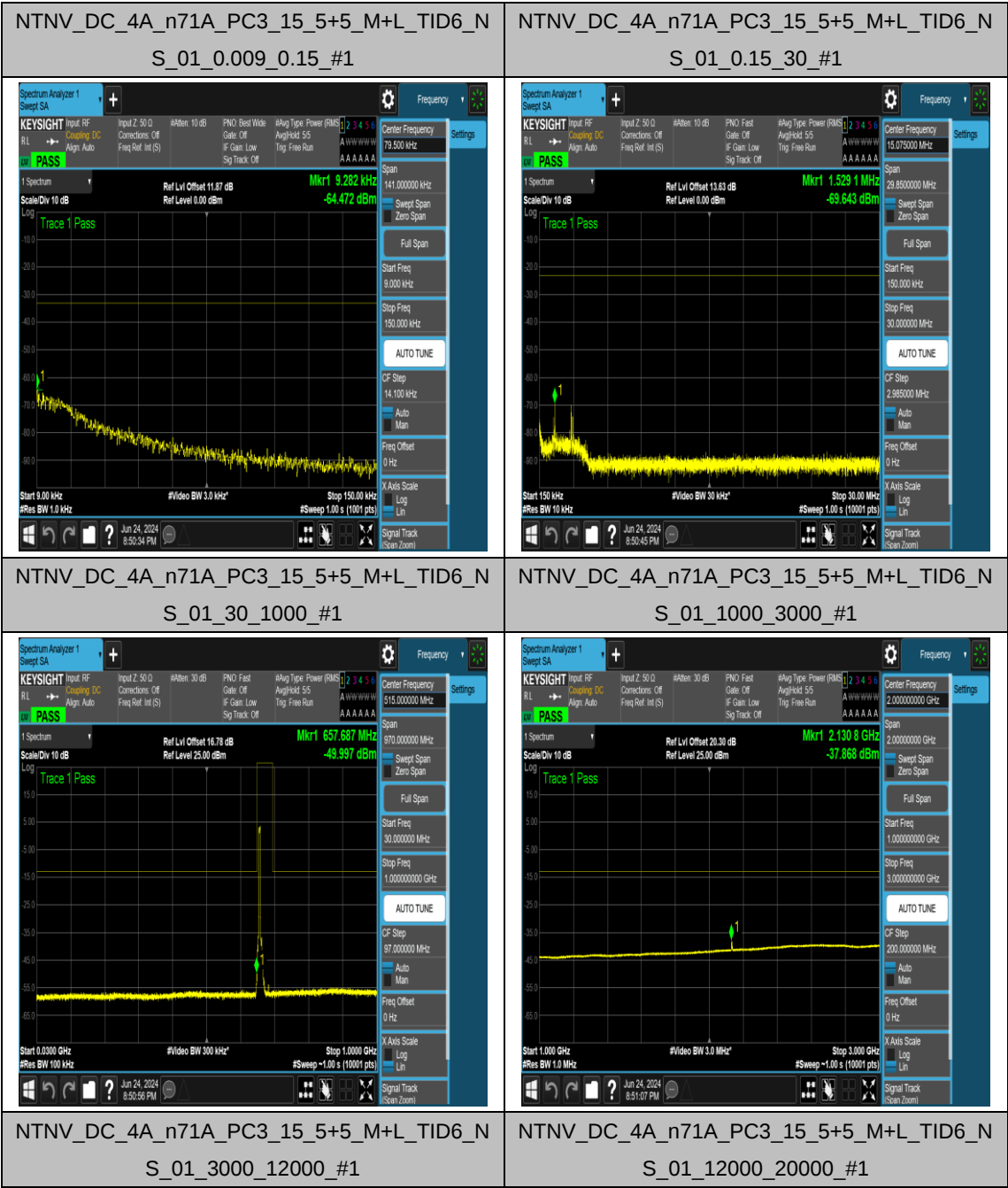
DC_4A_n71A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-47.54	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.36	-33	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.01	-23	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-43.98	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.36	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.55	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.71	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.66	-33	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.56	-23	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.95	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.24	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.48	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-47.84	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.44	-33	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.61	-23	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-51.70	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.55	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.56	-13	PASS
DC_4A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-47.78	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.87	-33	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-70.36	-23	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-41.80	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-38.24	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.46	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	12000	20000	-47.76	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.60	-33	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-68.99	-23	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.58	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-36.37	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-38.75	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-47.69	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.59	-33	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-69.48	-23	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.43	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.92	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-46.50	-13	PASS
DC_4A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-47.85	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.21	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-70.96	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-50.79	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.05	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-46.51	-13	PASS

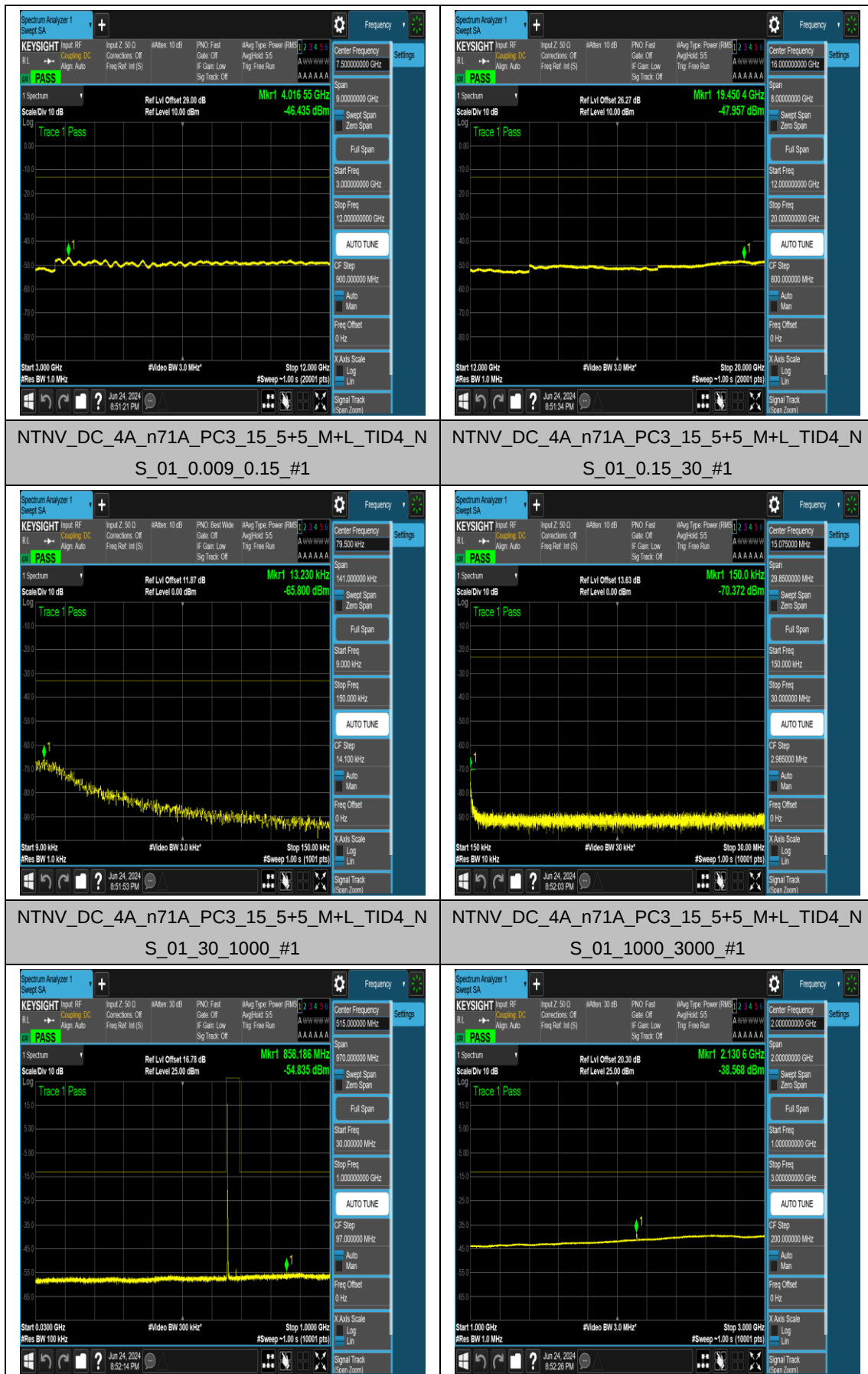
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DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.26	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.33	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.86	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.64	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-46.57	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.82	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.40	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.94	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.76	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.96	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-36.36	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.91	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.63	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-72.16	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-49.63	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.28	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	3000	12000	-46.57	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	12000	20000	-47.74	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.34	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-69.41	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.76	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.32	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-46.34	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-47.85	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.47	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-69.69	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.80	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-35.62	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-46.53	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-47.88	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.81	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.80	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-42.74	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.87	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.48	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-47.79	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.65	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-70.74	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.18	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.05	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-36.09	-13	PASS

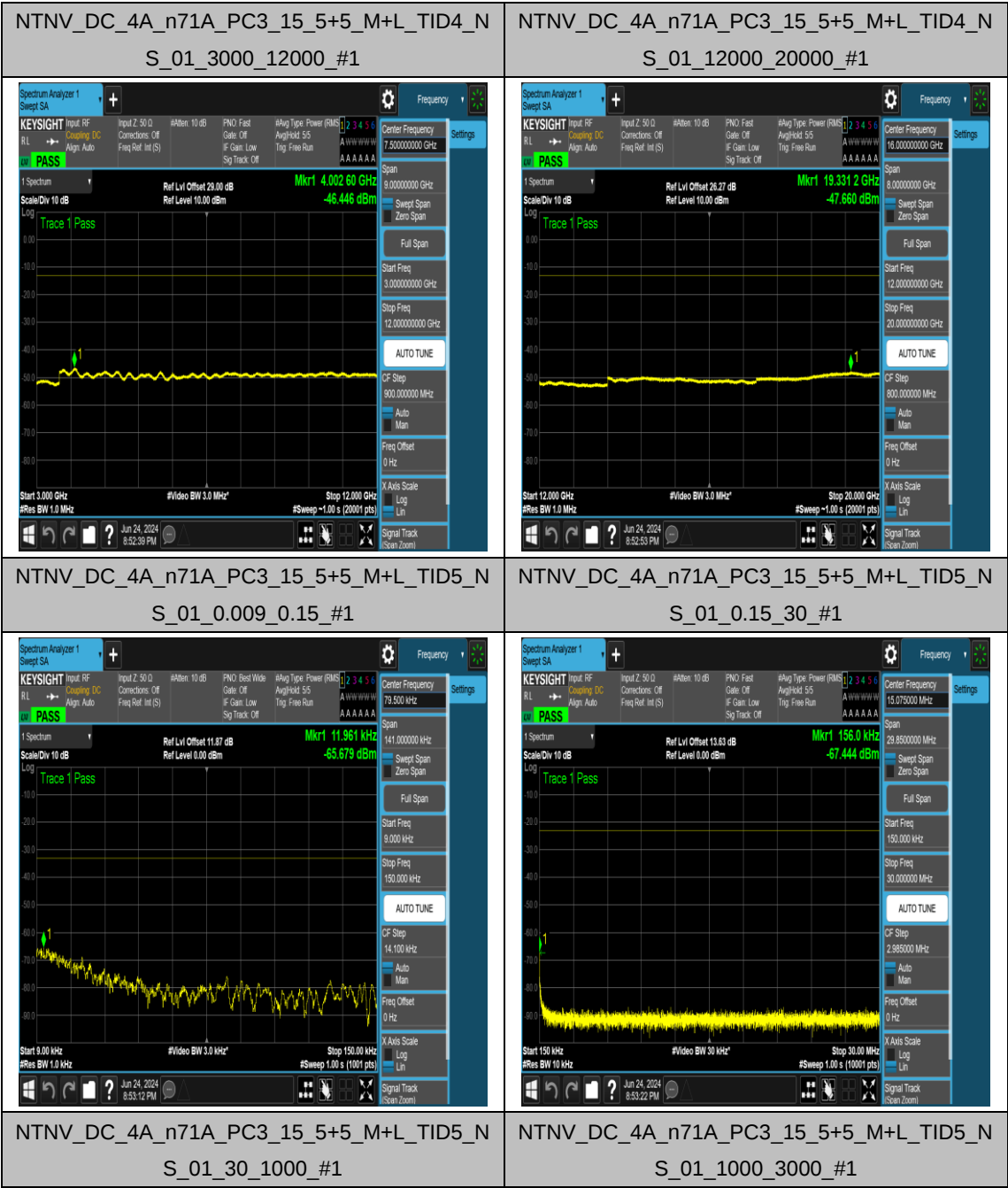
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-47.83	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.80	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.63	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.77	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.07	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.51	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.82	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.25	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-69.97	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-44.04	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.17	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	3000	12000	-46.49	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	12000	20000	-47.97	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.72	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-69.76	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.37	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.92	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-46.32	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-47.79	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.52	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-70.25	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.79	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.15	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-46.71	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-47.75	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.66	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.16	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-46.31	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.41	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.58	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.86	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.65	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.43	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.90	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.15	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.55	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-47.82	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.68	-33	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.98	-23	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.33	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.85	-13	PASS
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.53	-13	PASS

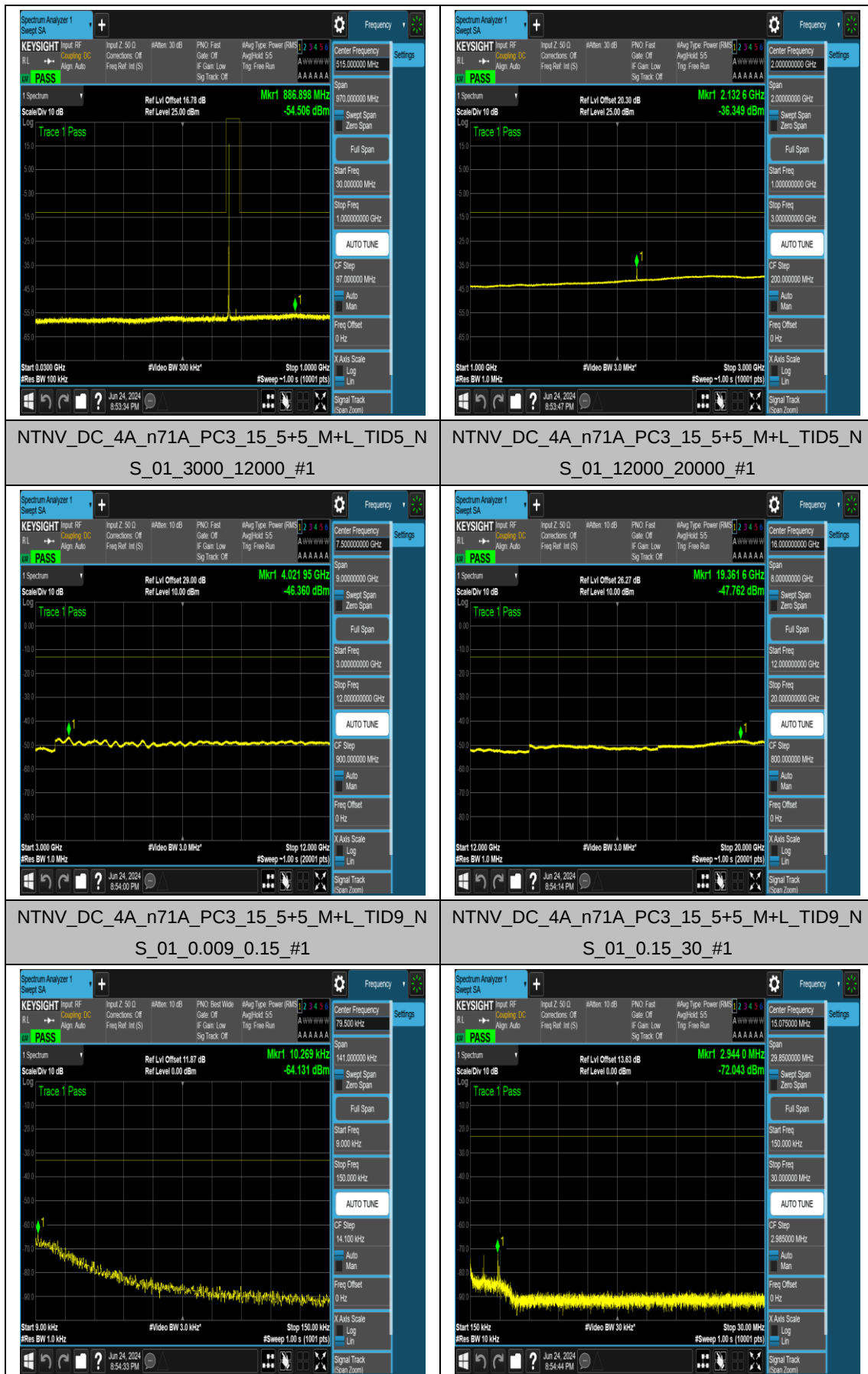
DC_4A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-47.66	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.92	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.90	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-43.99	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.23	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.50	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	12000	20000	-47.66	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.53	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-67.47	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.61	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-36.81	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-46.56	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-47.85	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.76	-33	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-67.38	-23	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.18	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-14.09	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-46.57	-13	PASS
DC_4A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-47.91	-13	PASS

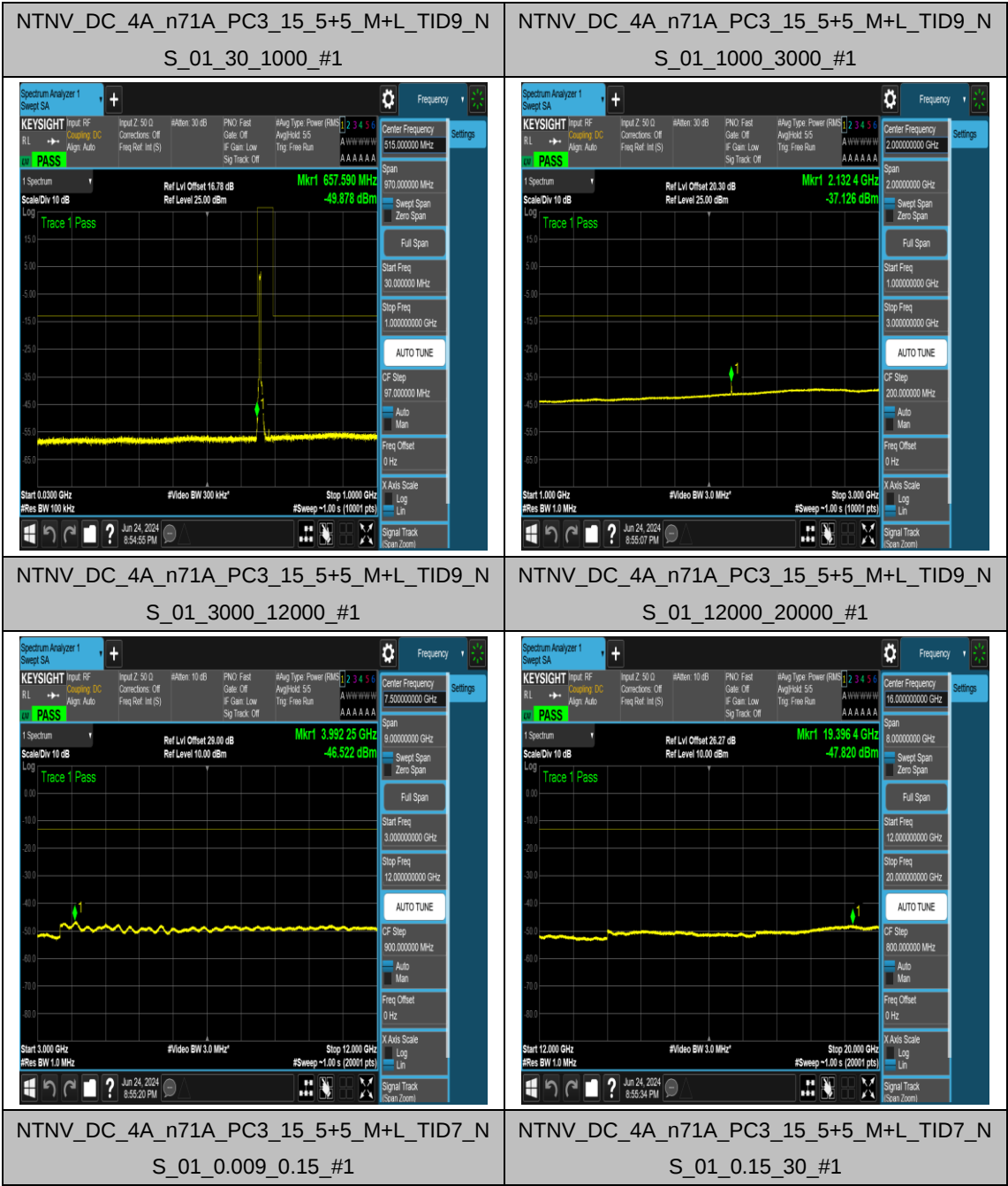
Test Graphs

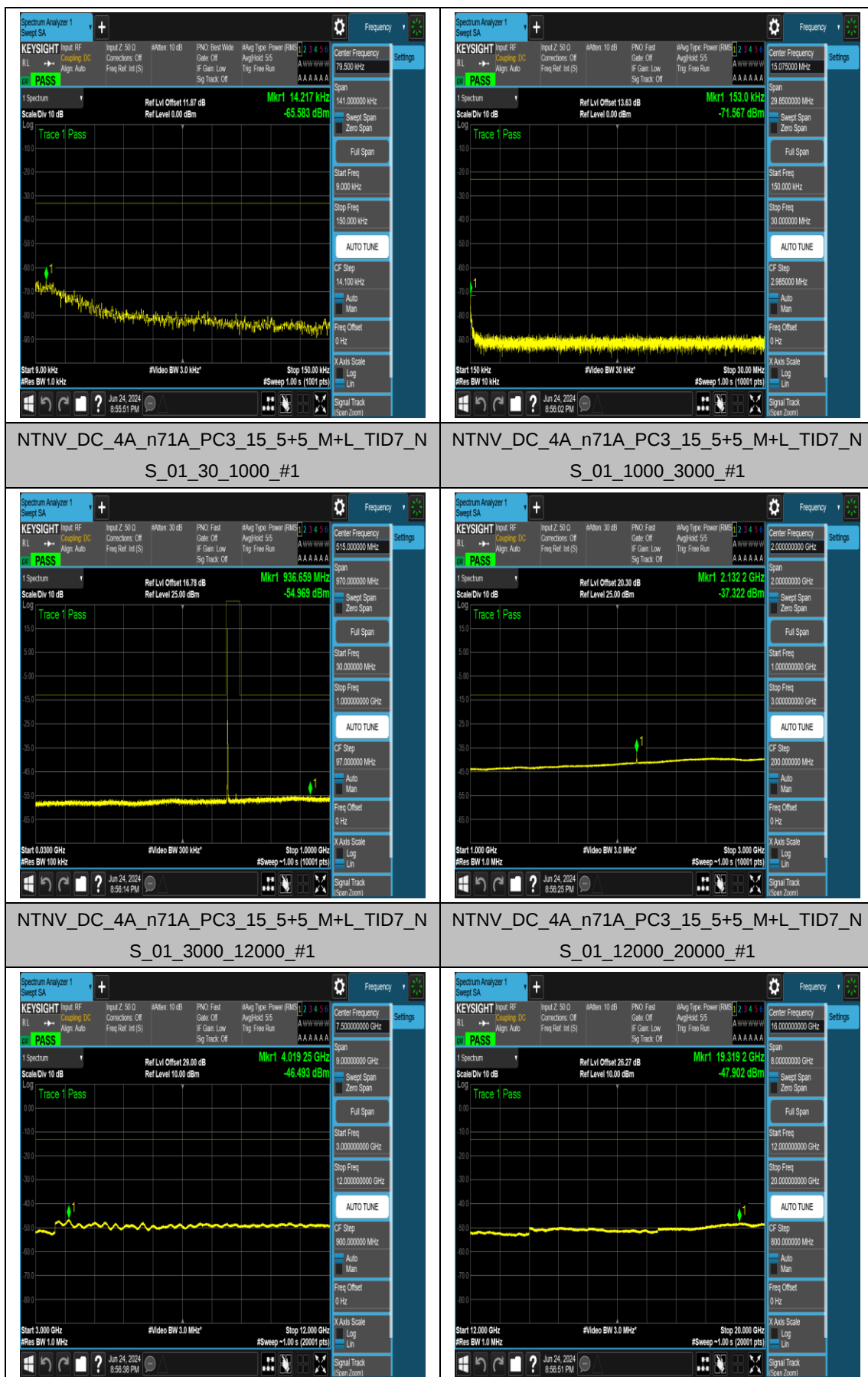












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