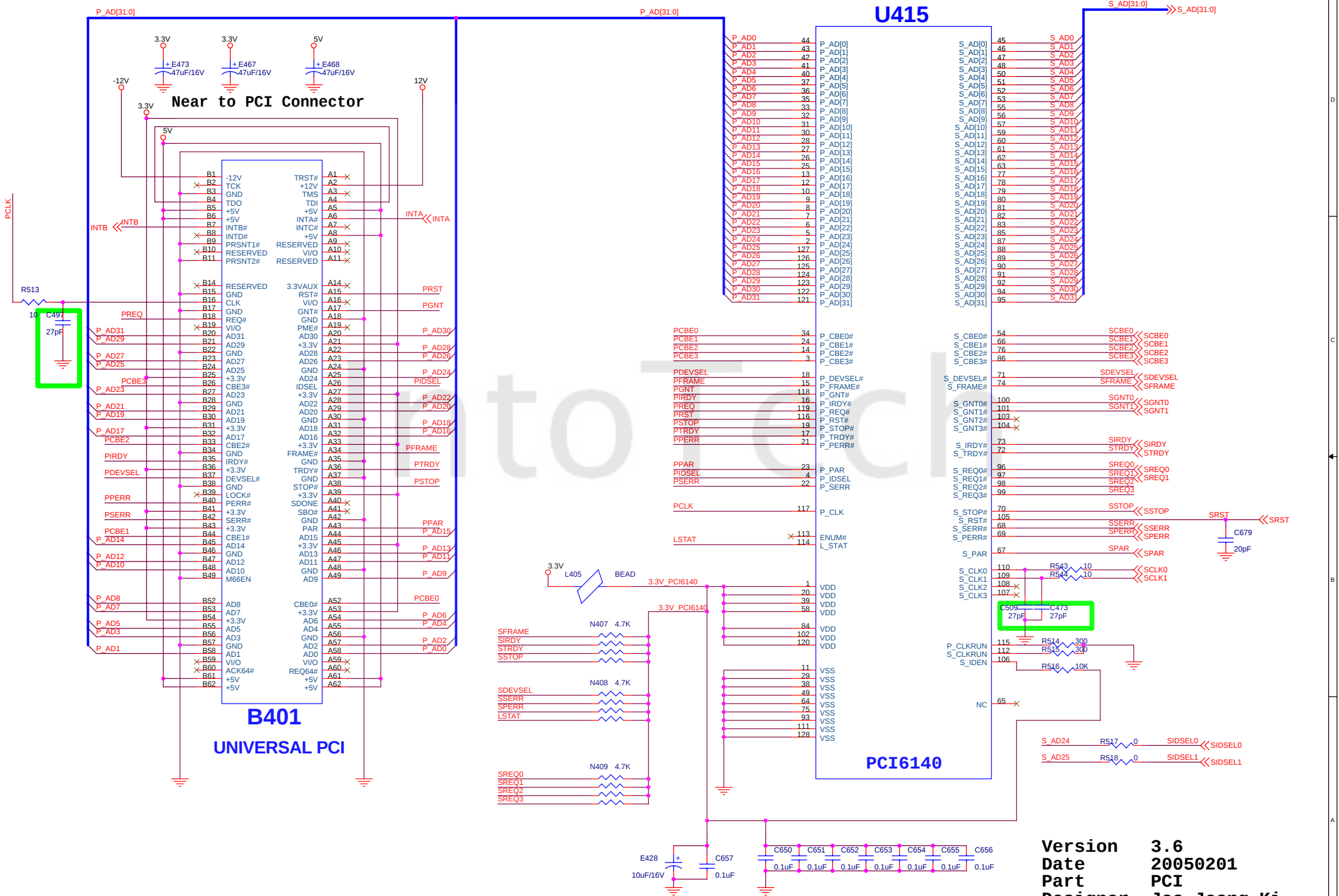
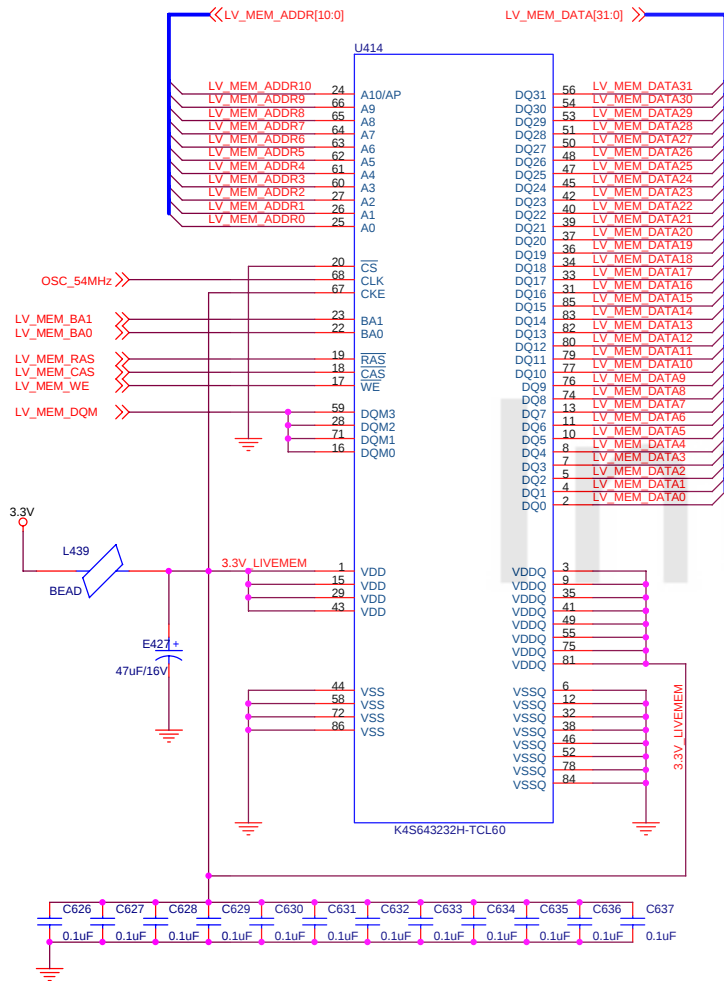




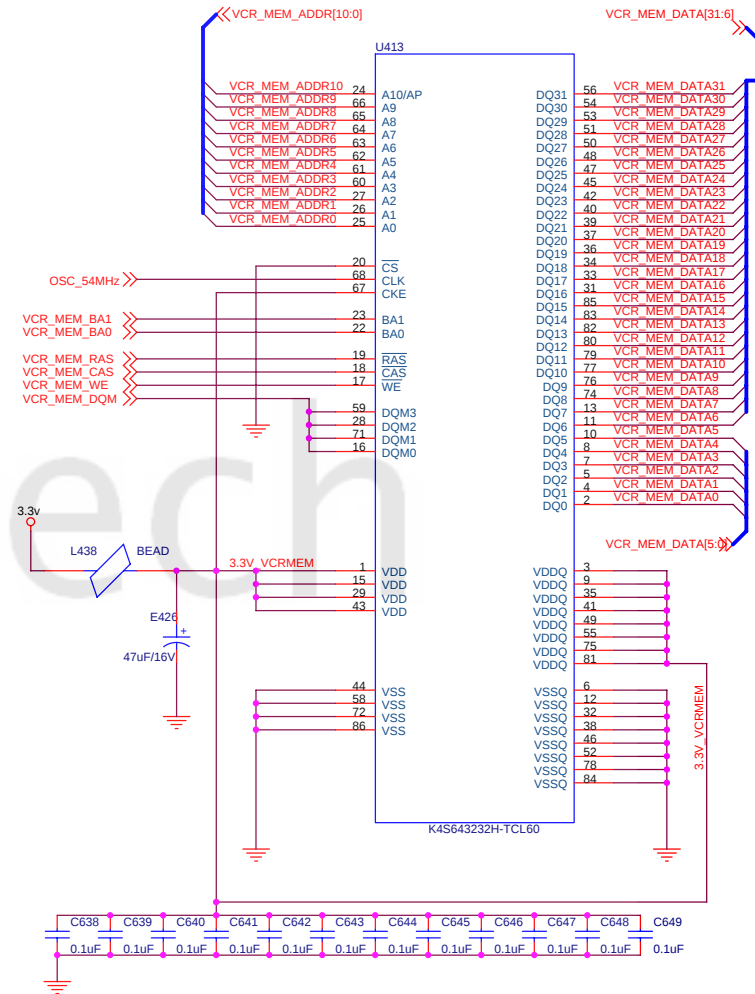
Version 3.6
Date 20050201
Part RS232 CONVERTER & POWER
Designer Joo Jeong Ki



Version 3.6
Date 20050201
Part PCI
Designer Joo Jeong Ki



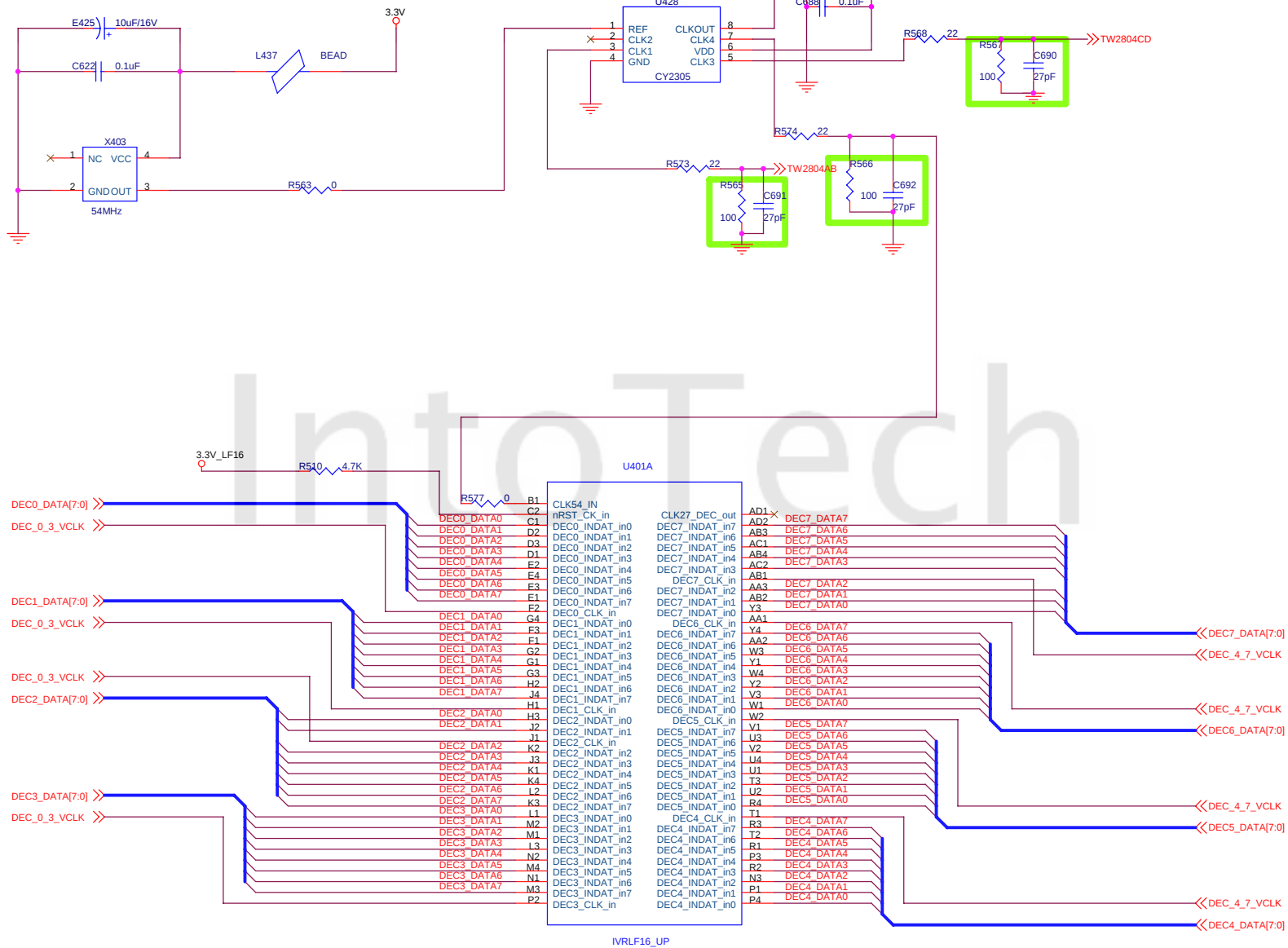
DISPLAY_MEMORY



RECORD_MEMORY

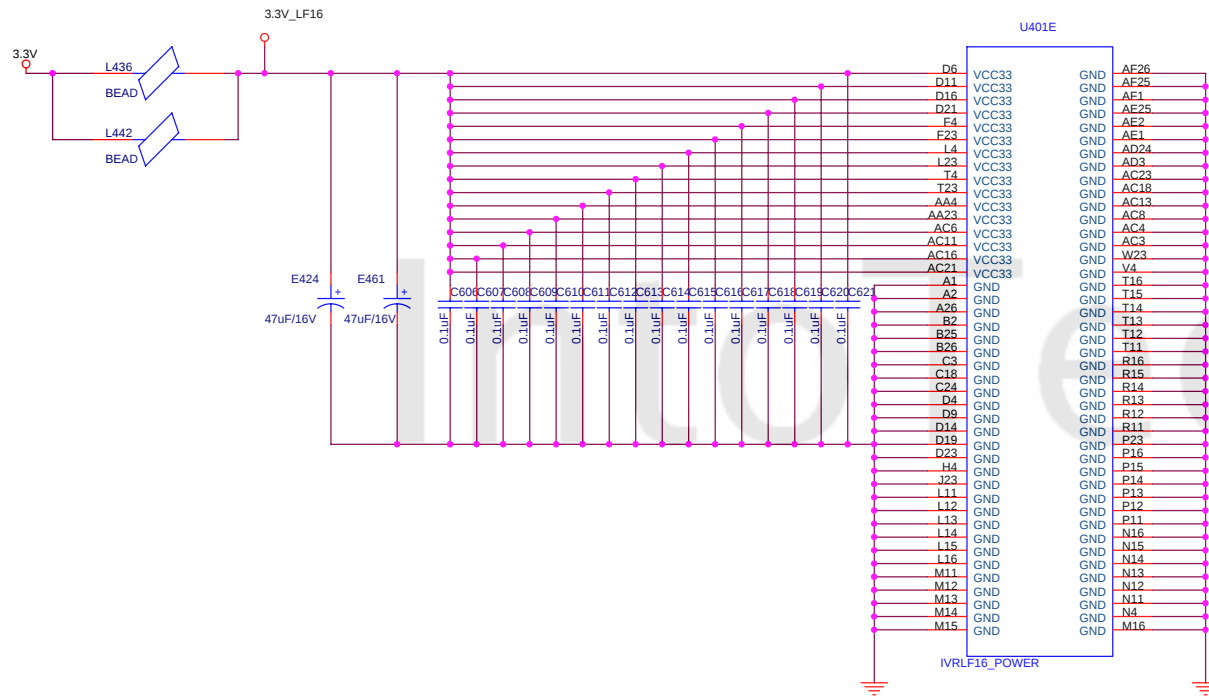
Version 3.6
 Date 20050201
 Part MEMORY
 Designer Joo Jeong Ki

LF16 UP

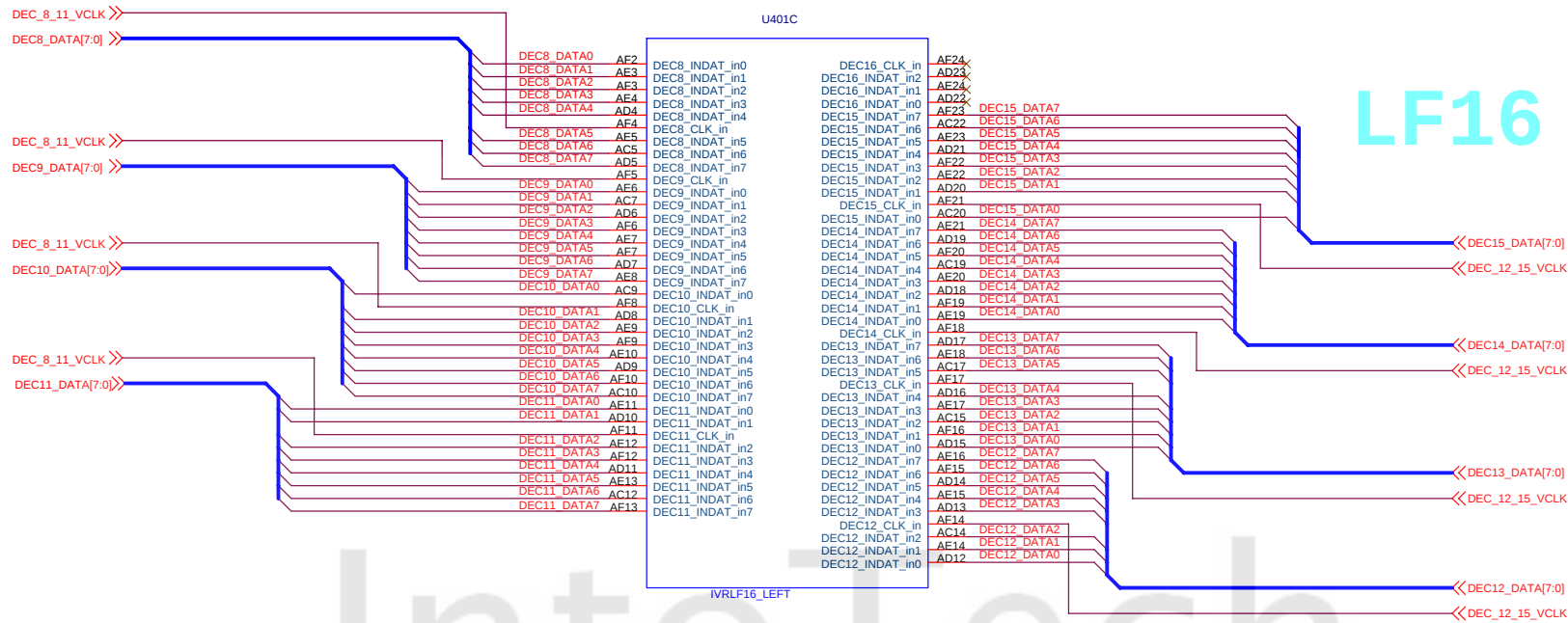


Version 3.6
Date 20050201
Part LF16 UP
Designer Joo Jeong Ki

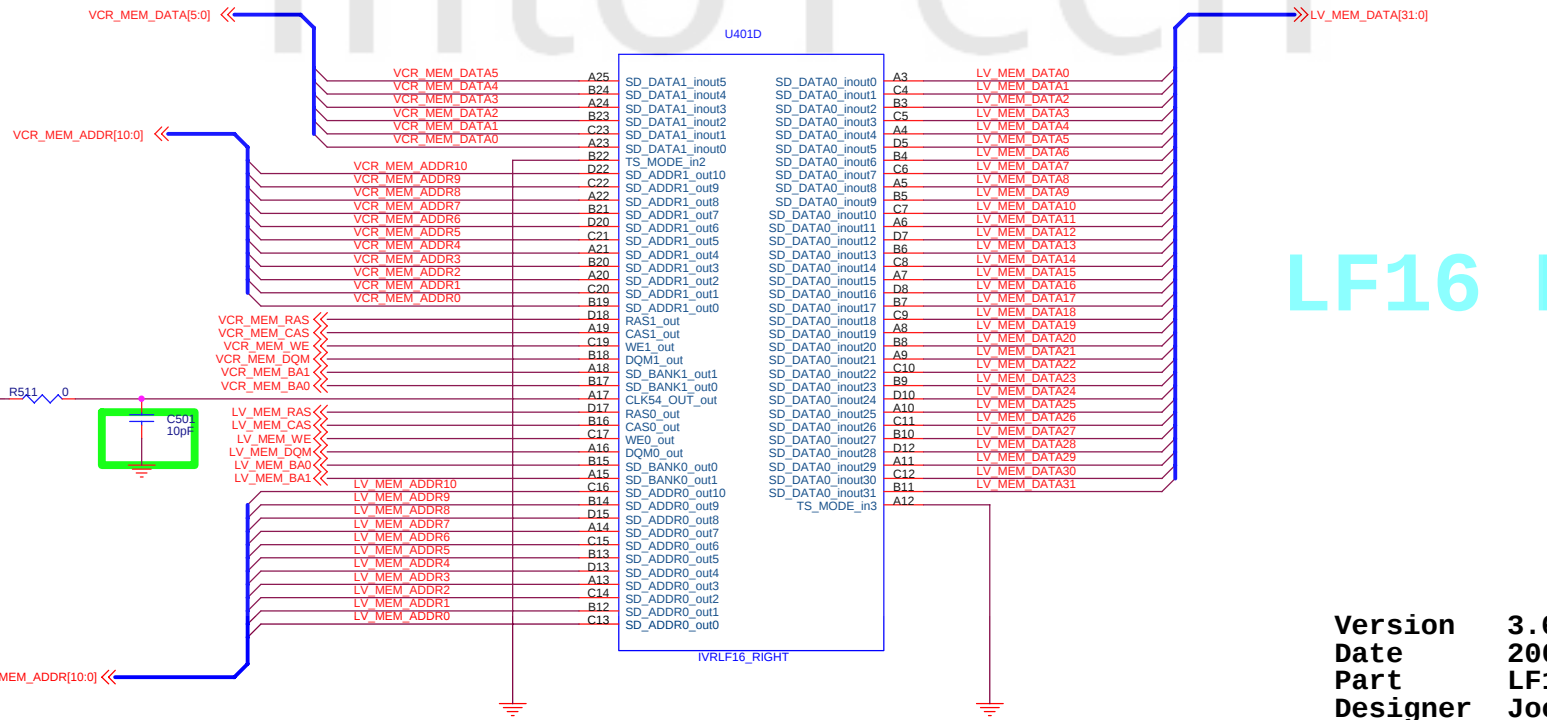
LF16 POWER



Version 3.6
Date 20050201
Part LF16 POWER
Designer Joo Jeong Ki

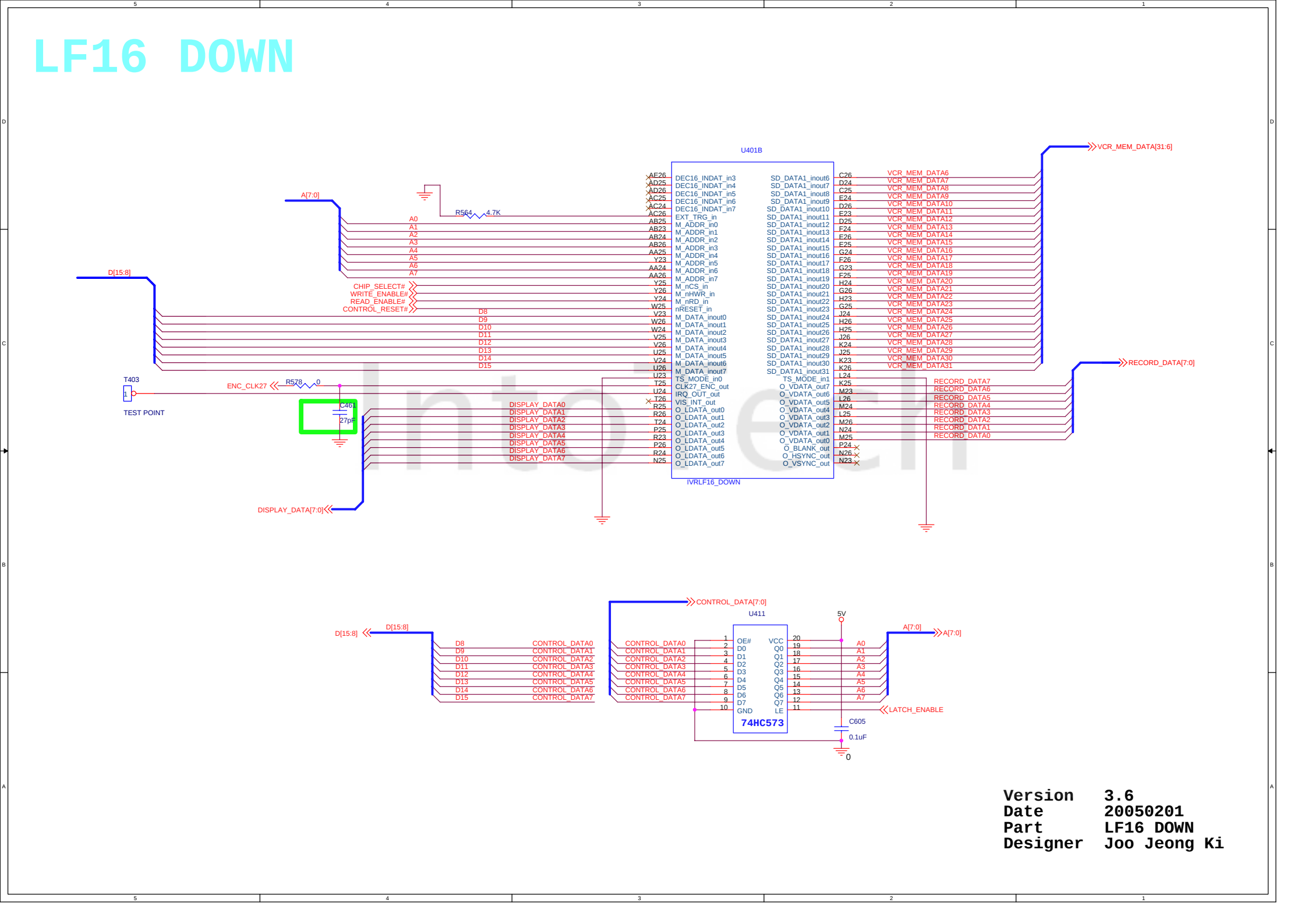


LF16 LEFT



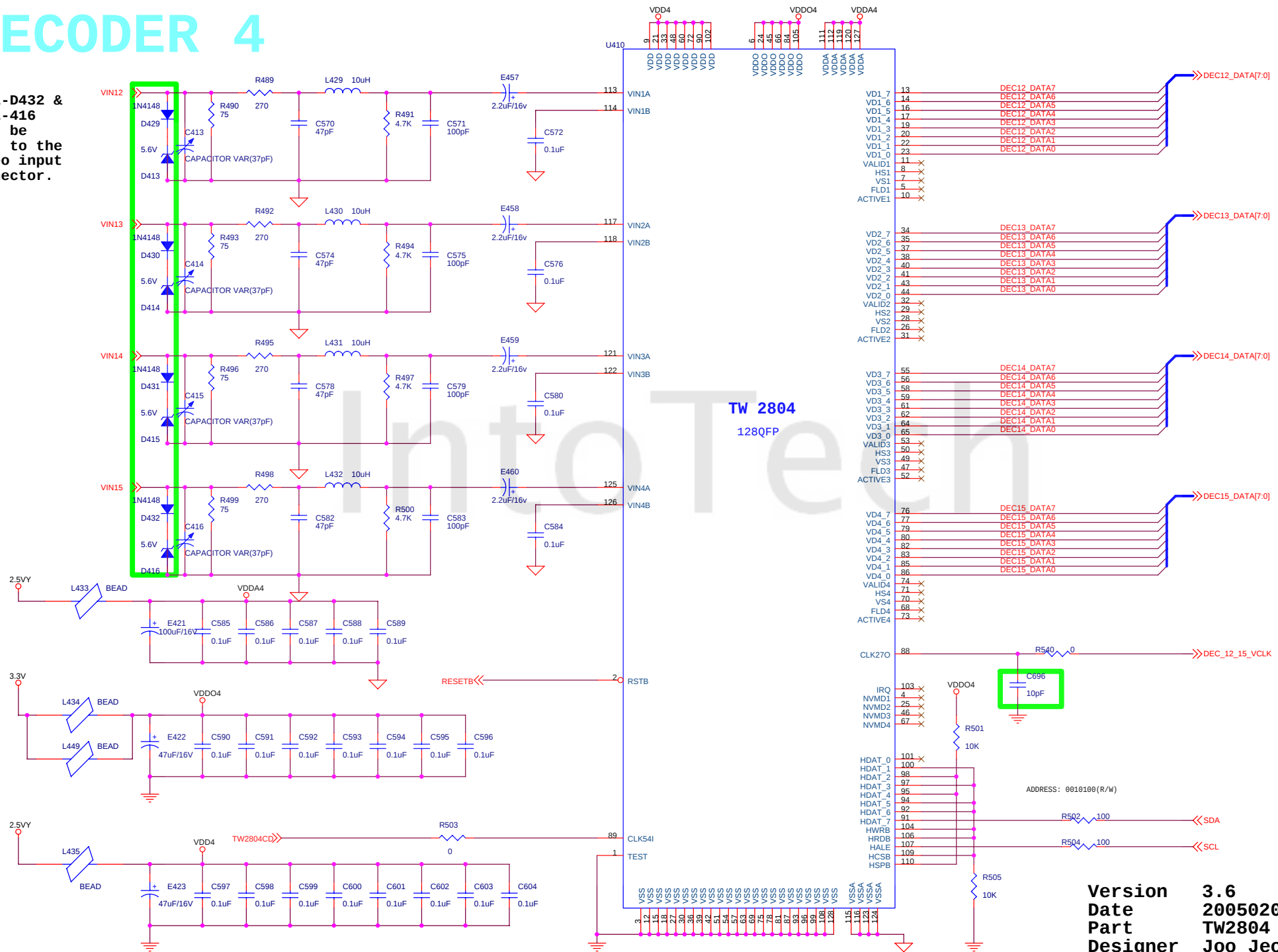
LF16 RIGHT

Version 3.6
Date 20050201
Part LF16 LEFT&RIGHT
Designer Joo Jeong Ki

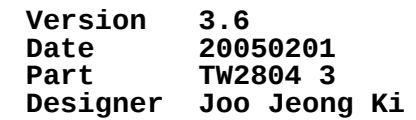
[illegible][illegible]

DECODER 4

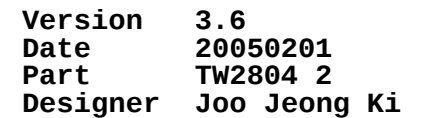
D401-D432 & C401-416 must be near to the video input Connector.



**D401-D432 &
C401-416
must be
near to the
video input
Connector.**

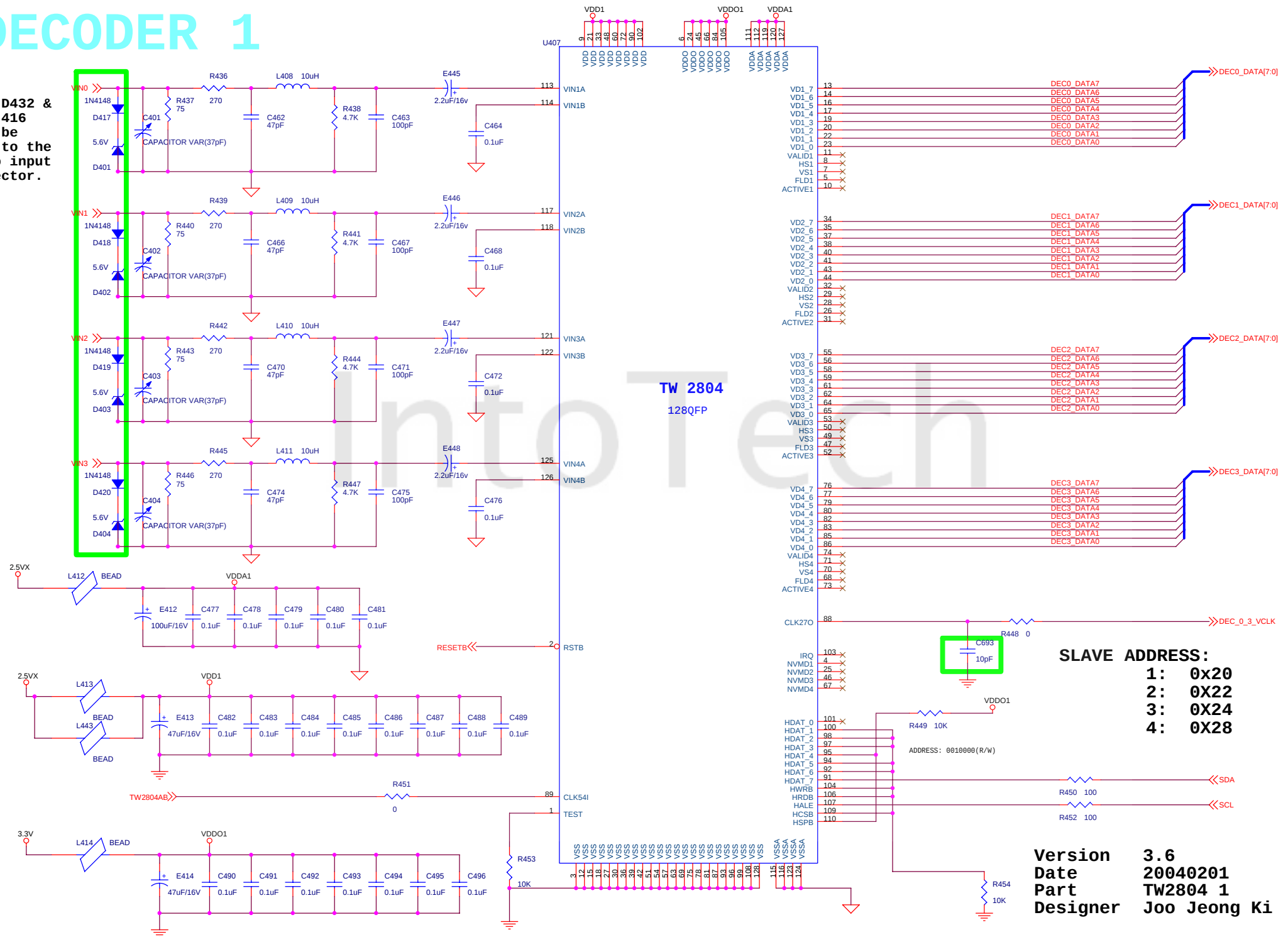


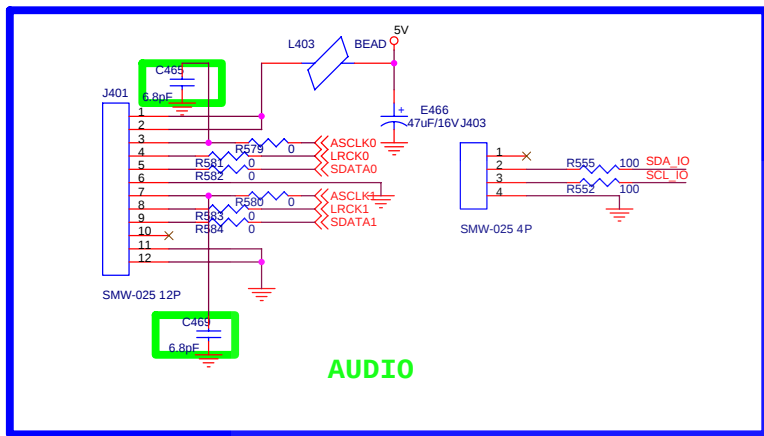
**D401-D432 &
C401-416
must be
near to the
video input
Connector.**



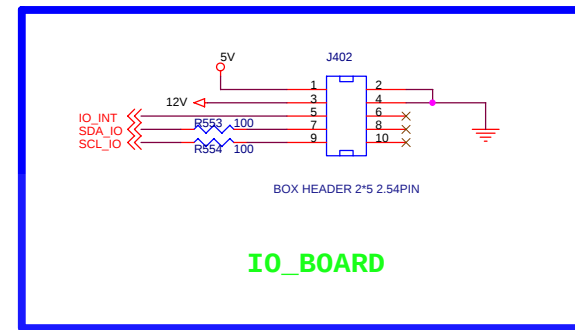
DECODER 1

D401-D432 & C401-416 must be near to the video input Connector.

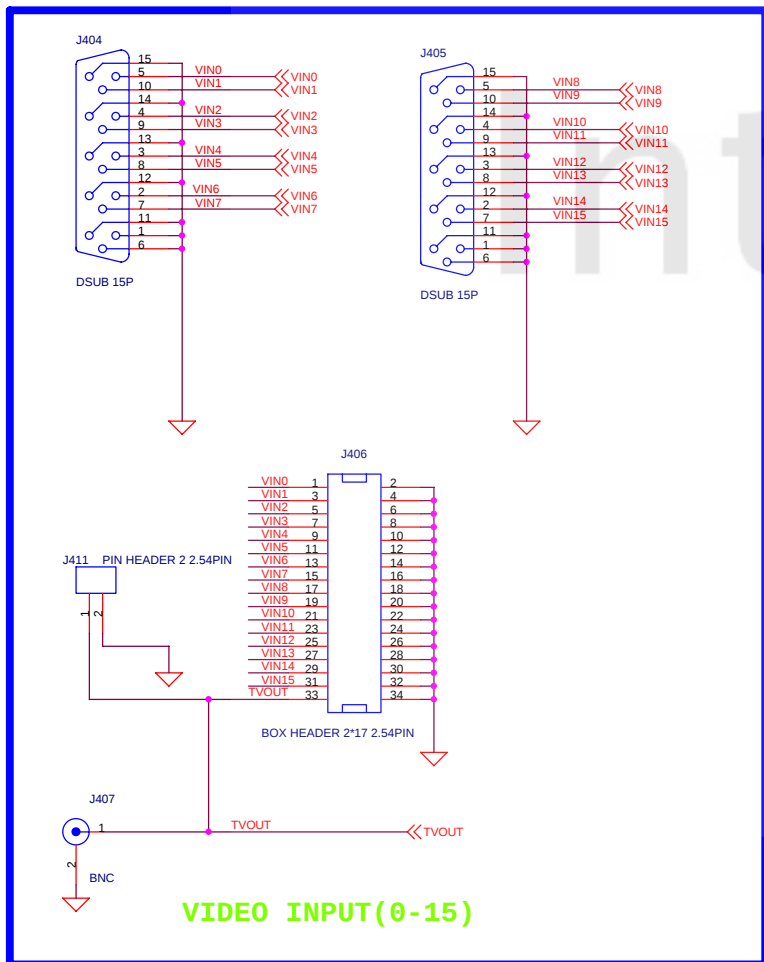




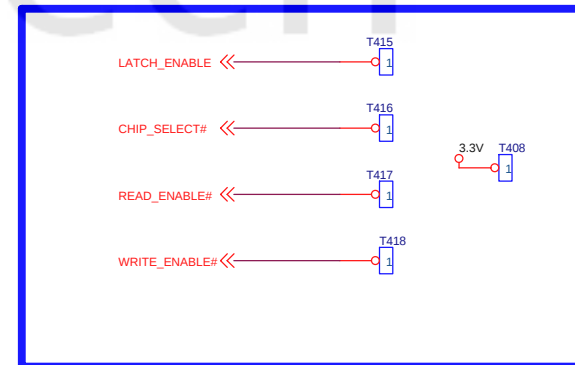
AUDIO



IO_BOARD



VIDEO INPUT(0-15)



Version 3.6
 Date 20050201
 Part CONNECTOR
 Designer Joo Jeong Ki

