

NO.**AT2530**

TITLE: Module, ZigBee, 14.9 x 21.9 x 2.65mm, 45 I/O, Edge Castellated, SMD

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ISSUE	DESCRIPTION OF CHANGES	DATE
0	Initial Release	22 nd May 2013



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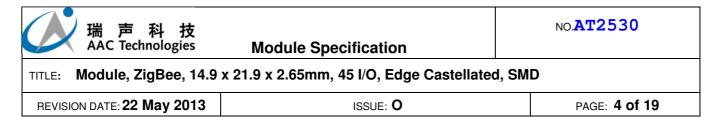
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1.0 Description and Application

1.1 Description

AT2530 module is based on the TI CC2530 ZigBee compliant SOC IC. The IC is a 2.4GHz, IEEE 802.15.4 compliant transceiver with an industry-standard enhanced 8051 MCU, 256 KB of flash and 8 KB of integrated RAM memory. The module is $14.9 \text{mm} \times 21.9 \text{mm} \times 2.65 \text{mm}$ in dimensions with 45 I/O edge castellated interconnect.

1.2 Application

The custom module support user-defined applications, peripherals include five-channel DMA, integrated Op-Amp and ultralow-power comparator, general-purpose timers, 12-Bit ADC, USART as well as up to 21 GPIOs. The target applications include Home/Building automation and control, lighting systems, Industrial control and monitoring, Consumer Electronics, Health care and General ZigBee wireless sensor networking.

1.3 Module Block Diagram

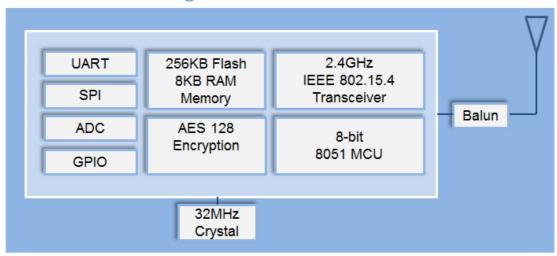
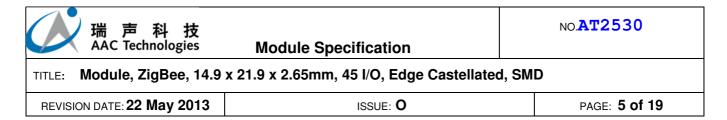
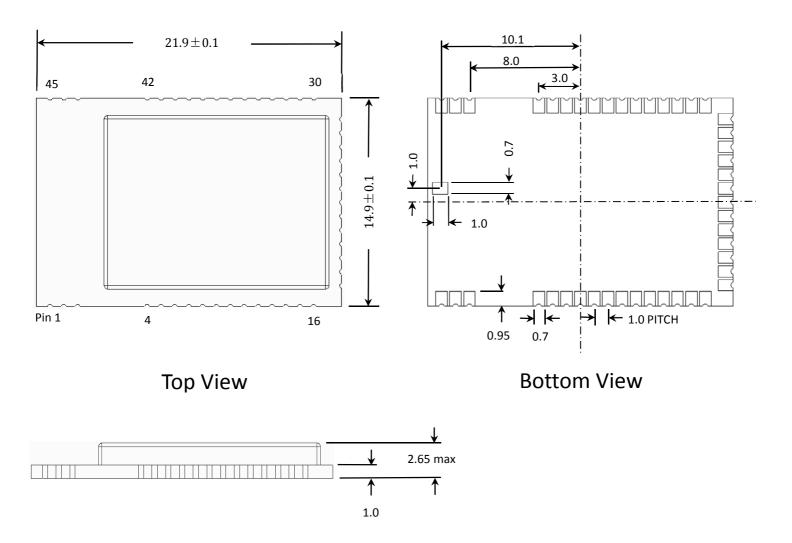


Figure 1: Module Block Diagram



2.0 Mechanical Specifications

2.1 Module Dimensions (mm)



Side View

Figure 2: Module Top, Bottom and Side View

2.2 Module Footprint

Top "see through" view (mm)

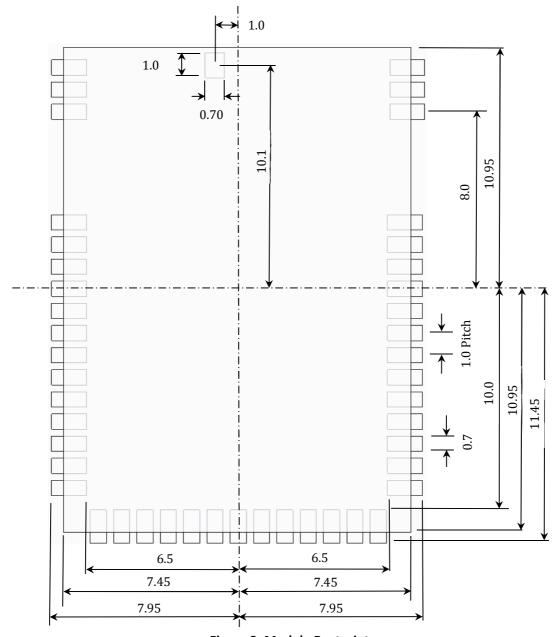


Figure 3: Module Footprint

2.3 Recommended Main PCB Copper Ground Design

(Only applicable to module with internal antenna)

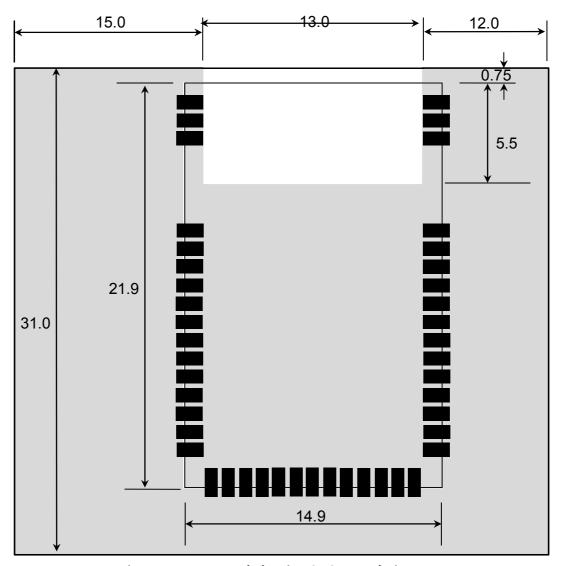


Figure 4: Recommended main PCB Copper design



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2.4 Module Pin-Out

Module Pin	Pin Name	Pin Type	Description
1	GND	GND	Ground
2	GND	GND	Ground
3	GND	GND	Ground
4	GND	GND	Ground
5	VDD	Power	2.0V - 3.6V Power Supply
6	GND	GND	Ground
7	GND	GND	Ground
8	GND	GND	Ground
9	GND	GND	Ground
10	P2_4/XOSC32K_Q1	Digital I/O, Analog I/O	Port2.4/32.768 khz XOSC
11	P2_3/XOSC32K_Q2	Digital I/O, Analog I/O	Port2.3/32.768 khz XOSC
12	GND	GND	Ground
13	P2_2	Digital I/O	Port 2.2 - DC (debug clock)
14	P2_1	Digital I/O	Port 2.1 - DD (debug data)
15	P2_0	Digital I/O	Port 2.0
16	GND	GND	Ground
17	GND	GND	Ground
18	P1_7	Digital I/O	Port 1.7
19	P1_6	Digital I/O	Port 1.6
20	NC	NC	No Connection
21	NC	NC	No Connection
22	P1_5	Digital I/O	Port 1.5
23	P1_4	Digital I/O	Port 1.4
24	P1_3	Digital I/O	Port 1.3
25	P1_2	Digital I/O	Port 1.2
26	P1_1	Digital I/O	Port 1.1 - 20mA drive capability
27	P1_0	Digital I/O	Port 1.0 - 20mA drive capability
28	P0_7	Digital I/O	Port 0.7
29	GND	GND	Ground
30	GND	GND	Ground
31	P0_6	Digital I/O	Port 0.6
32	P0_5	Digital I/O	Port 0.5
33	P0_4	Digital I/O	Port 0.4
34	P0_3	Digital I/O	Port 0.3
35	P0_2	Digital I/O	Port 0.2

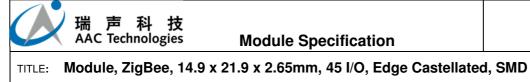


36	P0_1	Digital I/O	Port 0.1
37	P0_0	Digital I/O	Port 0.0
38	RESET_N	Digital Input	Reset, active-low
39	GND	GND	Ground
40	GND	GND	Ground
41	GND	GND	Ground
42	GND	GND	Ground
43	GND	GND	Ground
44	GND	GND	Ground
45	GND	GND	Ground
46	LGA pad	RF	TX/Rx Antenna connection. Not applicable to UFL and internal Chip antenna option

Table 1: Module Signal Description

Notes:

(1) The serial UART connections RXD, TXD, CTS and RTS are P0_2, P0_3, P0_4 and P0_5 respectively.



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Electrical Specifications 3.0

3.1 Absolute Maximum Ratings

PARAMETER	Min	Max	Unit
Supply voltage, VDD	-0.3	3.9	٧
Voltage on any digital pin	-0.3	VDD+0.3, ≤ 3.9	V
Input RF level		10	dBm
Storage Temperature	-40	85	°C

Table 2: Absolute Maximum Ratings

3.2 **Environmental Characteristics**

PARAMETER	MIN	MAX	UNIT
ESD on any pad (human body model)		2	kV
ESD on any pad (charged device model) 500		V	
Moisture Sensitivity Level	3		

Table 3: Environment Characteristics

3.3 **Operating Conditions**

PARAMETER	Min	Тур	Max	Unit
Power supply	2.0	3.0	3.6	V
Operating Temperature	-40	25	85	°C

Table 4: Operating Conditions

3.4 DC characteristics

TA = 25C, VDD = 3V, unless otherwise noted.

PARAMETER	TEST CONDITIONS	Min	Тур	Max	Unit
Receive Mode	32MHz XOSC running, radio in RX mode at - 100 dBm input power, no peripherals active, CPU idle		24.3		mA
Transmit Mode	32MHz XOSC running, radio in TX mode, 4.5 dBm output power, no peripherals active, CPU idle		34.0		mA
Sleep Mode	Power mode 2. Digital regulator off, 16MHz RCOSC and 32MHz crystal oscillator off; 32.768KHz XOSC, POR and sleep timer active; RAM and register retention		1	2	uA

Table 5: DC characteristics



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3.5 RF Characteristics

(VDD = 3V, TA = 25°C, Normal Mode measured at the RF connector with 50Ω termination)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Receiver sensitivity	PER = 1%		-97	-92	dBm
Saturation (max input level)	PER =1%		10		dBm
High-Side Adjacent Channel Rejection	Wanted signal –82dBm, adjacent modulated channel at 5MHz, PER =1%		49		dB
Low-Side Adjacent Channel Rejection	Wanted signal –82dBm, adjacent modulated channel at -5MHz, PER =1%		49		dB
2 nd High-Side Adjacent Channel Rejection	Wanted signal –82dBm, adjacent modulated channel at 10MHz, PER =1%		57		dB
2 nd Low-Side Adjacent Channel Rejection	Wanted signal –82dBm, adjacent modulated channel at -10MHz, PER =1%		57		dB
Co-channel rejection	Wanted signal at -82dBm. Undesired signal is an IEEE 802.15.4 modulated at the same frequency as the desired signal. Signal level for PER = 1%		-3		dB
Spurious emission 30 ~ 1000MHz 1 ~ 12.75GHz			-88 -75		dBm dBm

Table 6: Rx Characteristics

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output power	Maximum output-power setting	0	4.5	8	dBm
Programmable output power			32		dB
range					
Error vector magnitude (EVM)	Measured using maximum-recommended		2		%
	output-power setting				70
	Max recommended output power setting				
2 nd Harmonic			-62		dBm
3 rd Harmonic			-78		иын
Spurious emission	Max recommended output power setting				
30 ~ 1000 MHz			-56		dBm
1 ~ 2.5 GHz			-68		dBm
2.5 ~ 12.7 GHz (outside			-69		dBm
restricted bands)					

Table 7: Tx Characteristics

3.6 Transmit Power Characteristics

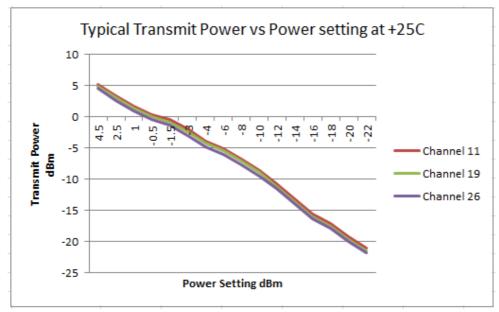


Figure 5: Transmit Power vs Power Setting

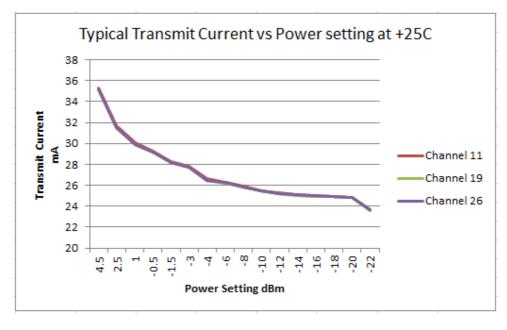
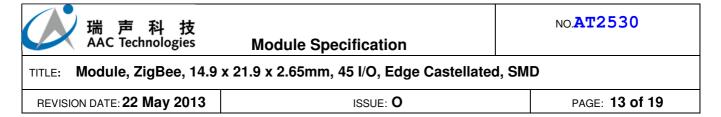


Figure 6: Transmit Current vs Power Setting



4.0 Assembly Information

4.1 Lead-Free soldering reflow profile

Condition	Exposure
Average ramp-up rate (200°C to 250°C)	< 3 °C / second
> 217°C	60-120 seconds
Peak temperature	250 +0/-5°C
Time within 5°C of peak	20-30 seconds
Ramp-down rate (Peak to 50°C)	Less than 6°C / second

Table 8: Reflow Profile Recommendation

5.0 Reliability Tests

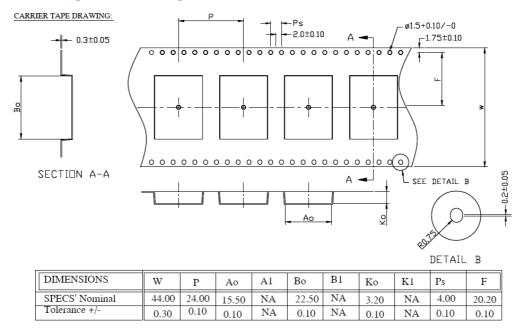
No.	Test item	Test condition	
1	Reflow Thermal Cycle	Normal Pbfree reflow Condition 2 times	
2	Thermal Shock Cycle	30min. at -40°C, 30min. at 85°C, 100Cycles	
		Recovery Time 1hours	
3	Vibration Test	JESD22-B103-B Service Condition 5, 5Hz -> 500Hz, Acceleration 0.3g, 4min/Cycle.	
		Total 4 cycles per axis	
4	High Temperature Storage Test	96 hours at 85°C, Recovery Time 1hours	
5	Low Temperature Storage Test	96 hours at -40°C, Recovery Time 1hours	
6	High Temperature & Humidity Storage Test	96 hours at 60°C & 90%RH±2%RH. Recovery Time 1hours	
7	Drop Test	Drop Test Height min 76 cm, All sides onto metal plate	

Table 9: Reliability Tests



6.0 Packaging Information

6.1 Tape and Reel specification



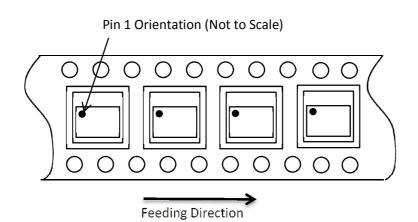
All dimensions in mm

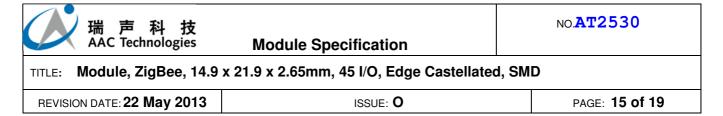
6.2 Component Orientation

Tape Width: 44 mm

Tape Pitch (part to part): 24 mm

Reel Diameter: 330 mm





6.3 Module Marking

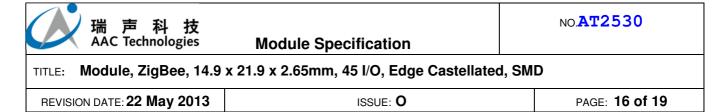
AT2530 - Module Model Number

1013 - Date Code (Example: 10-Workweek, 13-Year)

XXXX - Customer Program information

AT2530 1013 FCC ID: S2J-2530

XXXX



7.0 Regulatory Information

7.1 FCC Notice

This module complies with Part 15 of the FCC rules and regulations. Compliance with the labeling requirements, FCC notice and antenna usage guideline is required. The OEM must comply with the following regulations:

- 1. The OEM integrator must ensure that the text on the external label provided with this device is placed on the outside of the final product.
- 2. AT2530 module may only be used with antenna that have been tested and approved for use with this module.

FCC Caution: Any changes or modifications not expressly approval by the party responsible for compliance could void the user's authority to operate this device.

7.2 OEM Labeling Requirements

The Original Equipment Manufacturing (OEM) must ensure that FCC labeling requirements are met. This includes a clearly visible label on the outside of the finished product specifying the AT2530 FCC identifier (FCC ID: S2J-2530).

Below FCC label statements are required for OEM products containing AT2530 Module.

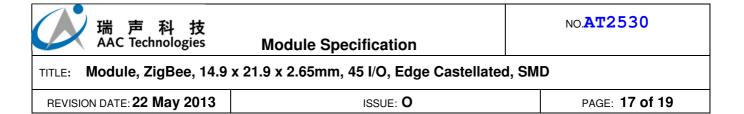
Contains FCC ID: S2J-2530

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference and (2) This device must accept any interference received, including interference that may cause undesired operation.

7.3 RF Exposure

This module has been certified for remote and base radio applications and is not intended to be operated within 20cm of the body. If the module will be used for portable applications, the OEM or OEM integrator is responsible for passing additional SAR (Specific Absorption Rate) testing in accordance with FCC Rules 2.1093.

The preceding statement must be included as a CAUTION statement in OEM product manuals in order to alert users of FCC RF exposure compliance.



To satisfy FCC RF exposure requirements, a separation distance of 20cm or more should be maintained between the antenna of this device and persons during device operation. To ensure compliance, operation at closer than this distance is not recommended. The antenna used for this transmitter must not be co-located or operating in conjunction with other antennas or transmitters.

7.4 FCC Approved Antenna

AT2530 Module has been tested and approved for use with the following antenna:

- Dipole (2.5dBi, AAC part number A.01.120)

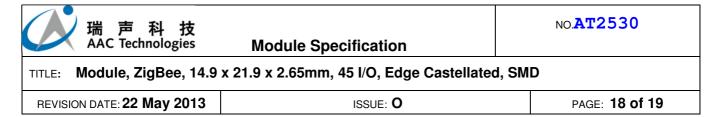
Table 15 shows approved channels of operations with the corresponding maximum output power steps settings.

	Channel 11-25	Channel 26
AT2530	+4.5dBm	-4dBm

Table 15: Maximum Power Settings for FCC Compliance

7.5 CE Notice

The AT2530 module has been tested and certified for use in the European Union. If this module is to be incorporated into a product, the OEM must ensure compliance of the final product to the European Harmonized EMC and low voltage/safety standards. A Declaration of Conformity must be issued for each of these standards and kept on file as described in Annex II of the R&TTE Directive. The final product must not exceed the specified power ratings, antenna specifications and installation requirements as specified in this user manual. If any of these specifications are exceeded in the final product then a submission must be made to a notified body for compliance testing to all of the required standards.



7.6 OEM Labeling Requirements

1. The CE conformity marking must consist of the initials CE taking the following form:



If the CE conformity marking is reduced or enlarged the proportions given in the above graduated drawing must be respected

- 2. Where the directive concerned does not impose specific dimensions, the CE marking must have a height of at least 5 mm.
- 3. The CE marking must be affixed to the product or to its data plate. However, where this is not possible or not warranted on account of the nature of the product, it must be affixed to the packaging, if any, and to the accompanying documents, where the directive concerned provides for such documents.
- 4. The CE marking must be affixed visibly, legibly and indelibly.

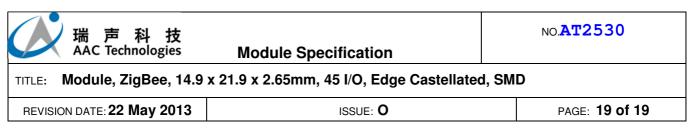
For more information please refer to http://ec.europa.eu/enterprise/faq/ce-mark.htm. Customers assume full responsibility for learning and meeting the required guidelines for each country in their distribution market.

7.7 CE Compliance Power Settings

The following antenna has been tested and approved for use with the AT357 Module:

- Dipole (2.5dBi, AAC part number A.01.120)

Table 16 shows approved channels of operations with the corresponding maximum output power steps settings.



	Channel 11-26
AT2530	+4.5dBm

Table 16: Maximum Power Settings for Europe (ESTI)

8.0 RoHS Declaration

Based on our supplier declarations, this product does not contain substances that are banned by Directive 2002/95/EC or contains a maximum concentration of 0.1% or 1000 ppm by weight in homogeneous materials for:

- Lead and lead compounds
- Mercury and mercury compounds
- Chromium (VI)
- PBB (polybrominated biphenyl)
- PBDE (polybrominated biphenyl ether)

And a maximum concentration of 0.01% or 100 ppm by weight in homogeneous materials for:

• Cadmium and cadmium compounds

9.0 Ordering Information

Module	Description	QTY/Reel	Order Number
	+4.5dBm Output Power ZigBee Module		
AT2530	Based on TI CC2530F256		
	AAC AT Style Command Interpreter	600	R001
	TI Z-Stack		
	U.FL/i-Pex Antenna Connector		

Note:

All Modules are pre-programmed with AAC AT style command interpreter based on the TI Z-stack. Customers who are using their own firmware will be able to erase and write to the flash memory of the CC2530.