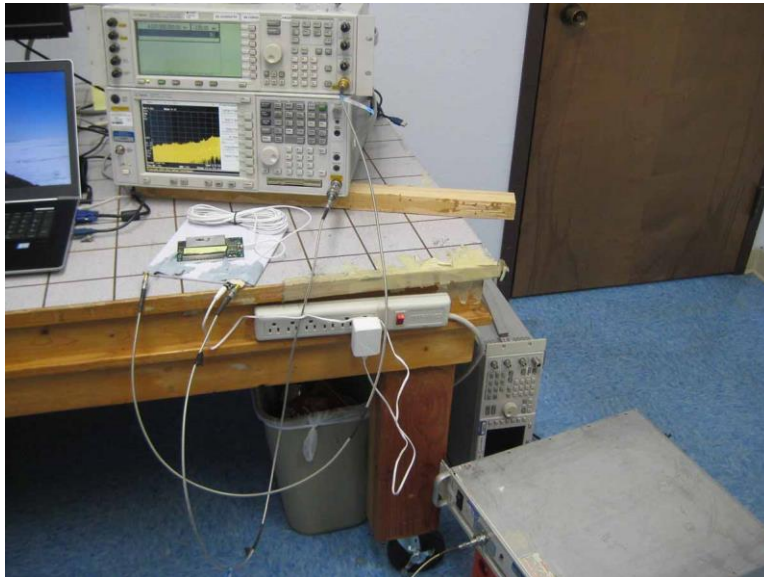




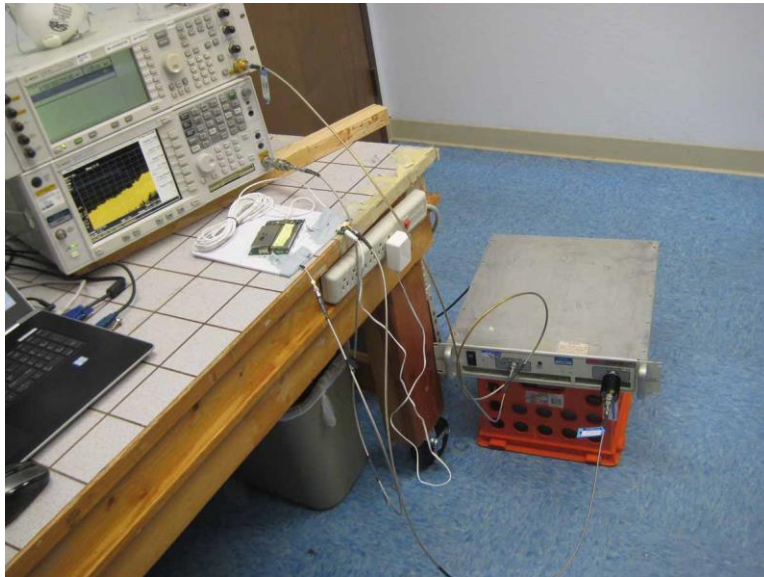
Section 7.1 UL Test Setup



Section 7.1 DL Test Setup



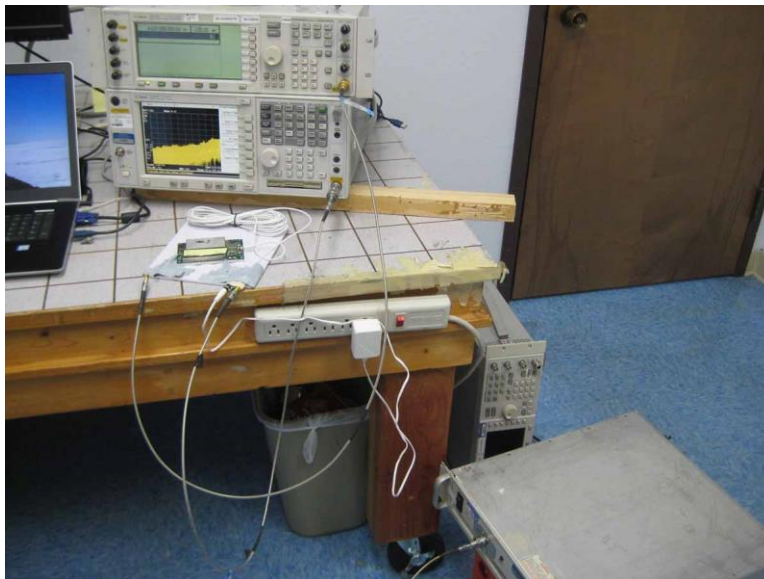
Section 7.2-7.3 UL Test Setup



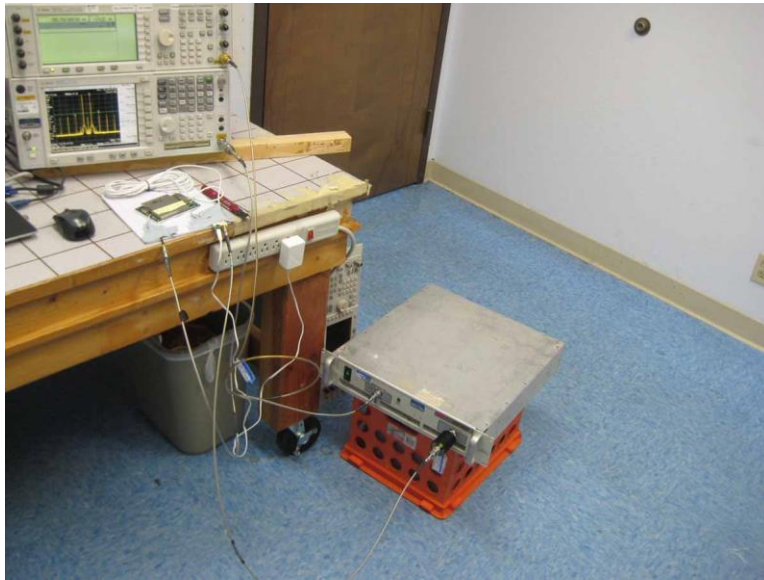
Section 7.2-7.3 UL Test Setup, Max Power



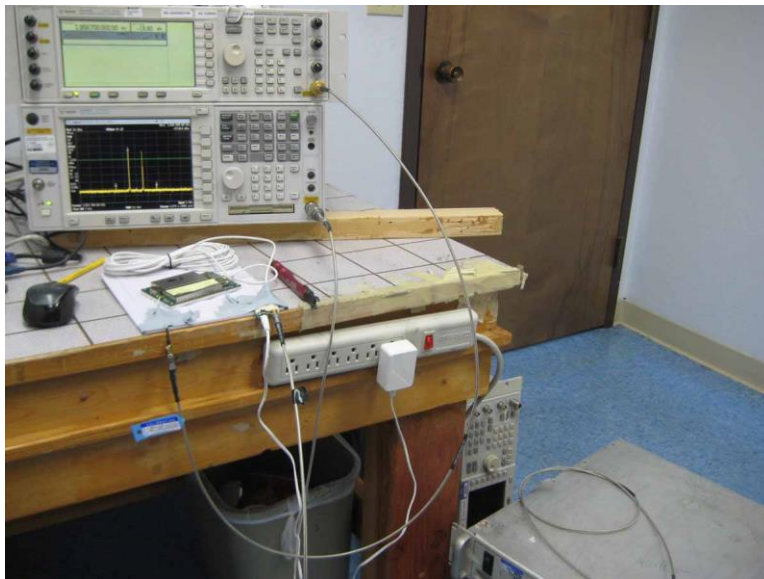
Section 7.2-7.3 UL Test Setup, Max Power Input Signal



Section 7.2-7.3 DL Test Setup



Section 7.4 UL Test Setup



Section 7.4 DL Test Setup



Section 7.5 UL Test Setup



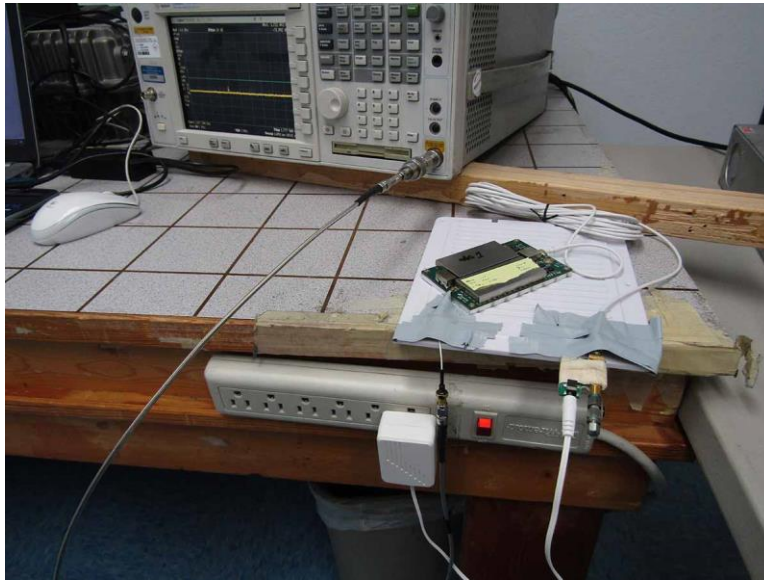
Section 7.5 DL Test Setup



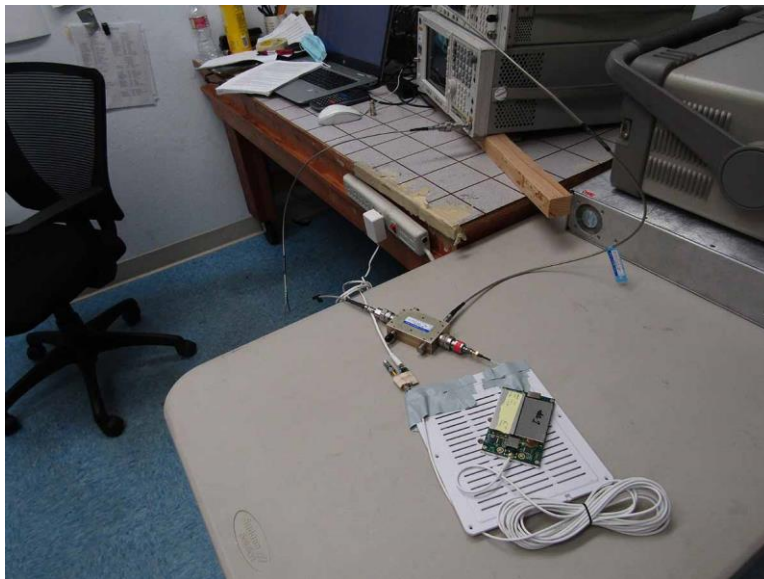
Section 7.6 UL Test Setup



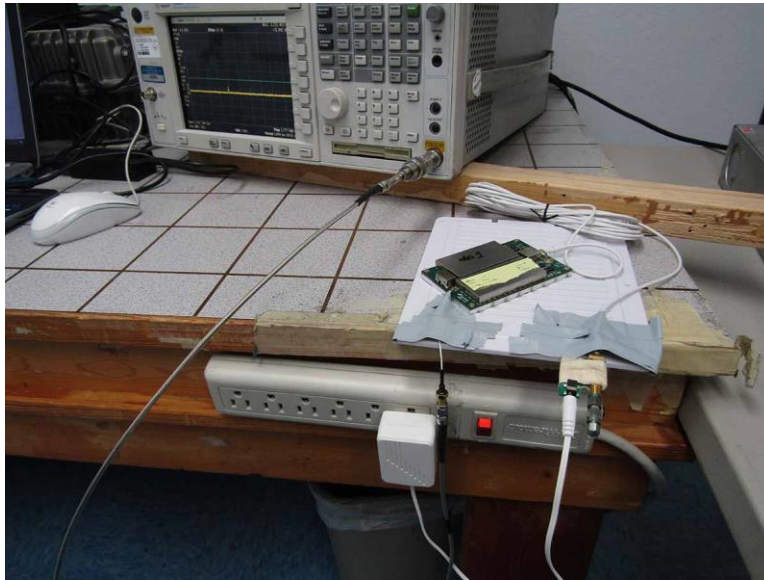
Section 7.6 DL Test Setup



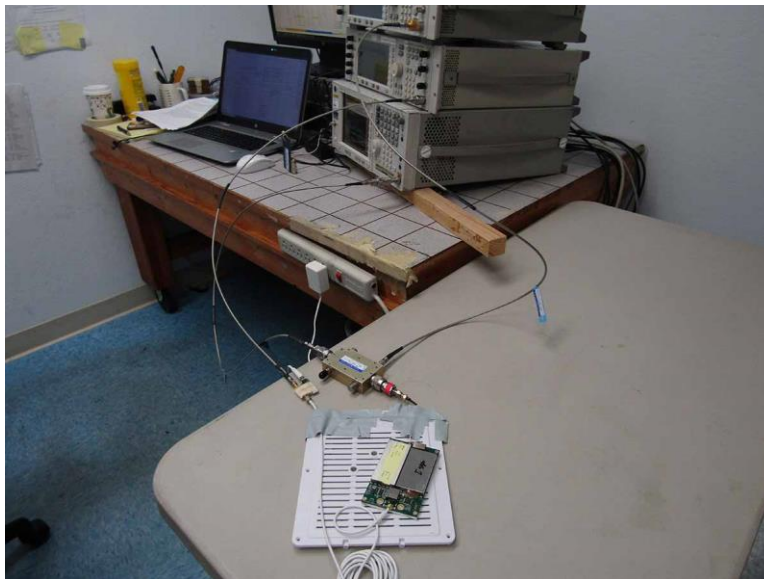
Section 7.7 Max Noise Test Setup



Section 7.7 Variable Max Noise Test Setup



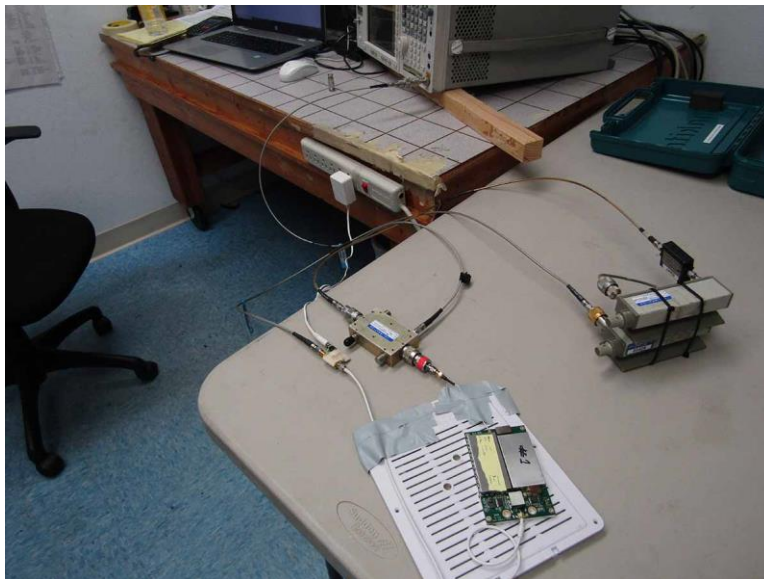
Section 7.8 Test Setup



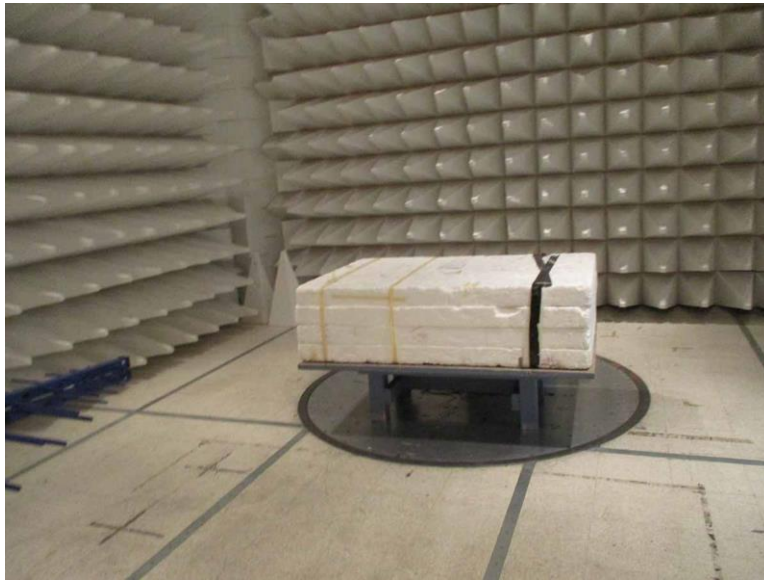
Section 7.9 Test Setup



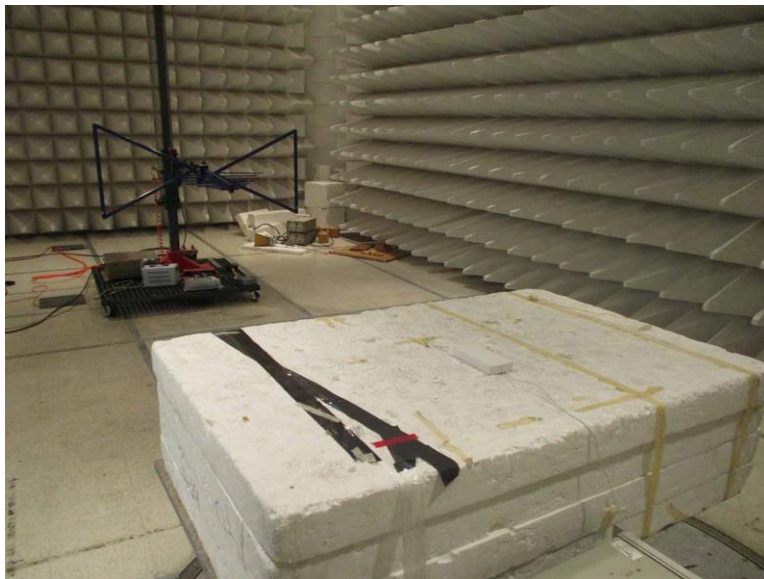
Section 7.10 Test Setup



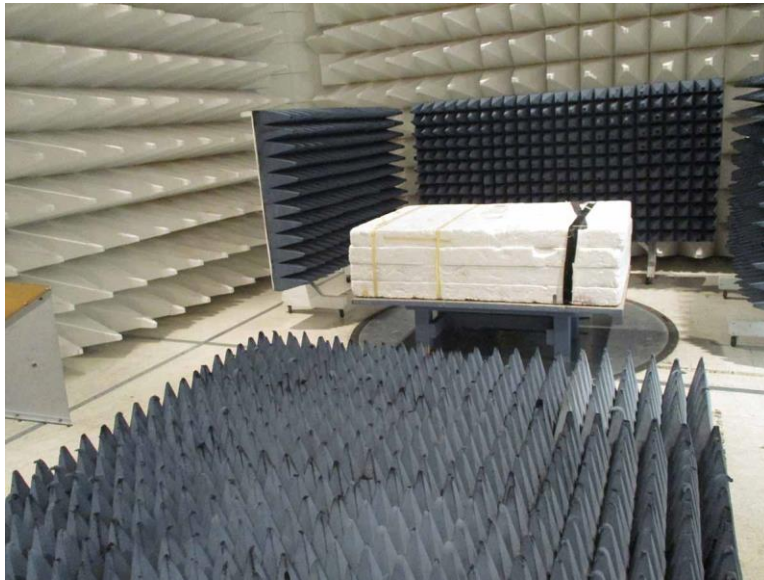
Section 7.11 Test Setup



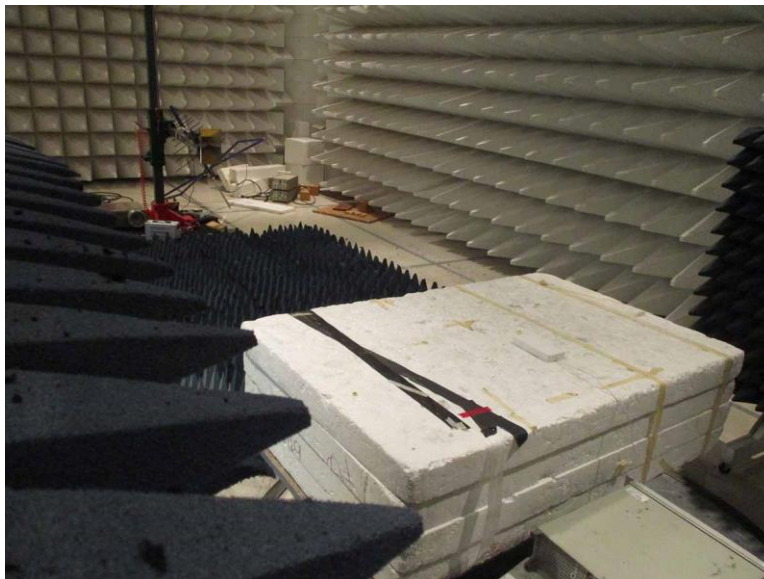
Section 7.12 Below 1GHz



Section 7.12 Below 1GHz



Section 7.12 Above 1GHz



Section 7.12 Above 1GHz