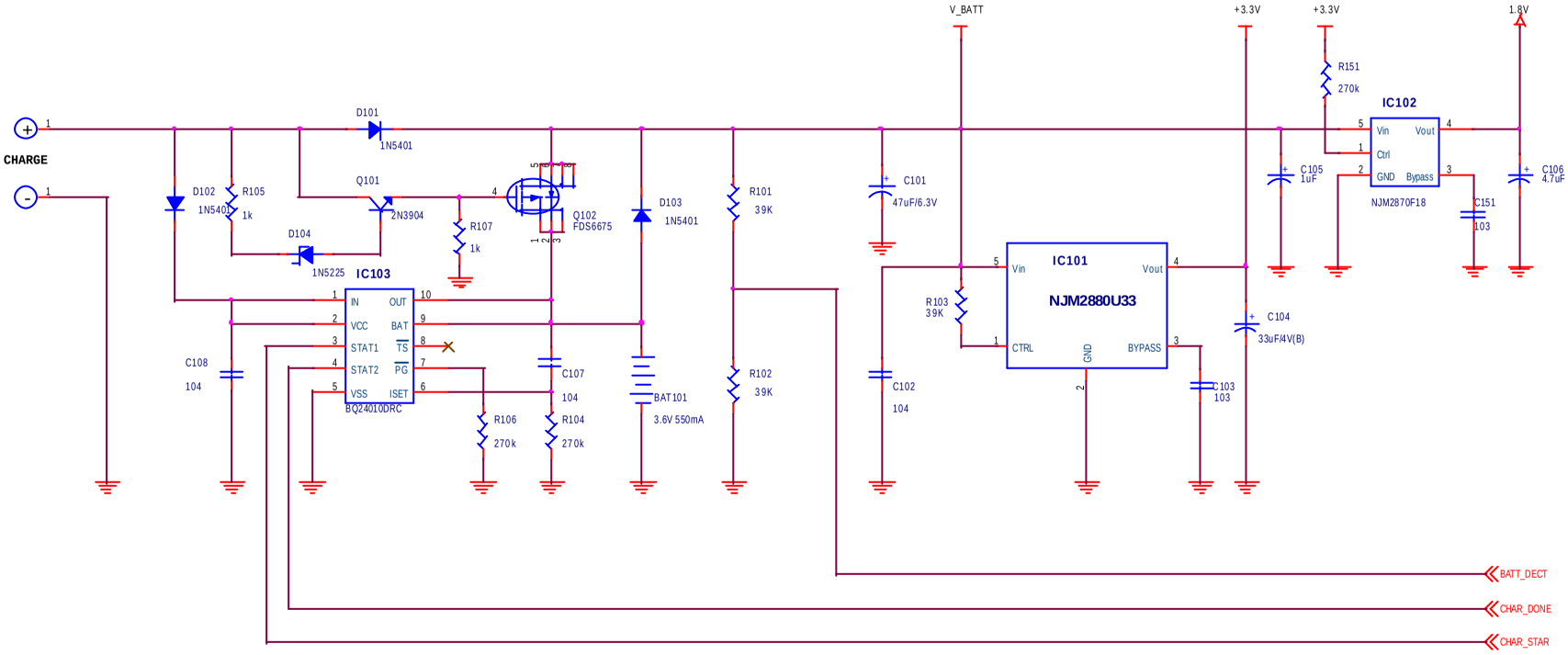
# 2. SCHEMATIC DIAGRAM

## 2-1. REGULATOR

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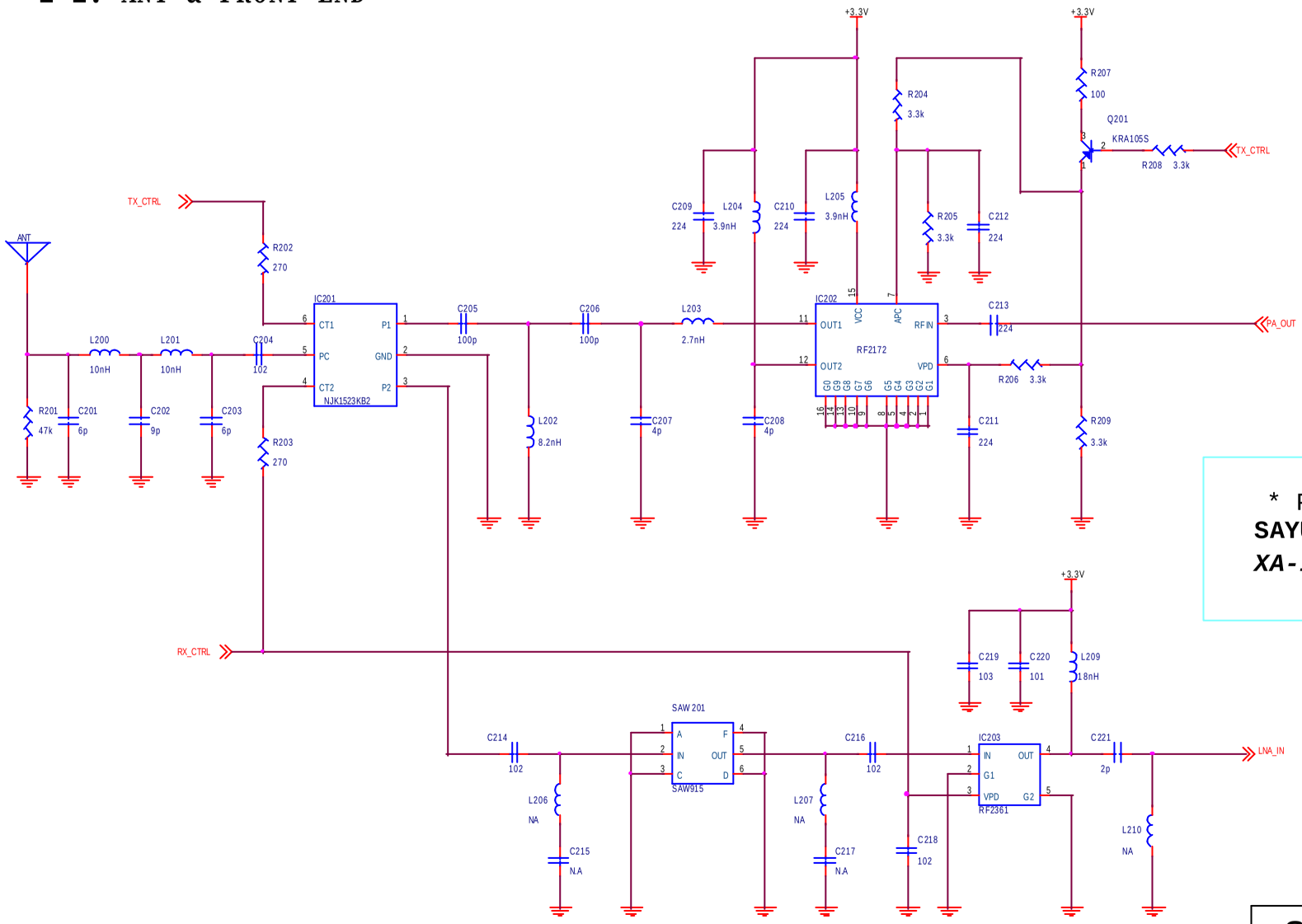
|---|---|---|
| SAYU INC., LTD. | | |
| Title: XA-1000 POWER | | |
| Size: A3 | Document Number: XA-1000 RF B'd.DSN | Rev: 1.2 |
| Date: Wednesday, December 03, 2003 | Sheet 1 of 6 | |

# 2-2. ANT & FRONT END




* PCB Marking "
**SAYU INC., LTD.**
***XA-1000 B'd Rev 1.2***

| | | |
|---|---|---|
| **SAYU INC., LTD.** | | |
| Title: XA-1000 ANT & FRONT END | | |
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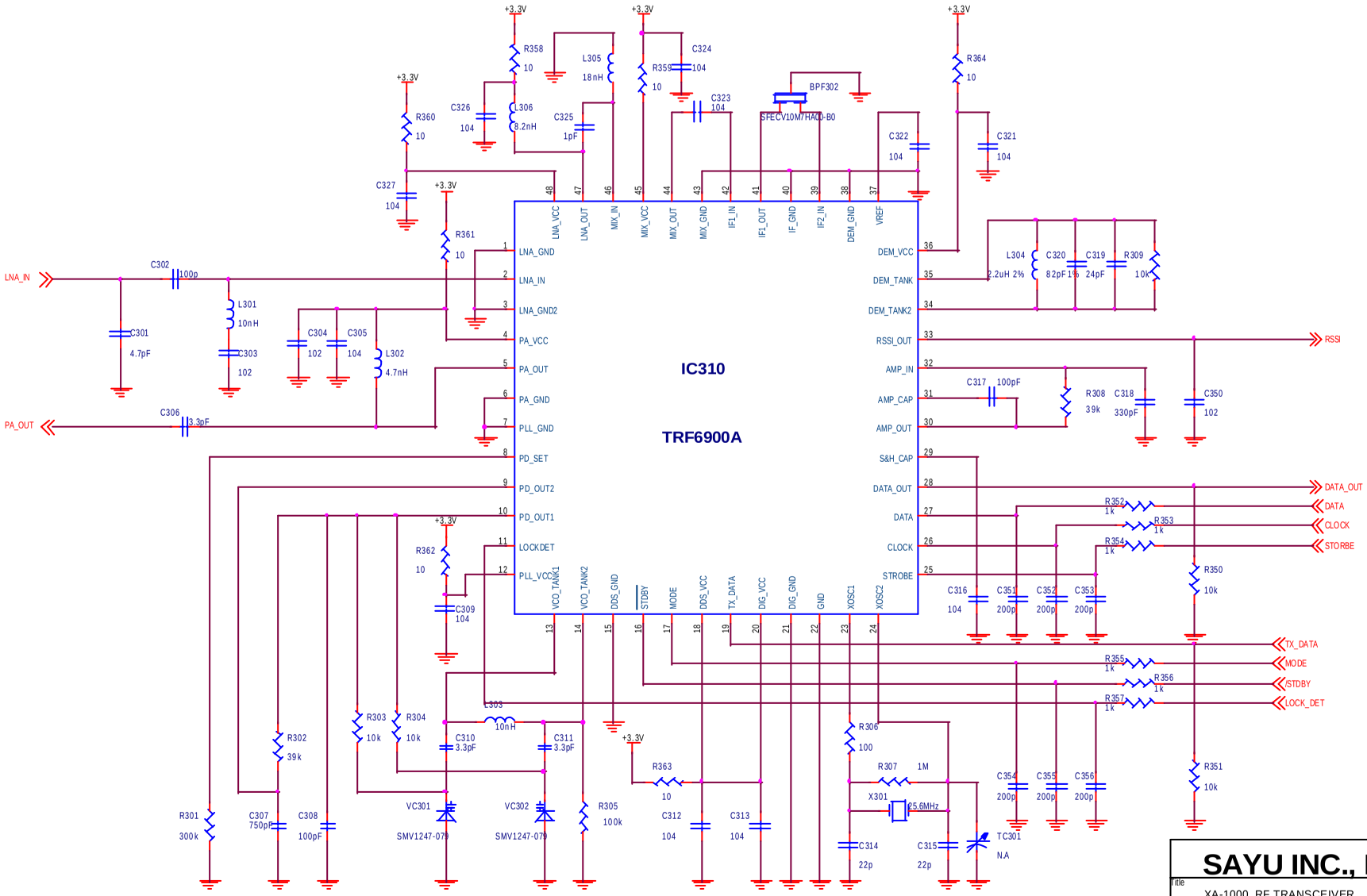
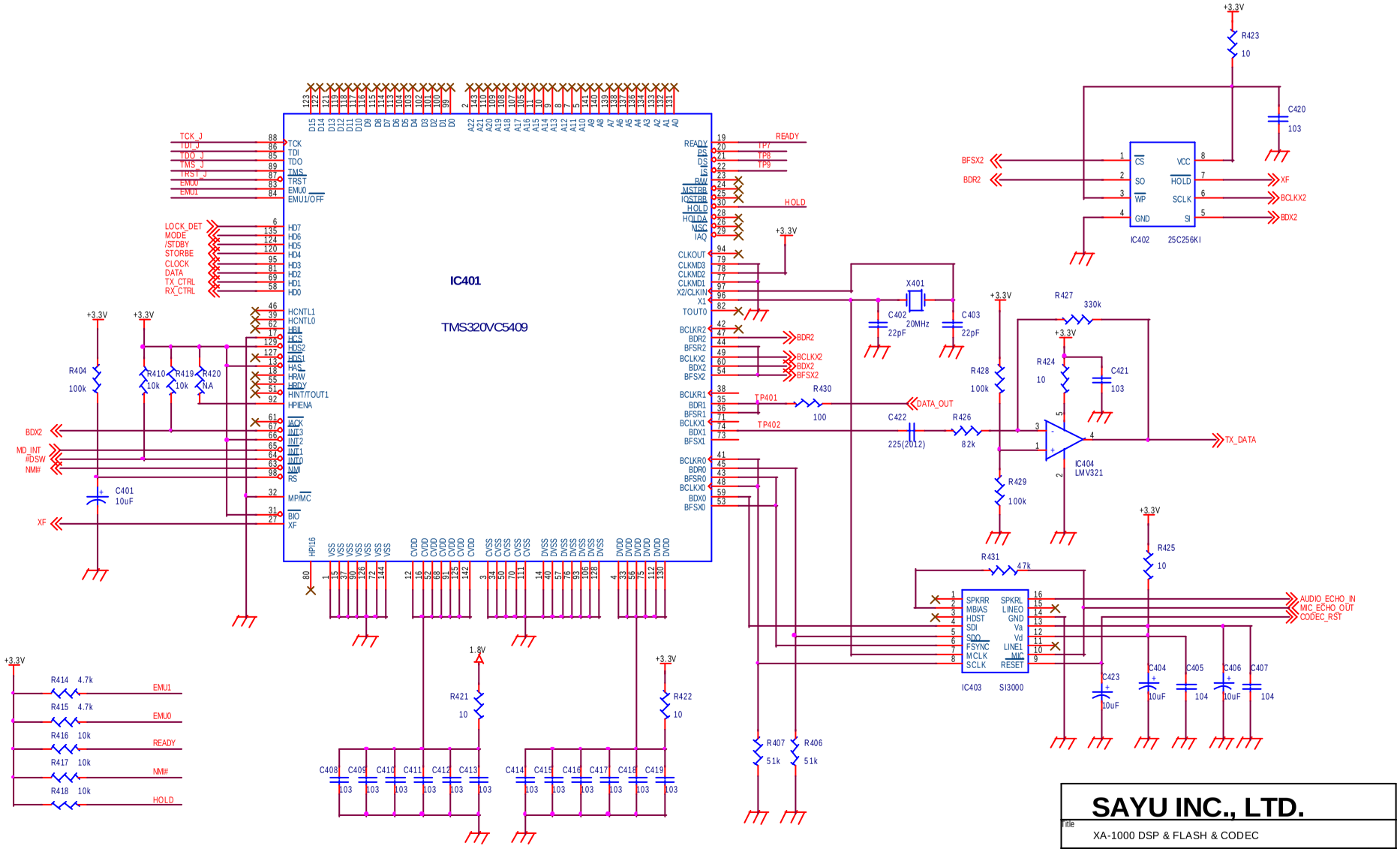
| Date: Wednesday, December 03, 2003 | Sheet 2 of 6 | |

# 2-3. RF TRANSCEIVER



# 2-4. DSP & FLASH & CODEC

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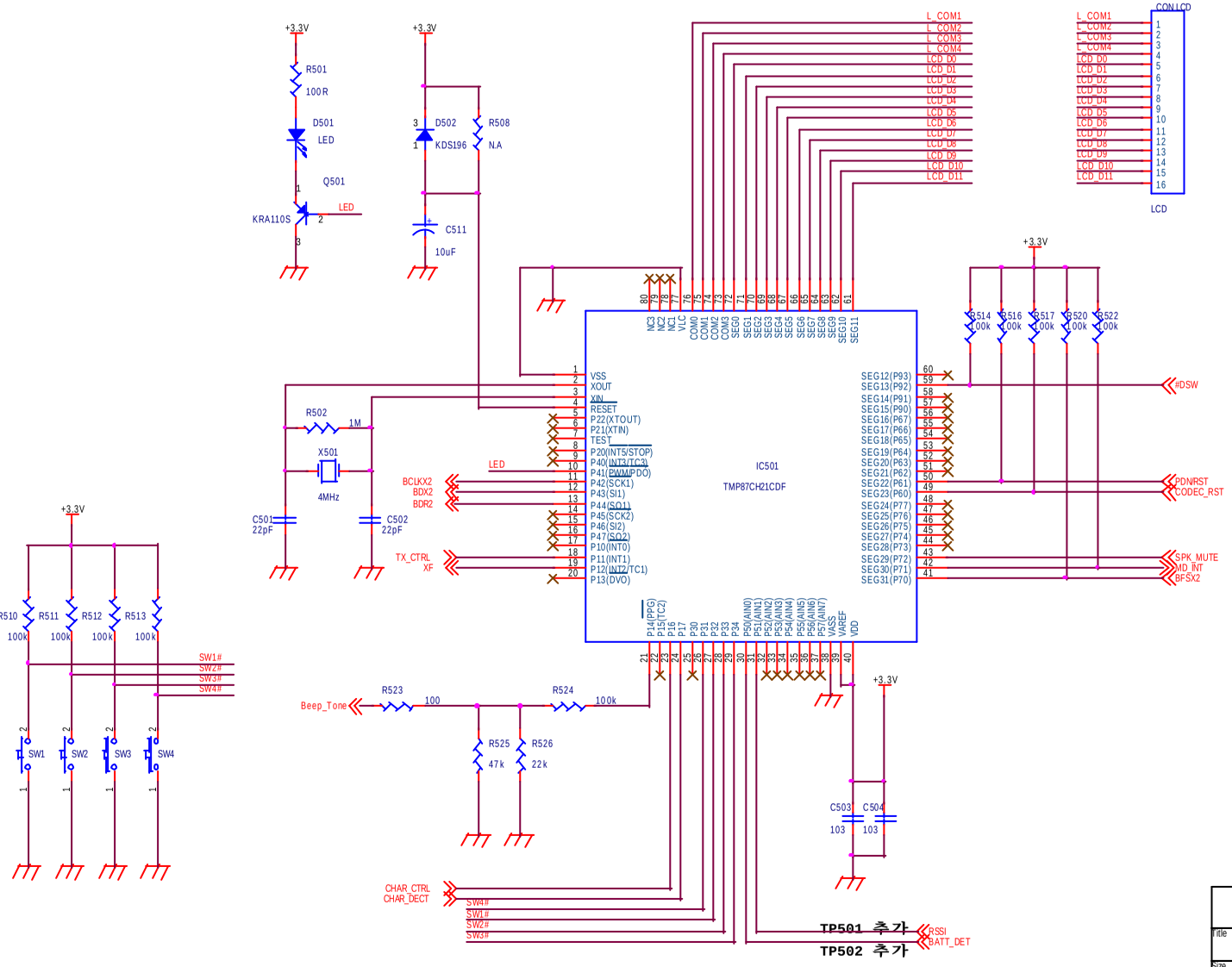
| Date: Wednesday, December 03, 2003 | Sheet 4 of 6 |

# 2-5. MCU & LCD

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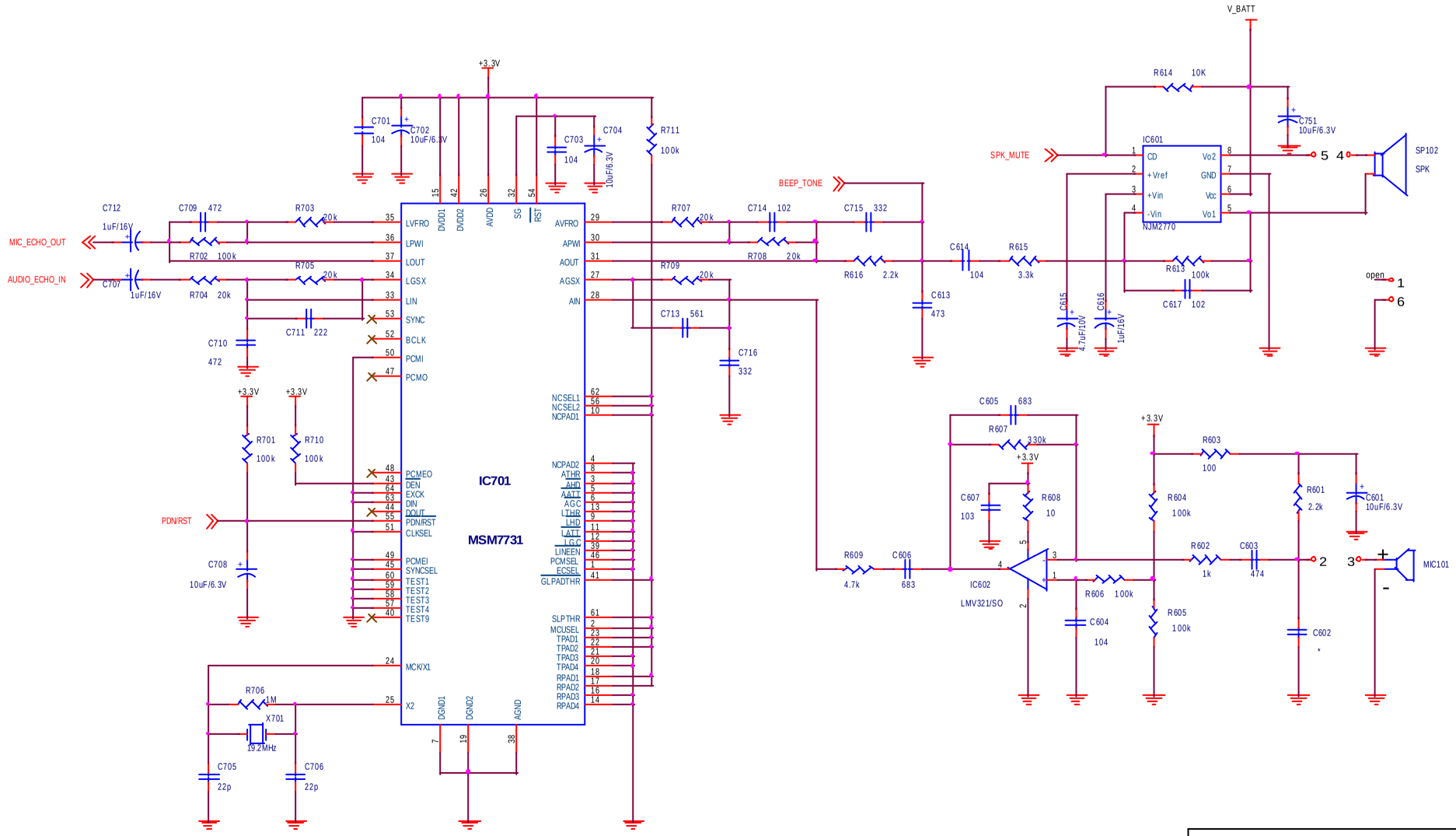
| Date: Wednesday, December 03, 2003 | Sheet 5 of 6 | |

# 2-6. AUDIO _ ECHO

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|---|---|---|
| SAYU INC., LTD. | | |
| Title: XA-1000 AUDIO _ ECHO | | |
| Size: A3 | Document Number: XA-1000 RF B'd.DSN | Rev: 1.2 |
| Date: Wednesday, December 03, 2003 | Sheet 6 of 6 | |