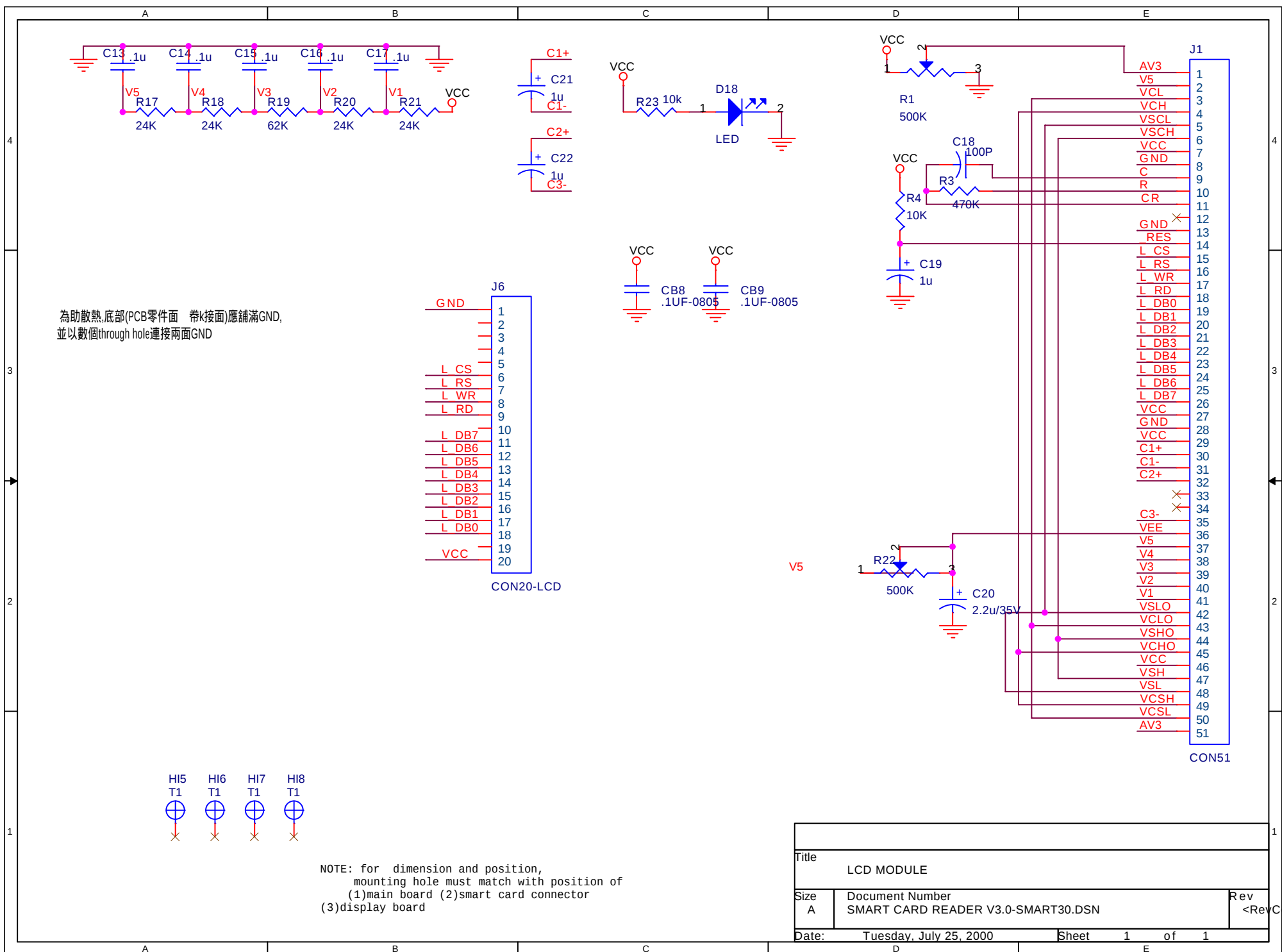
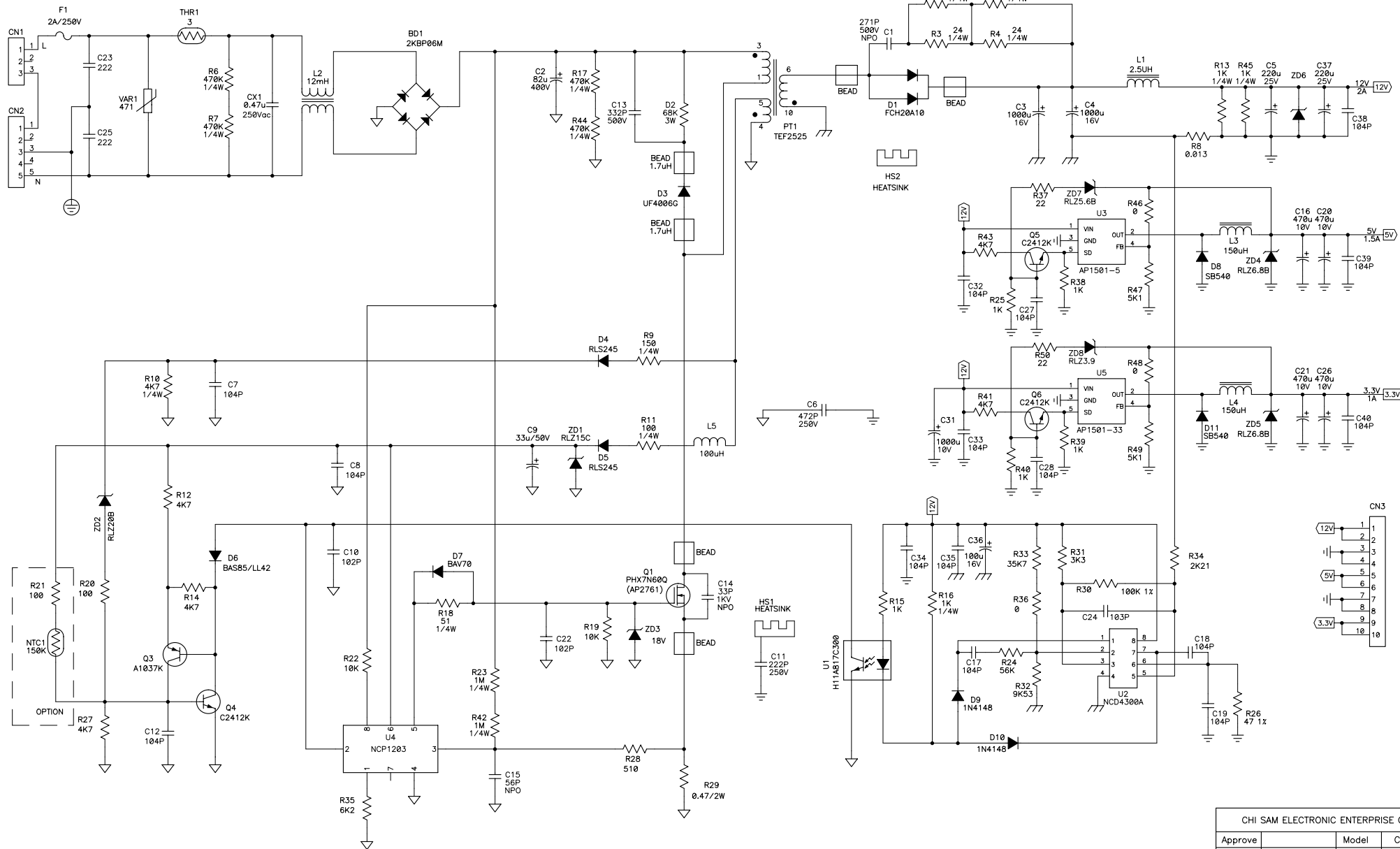
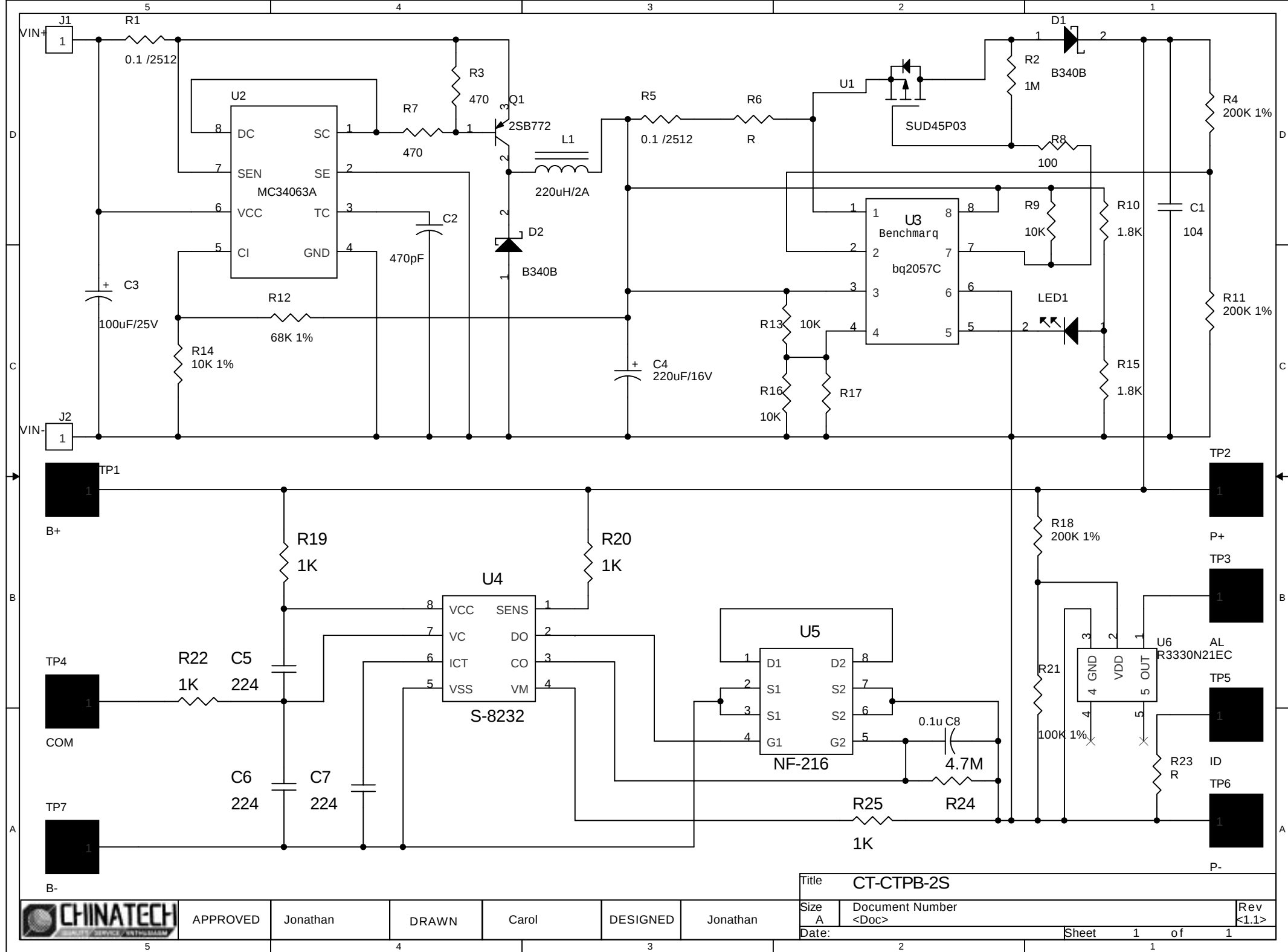


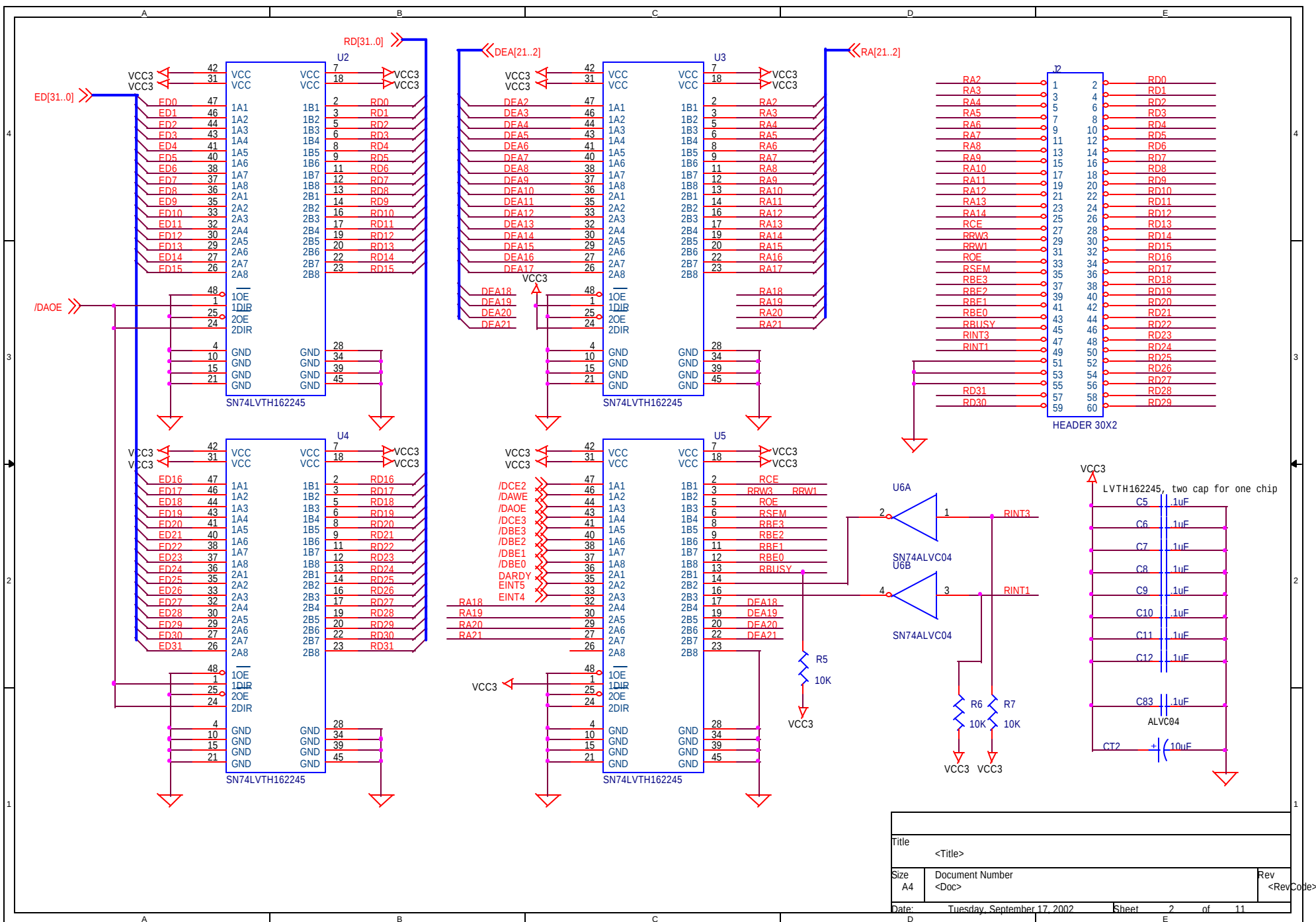


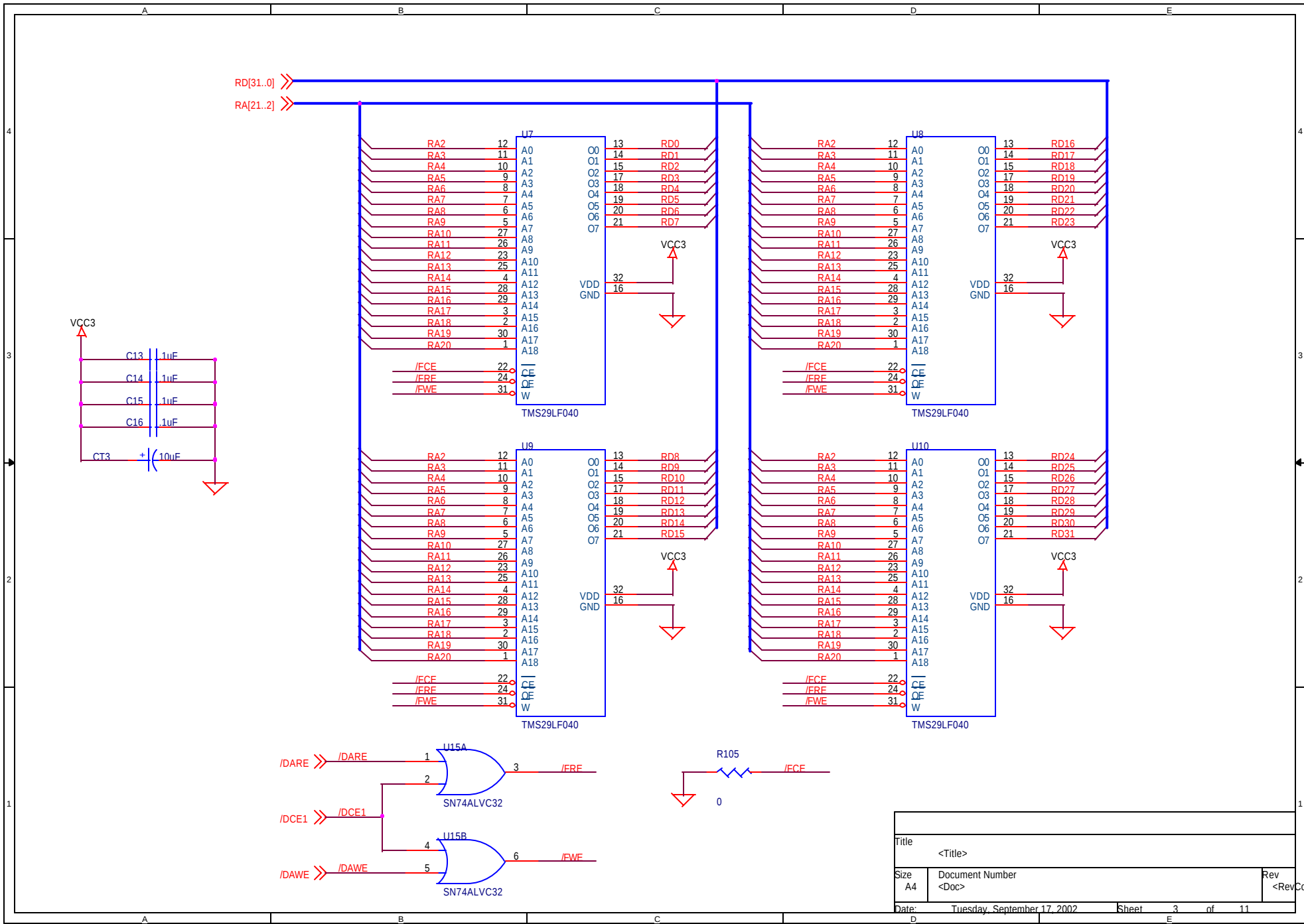
Rev	<RevCode>
-----	-----------



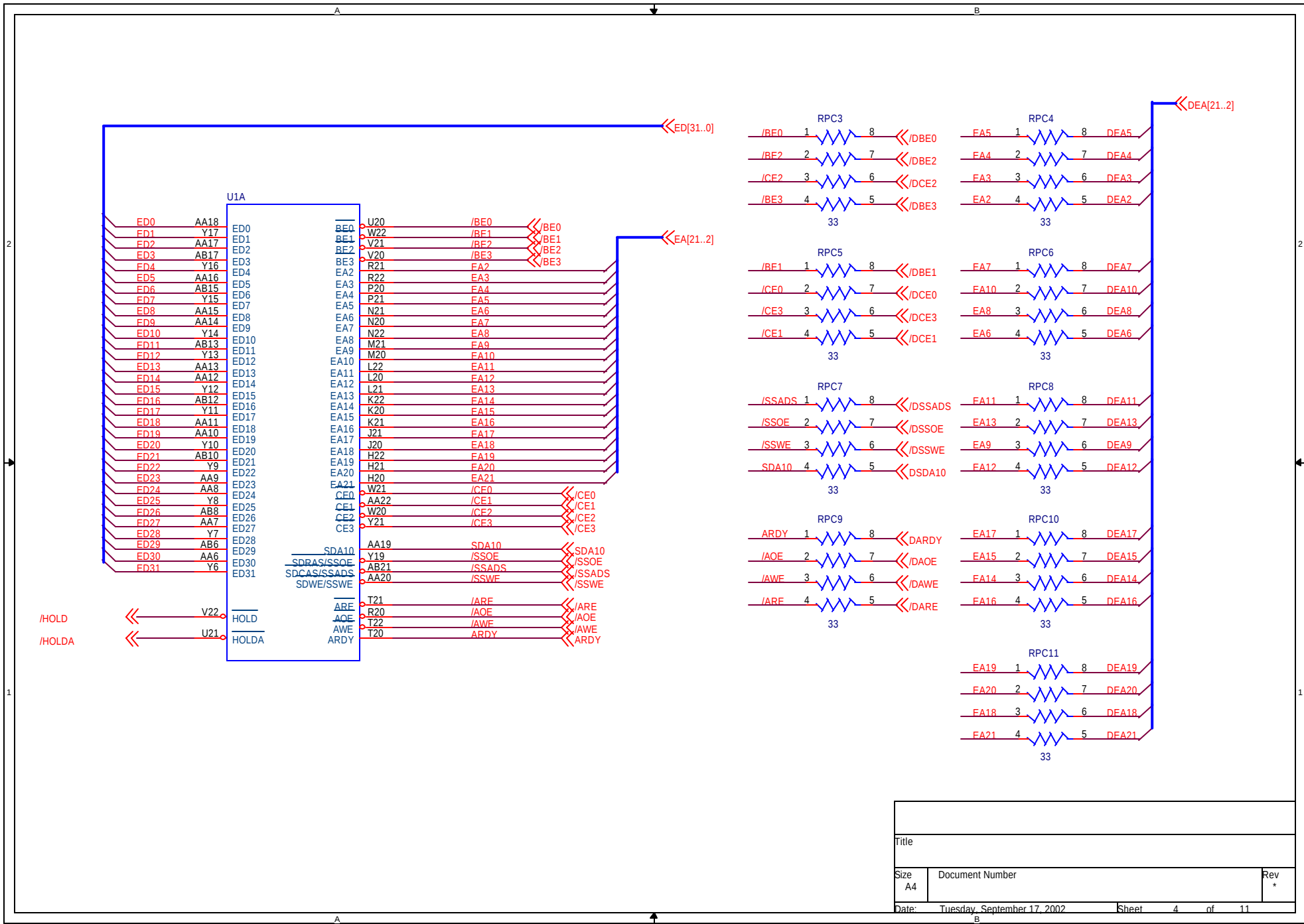
CHI SAM ELECTRONIC ENTERPRISE CO.,LTD.			
Approve		Model	CH-1253TA
Check		Number	
Drawn		Rev	B
Date	2003-12-05	Sheet	1 of 1

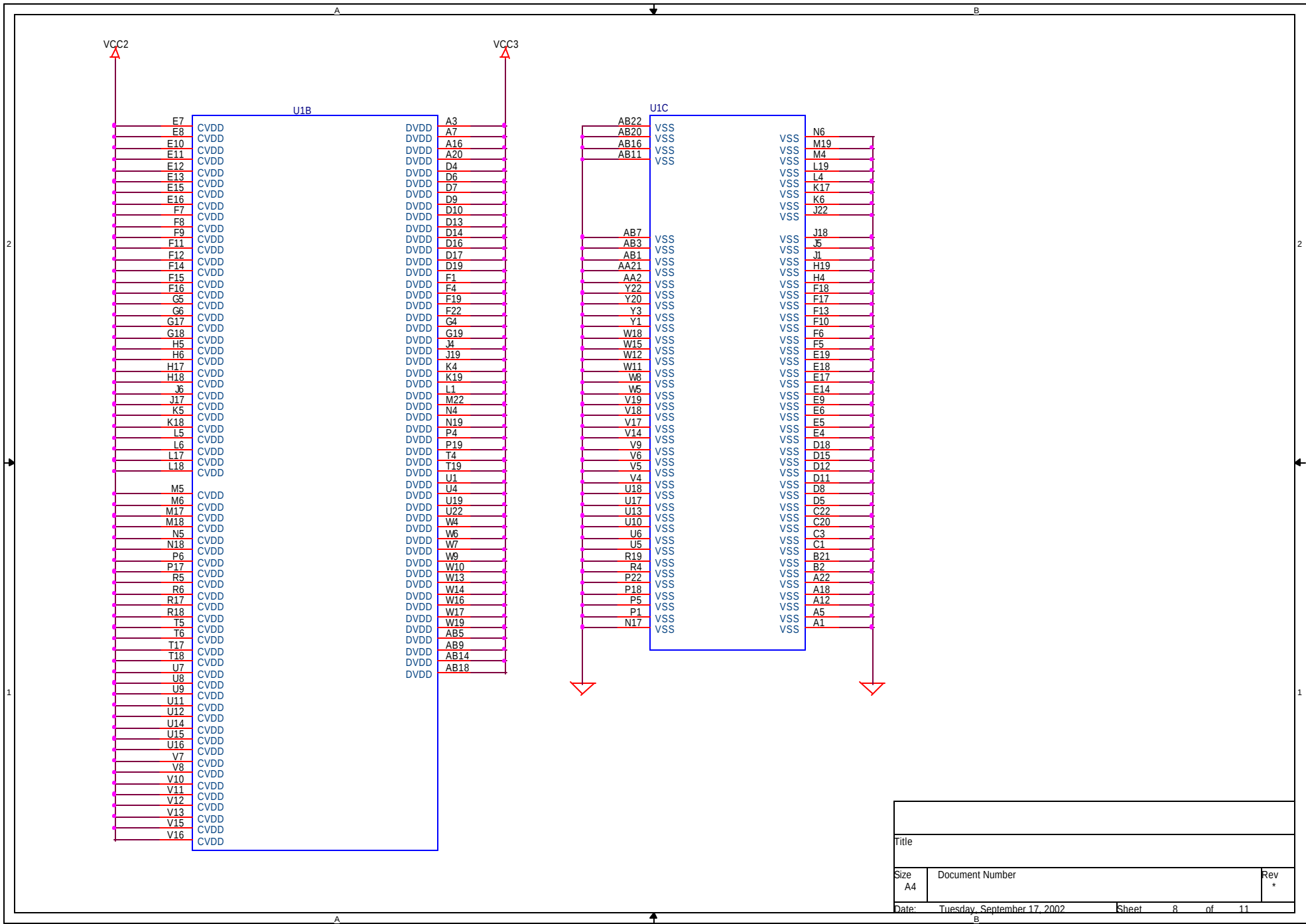




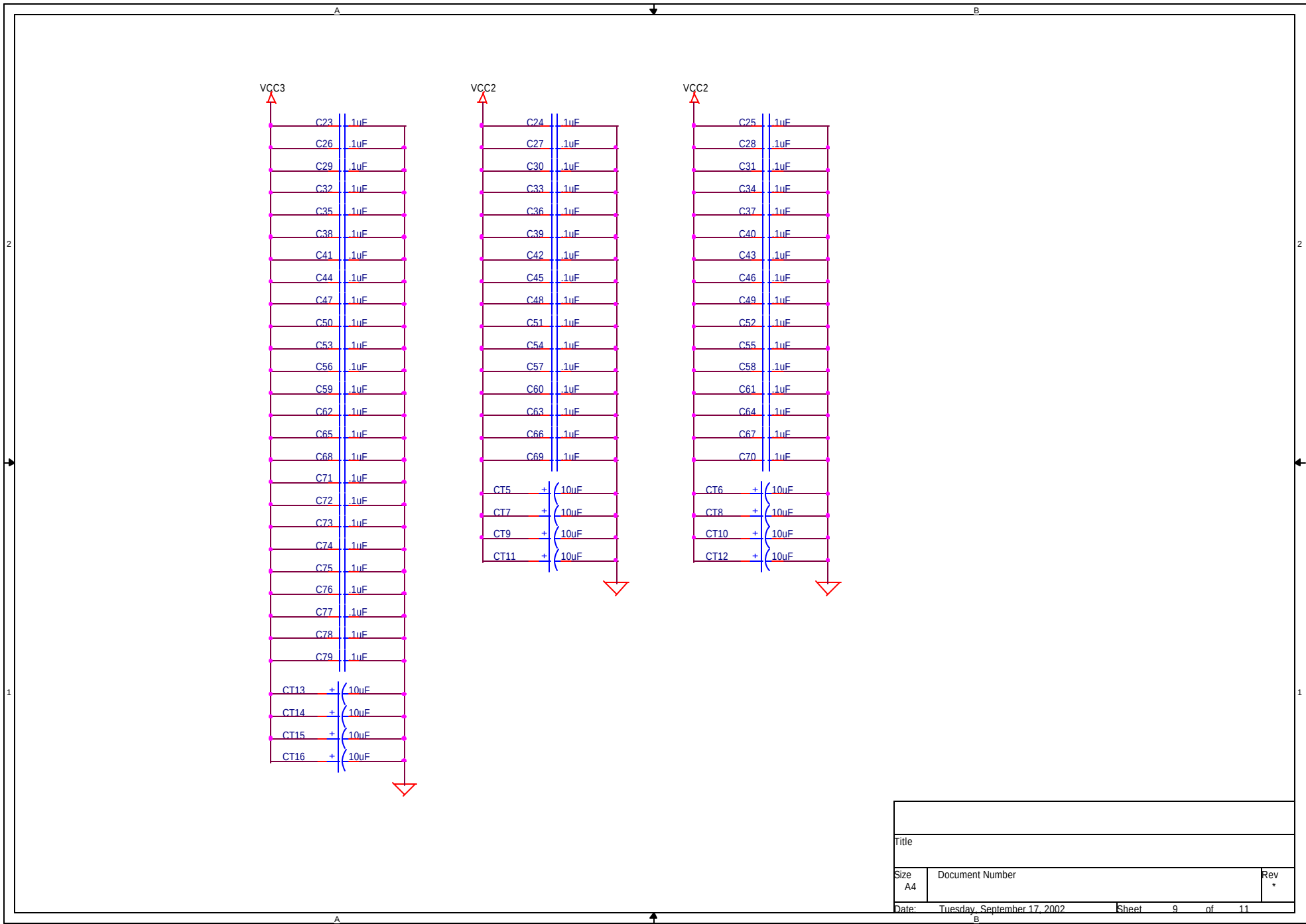


Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	<RevCode>
Date: Tuesday, September 17, 2002		
Sheet 3 of 11		

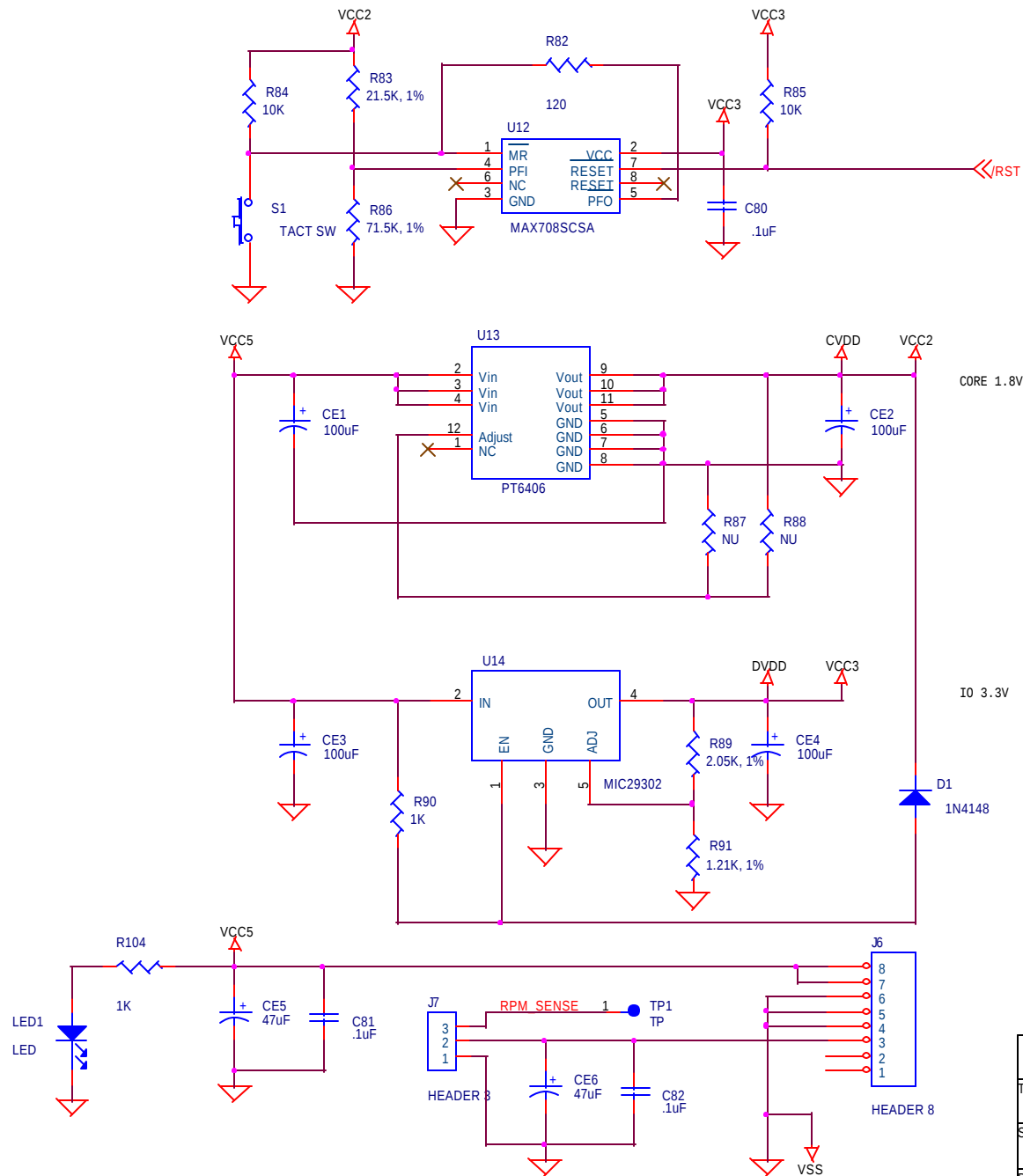




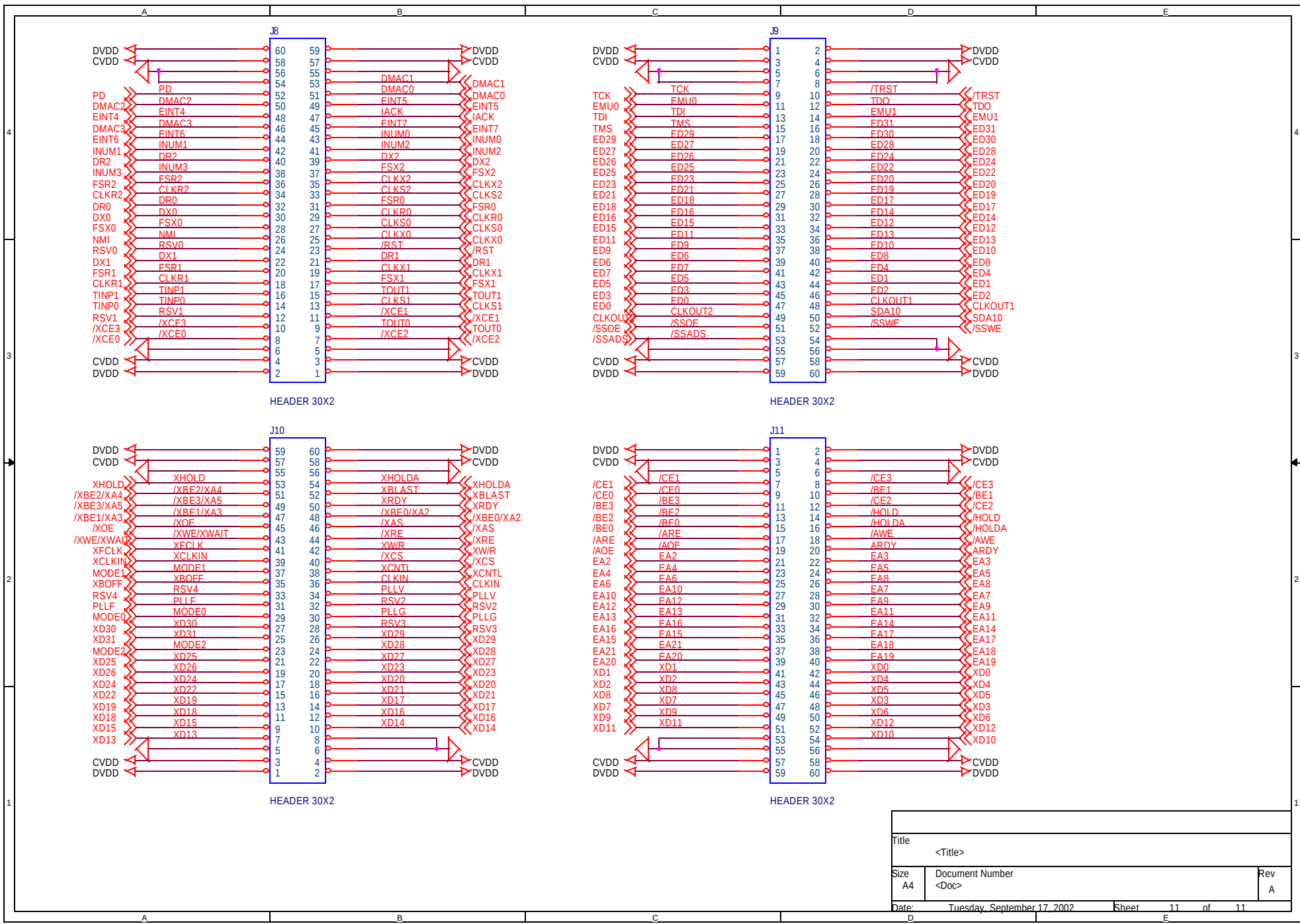
Title		
Size A4	Document Number	Rev *
Date:	Tuesday, September 17, 2002	Sheet 8 of 11



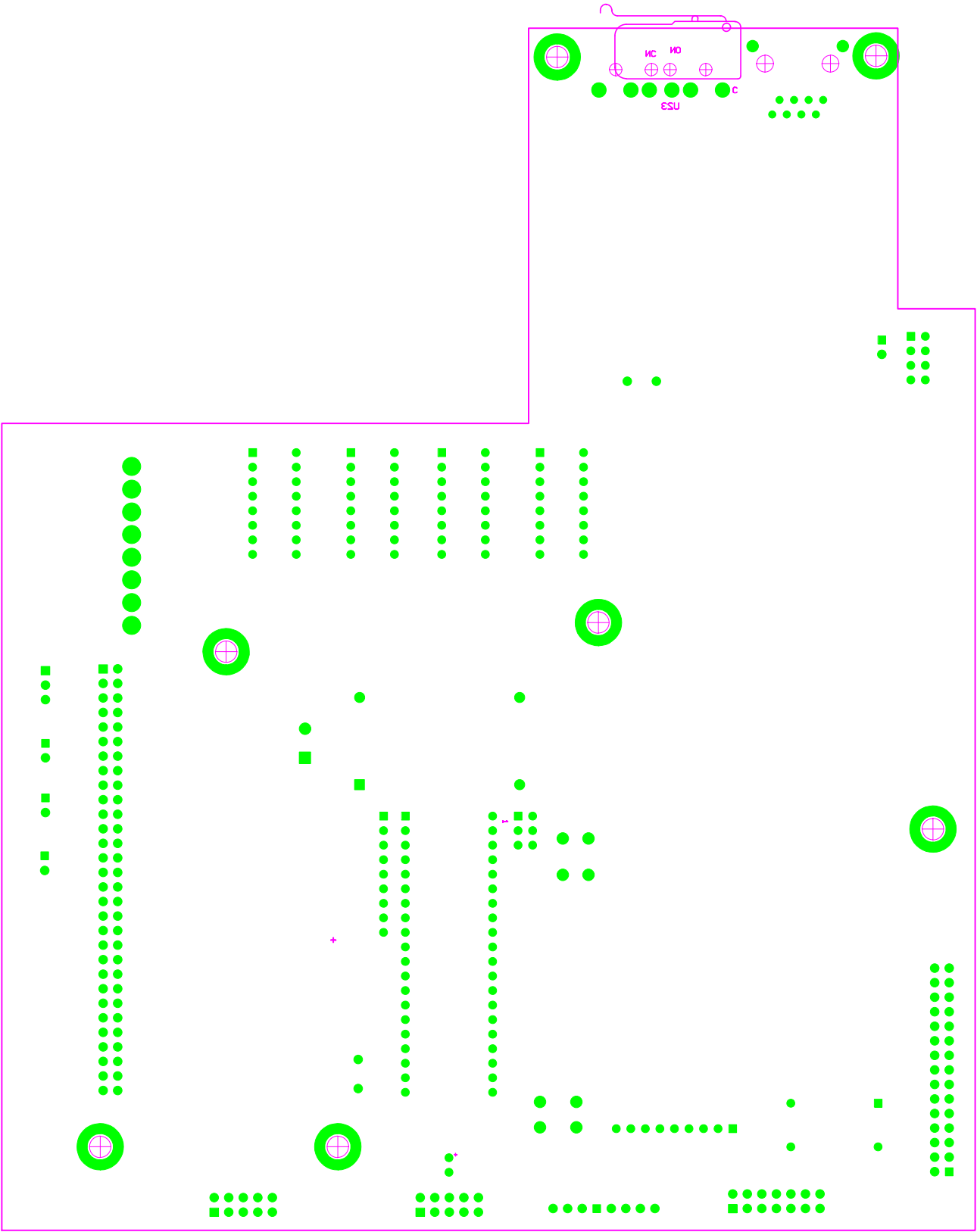
Title		
Size A4	Document Number	Rev *
Date: Tuesday, September 17, 2002	Sheet 9 of 11	



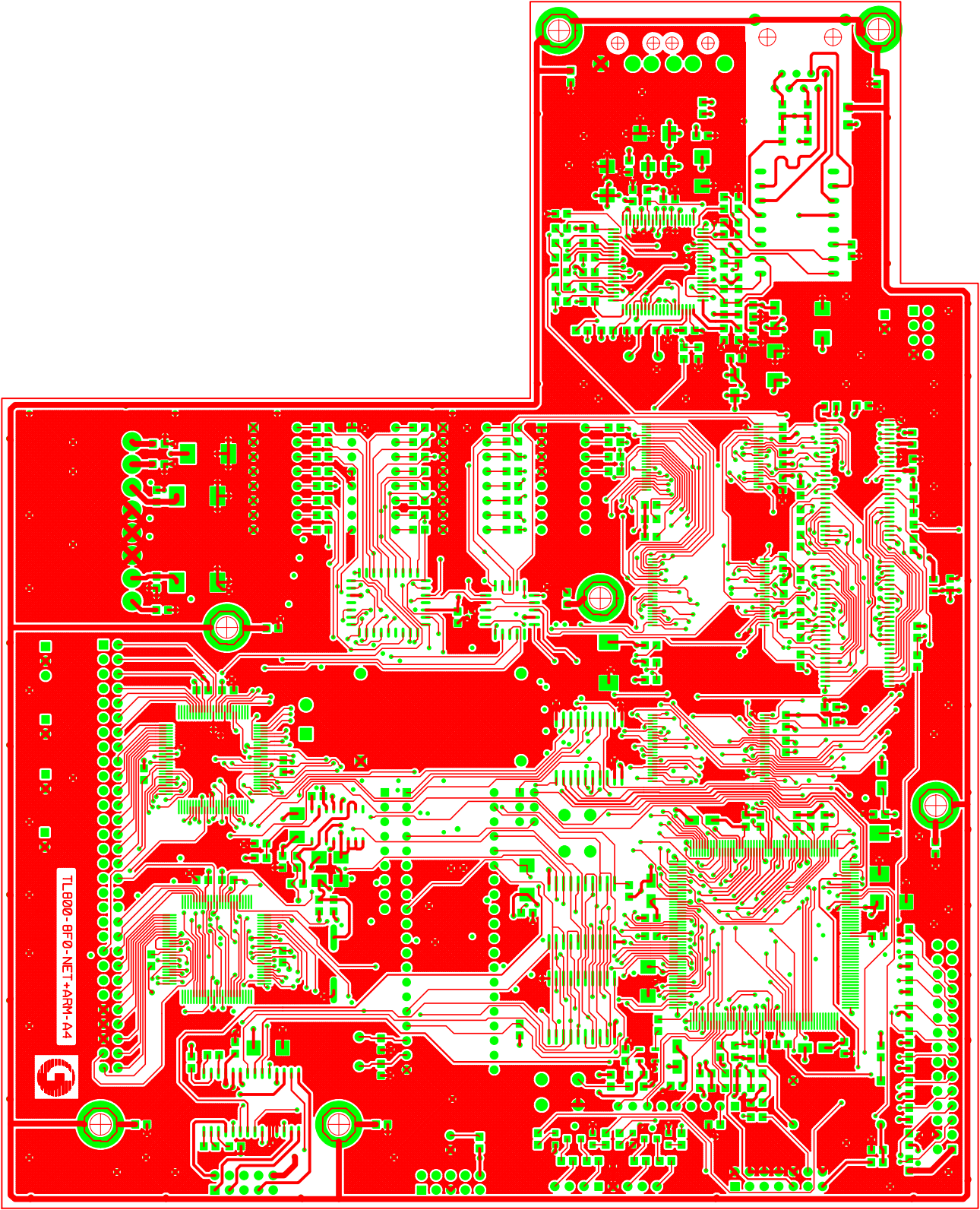
Title		
Size	Document Number	Rev
A4		*
Date:	Tuesday, September 17, 2002	Sheet 10 of 11



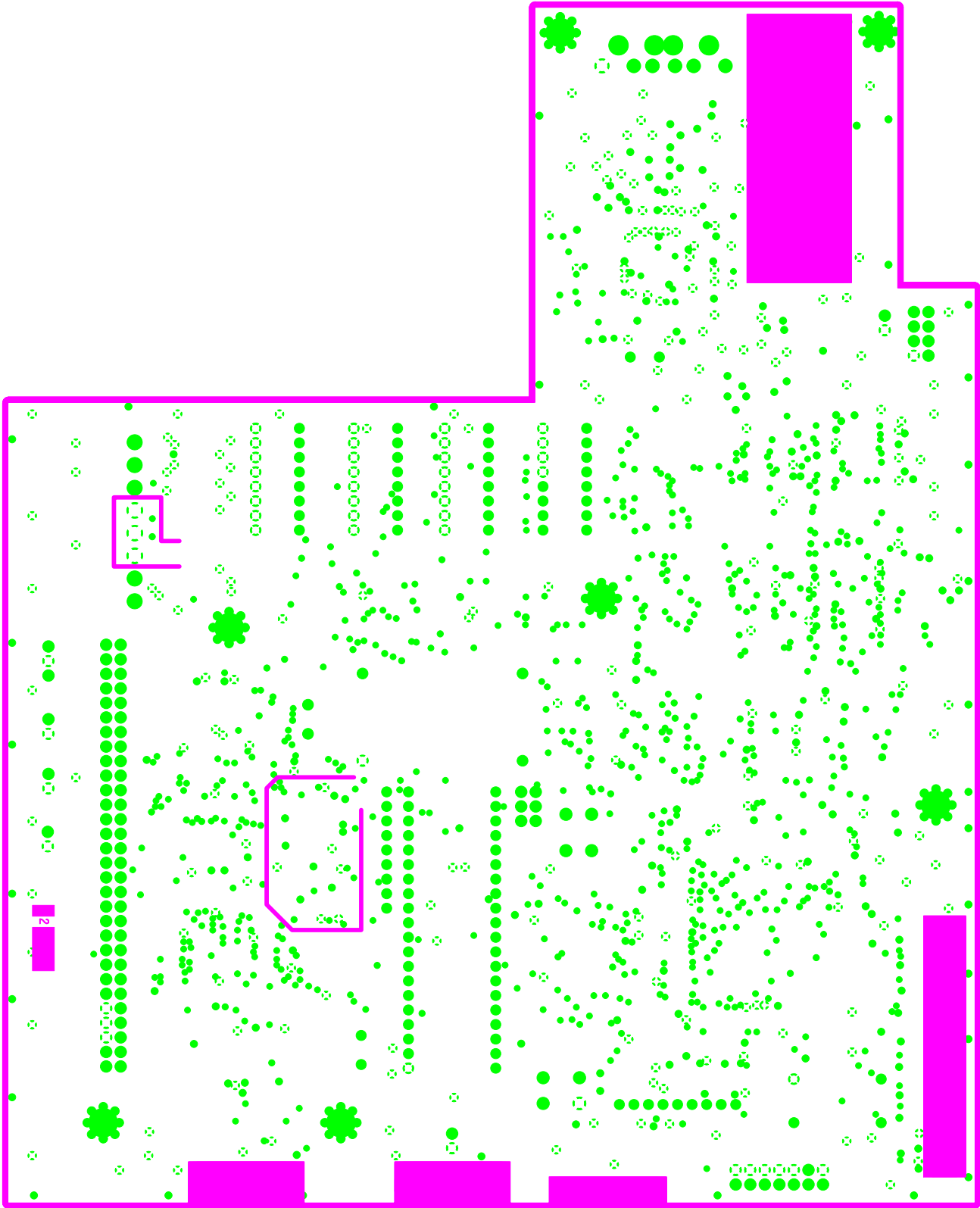
Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	A
Date: Tuesday, September 17, 2002		
Sheet 11 of 11		



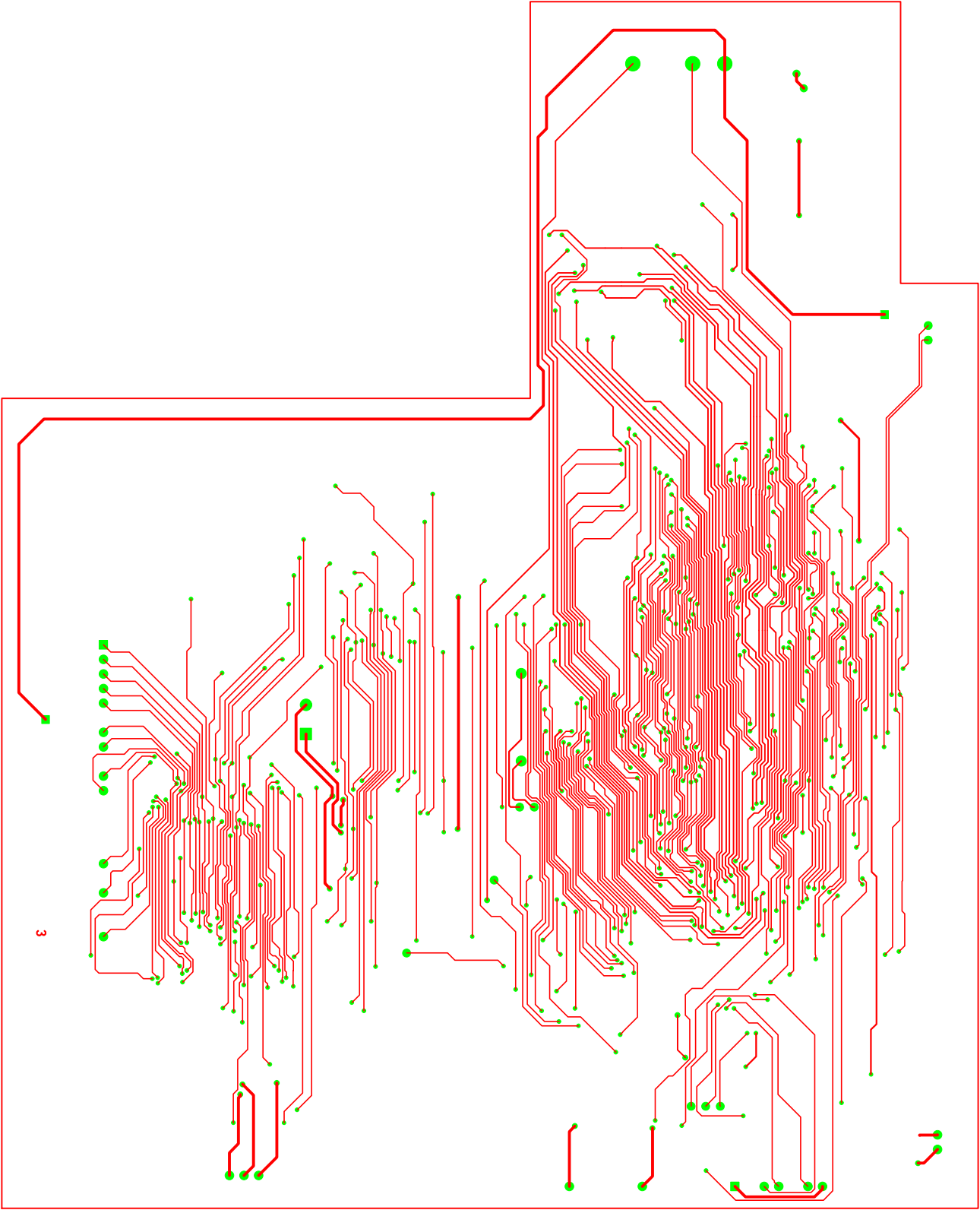
TL800-8F0-NET+ARM-A4(2003-10-23 suyo)



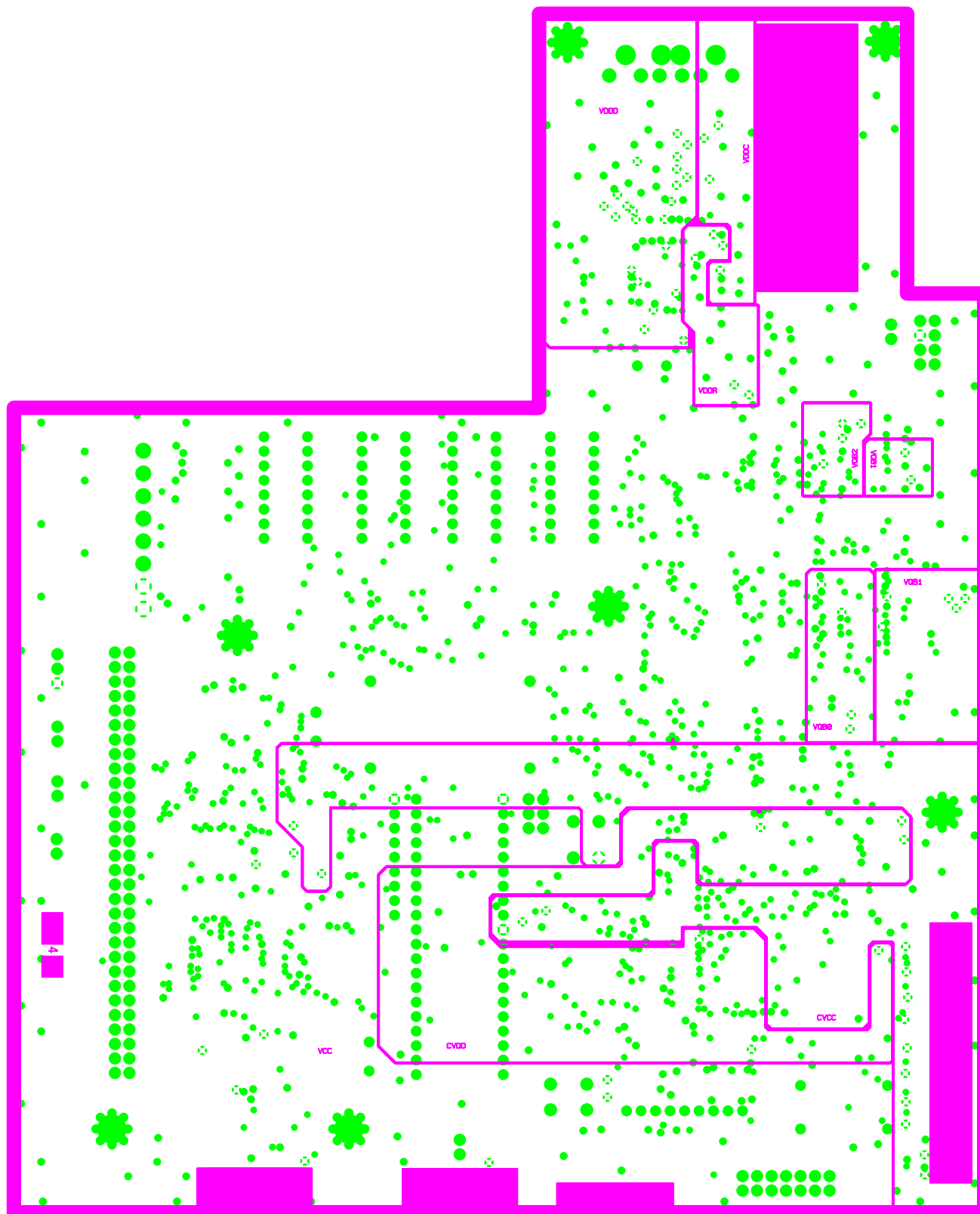
TLB00-BF0-NET+ARM-A4(2003-10-23 suyo) LAYER 1 (COMP)



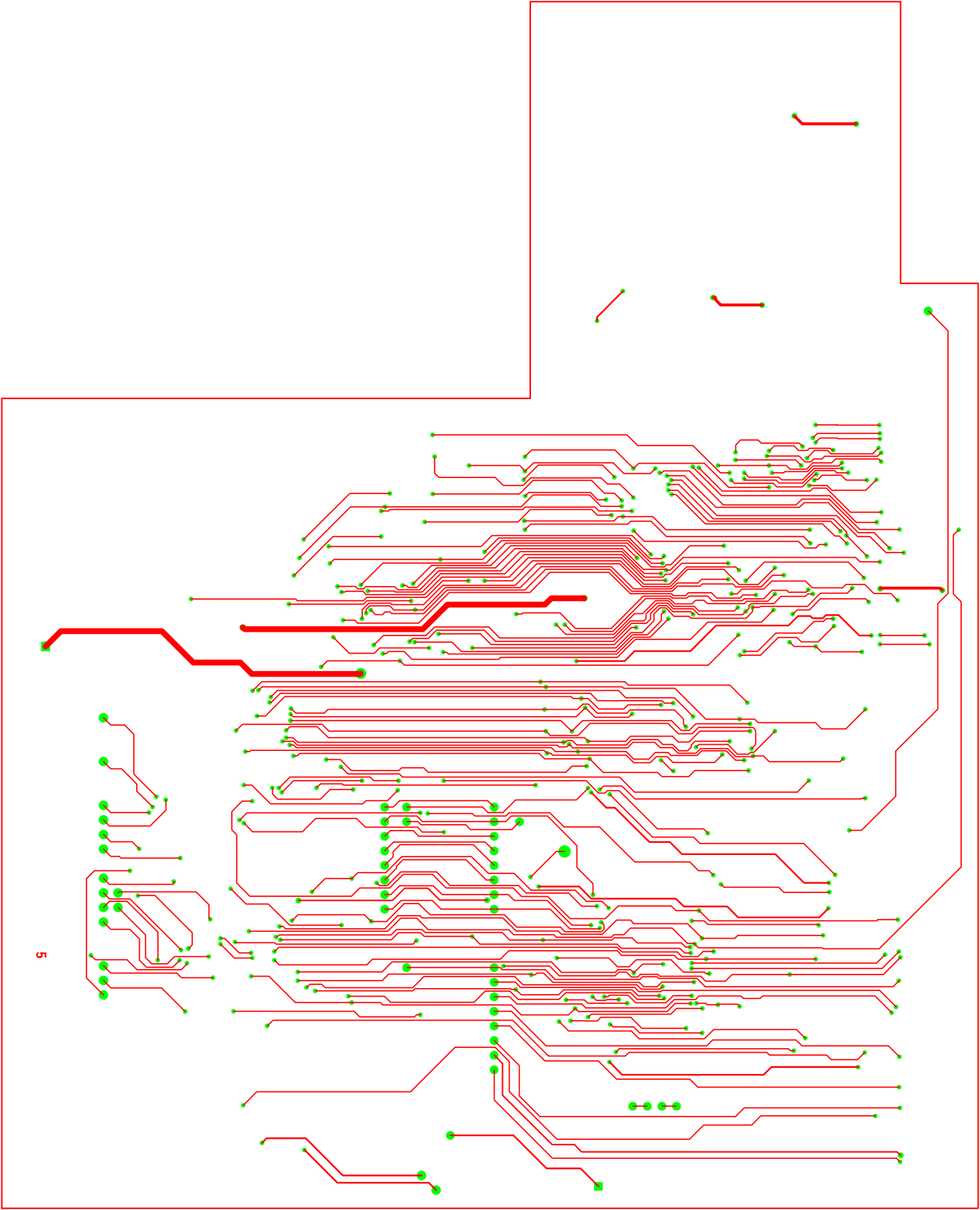
TLB00-8F0-NET+ARM-A4(2003-10-23 suyo) LAYER 2 (GND)



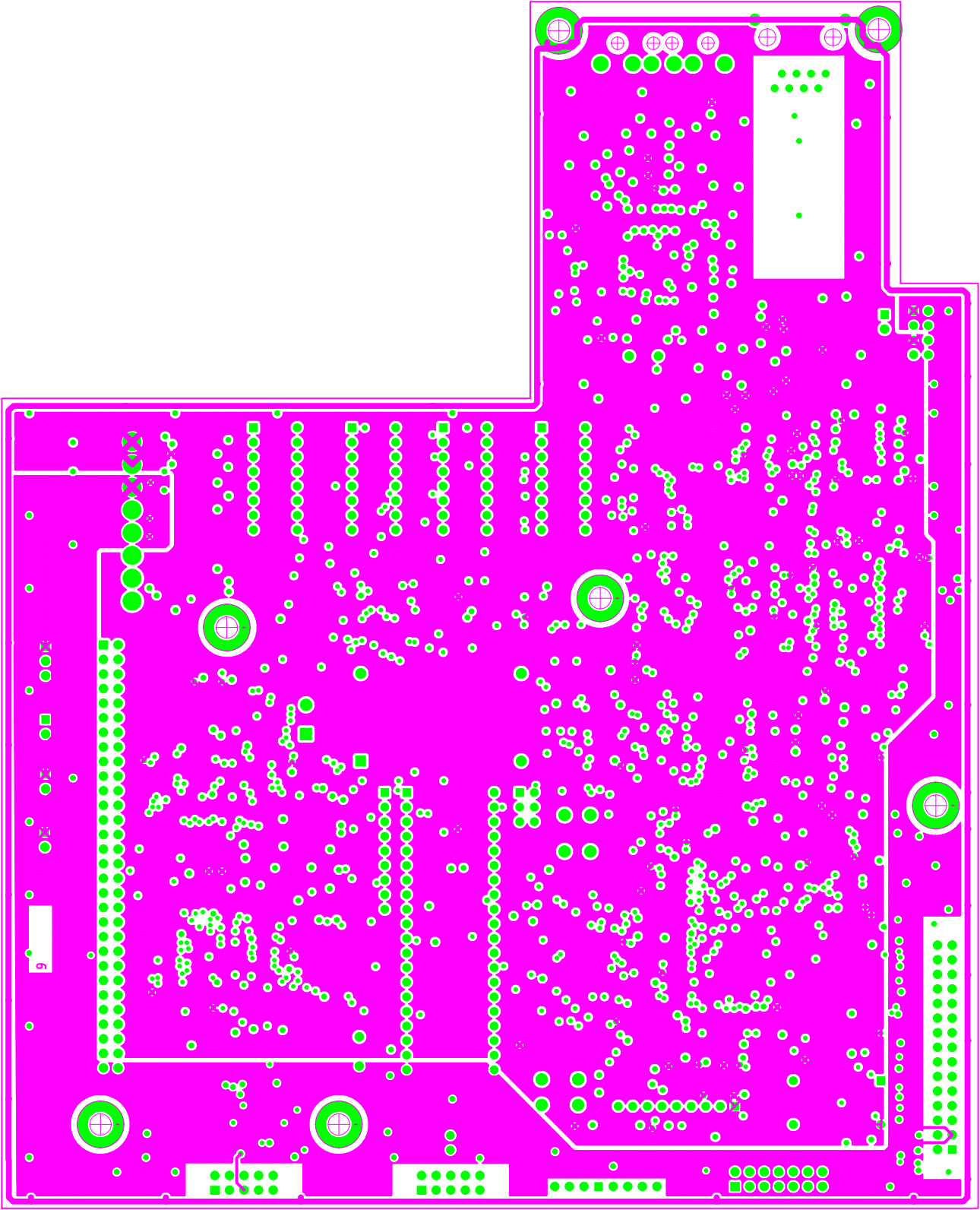
TL800-8F0-NET+ARM-A4(2003-10-23 suyo) LAYER 3



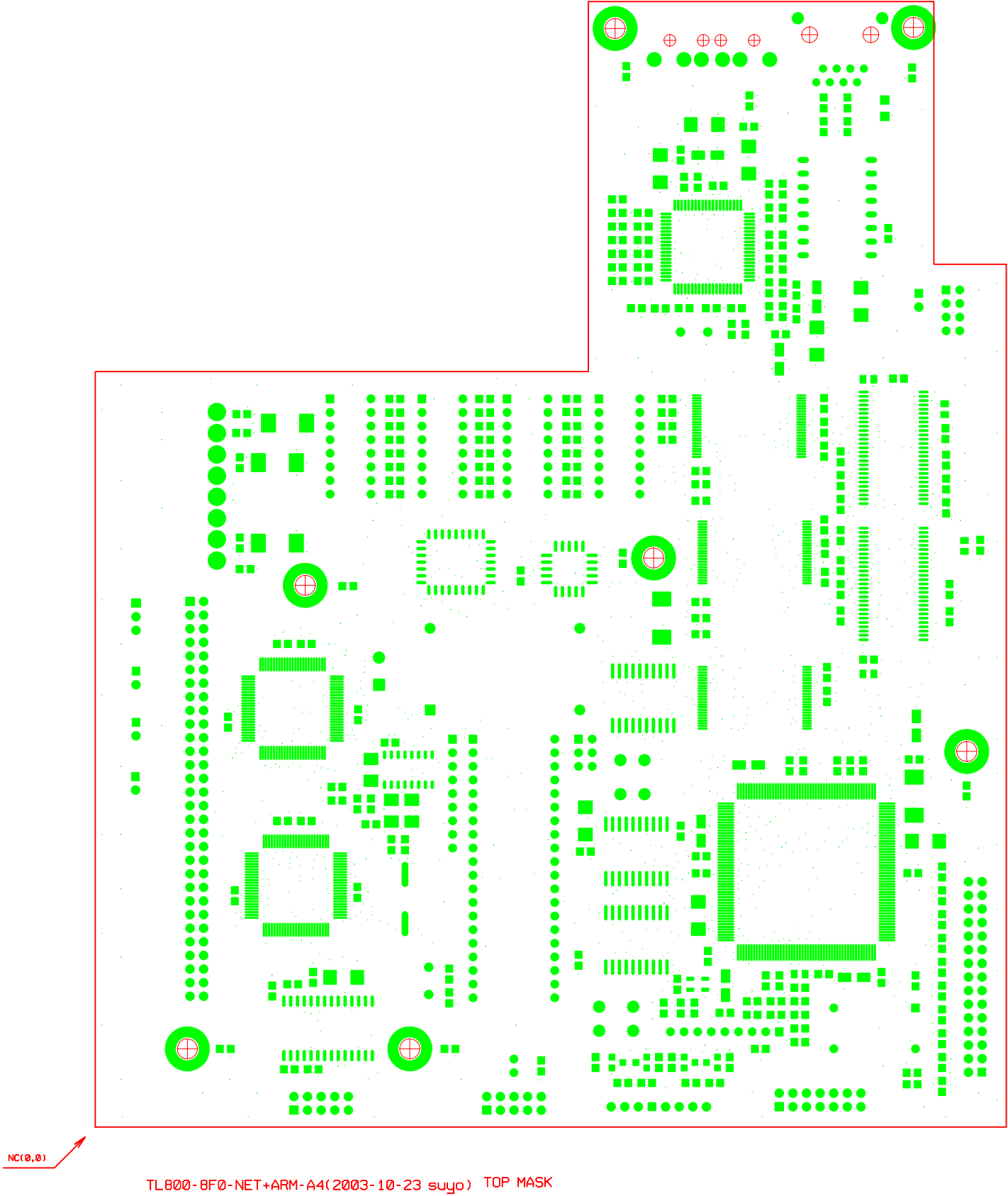
TLB00-8F0-NET+ARM-A4(2003-10-23 sup) LAYER 4 (CVCC,CVDD,VCC,VDDC,VDDD,VDDR,VQB0,VQB1,VQB2,VQB3)

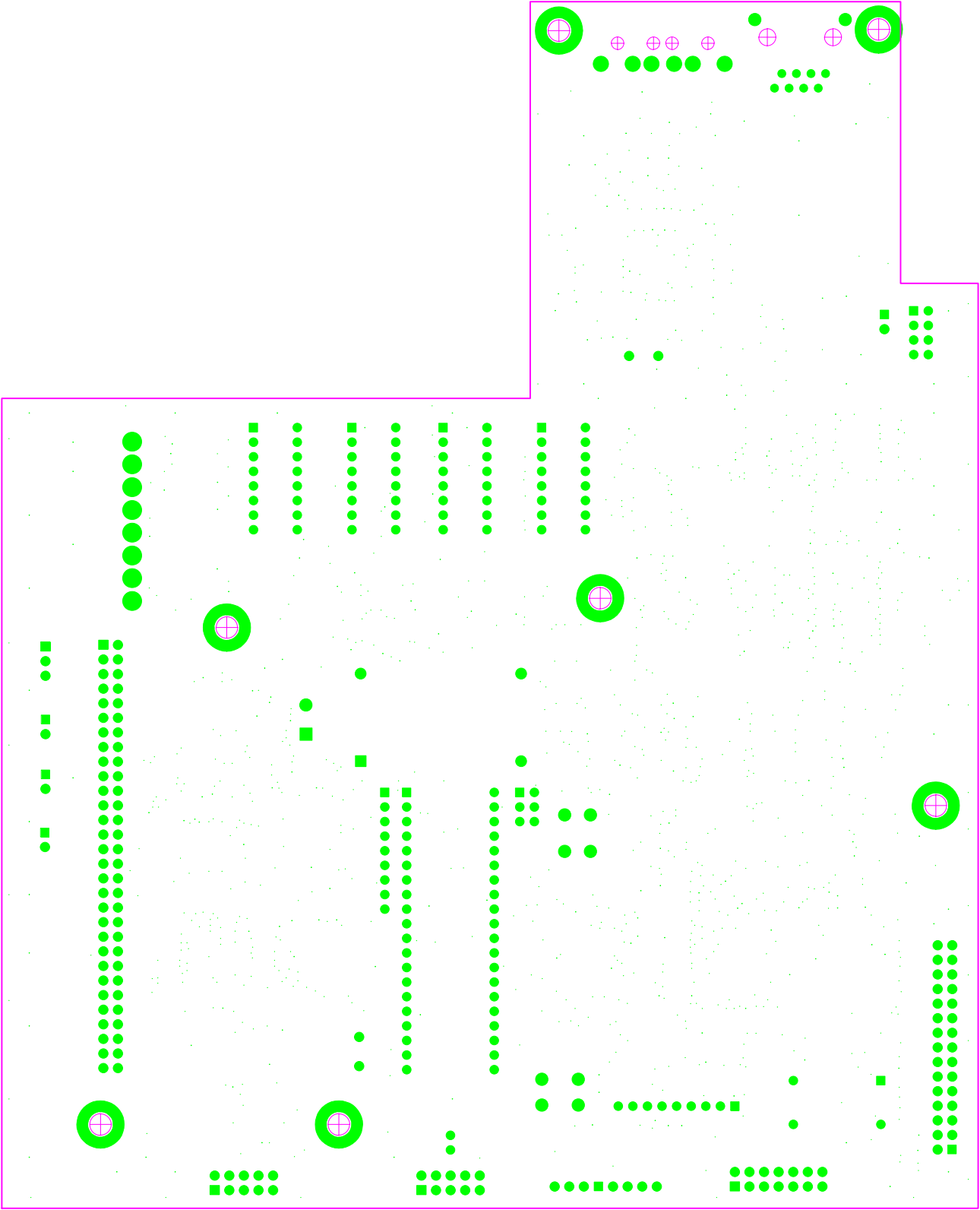


TL800-8F0-NET+ARM-A4(2003-10-23 sugo) LAYER 5

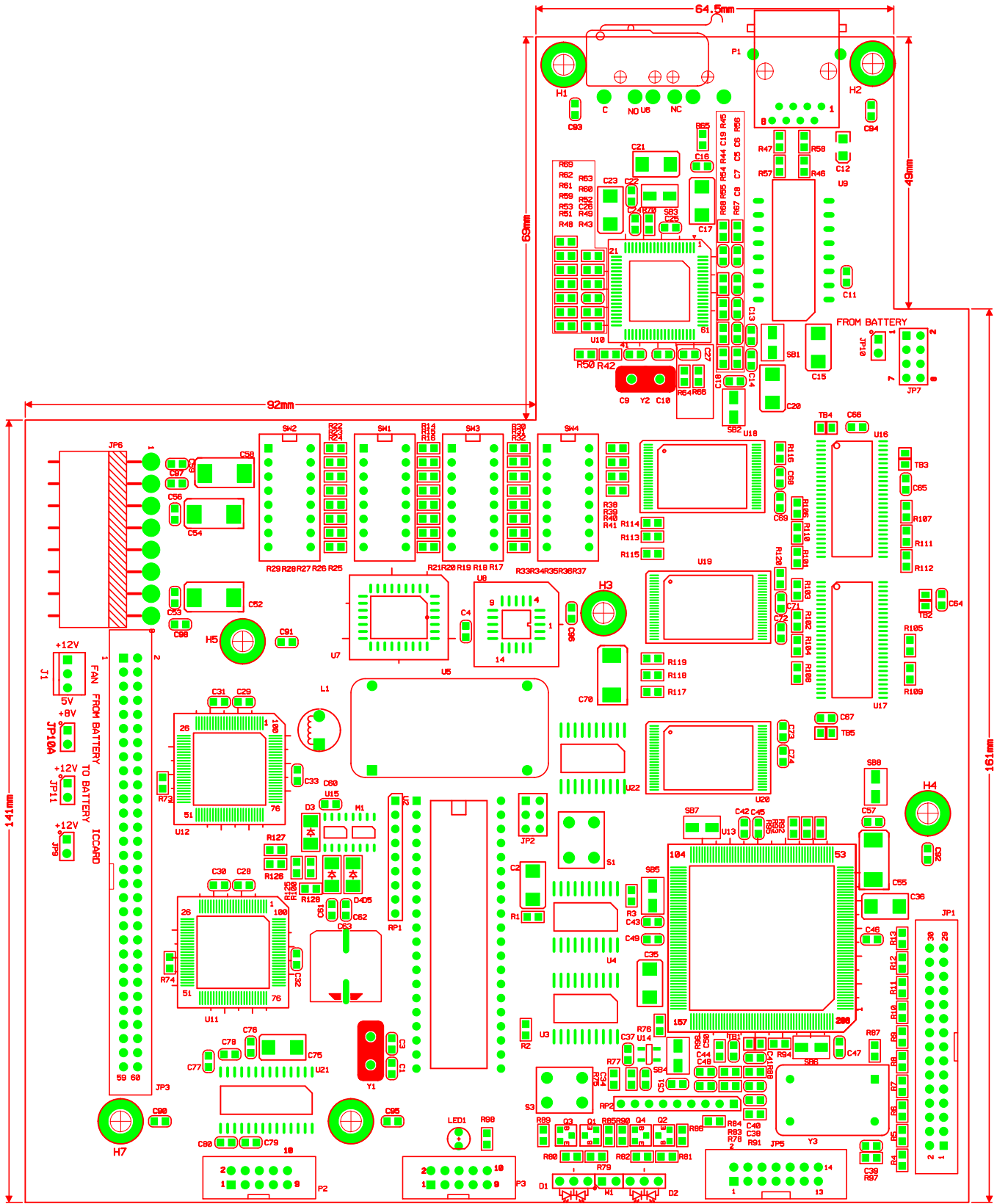


TLB00-8F0-NET+ARM-A4(2003-10-23 suyo) LAYER 6 (3.3V,12V)

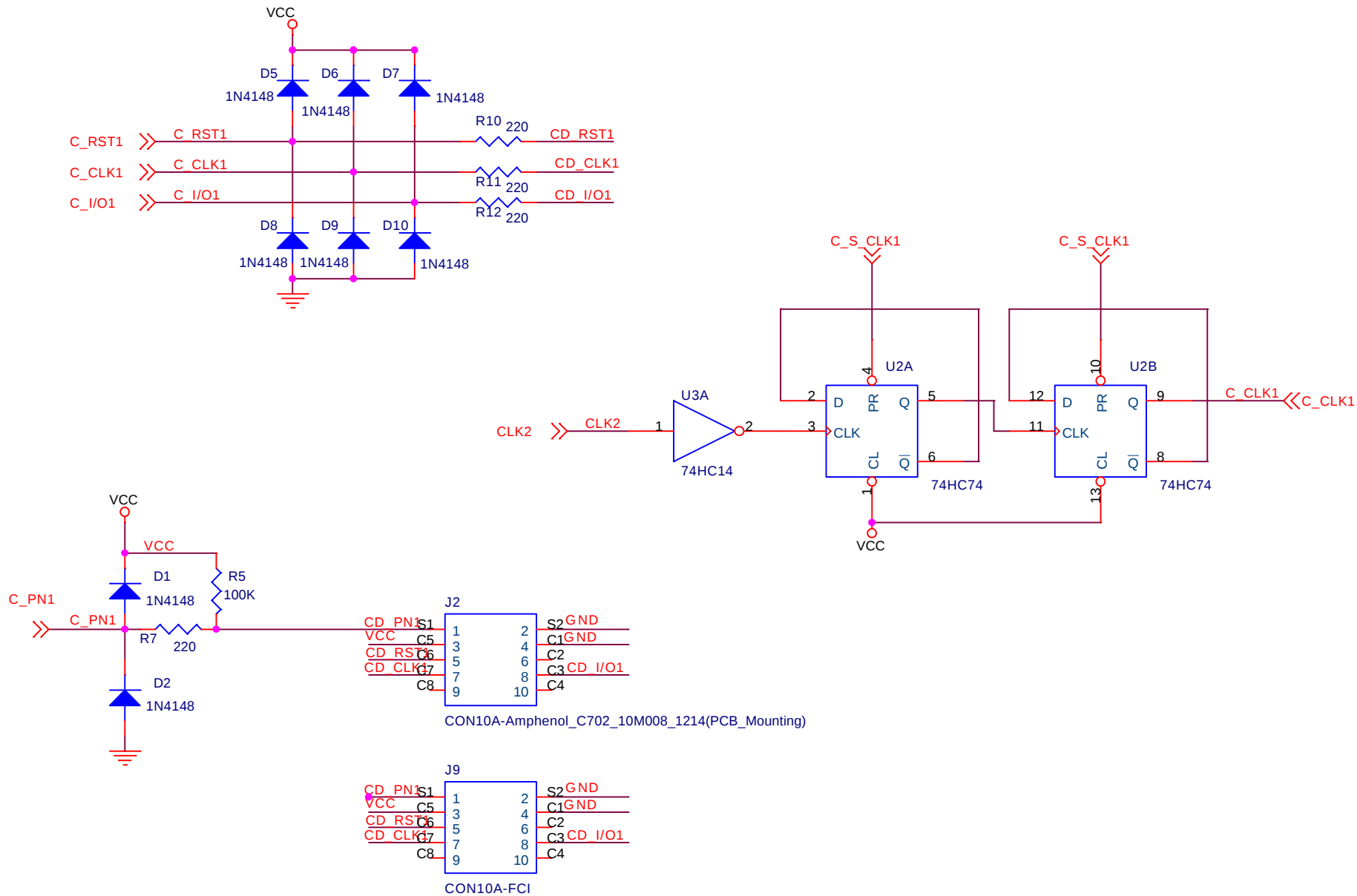




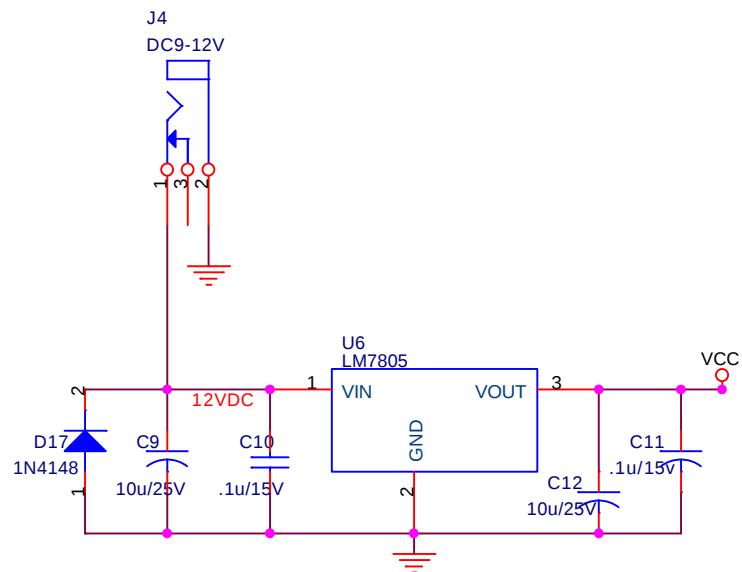
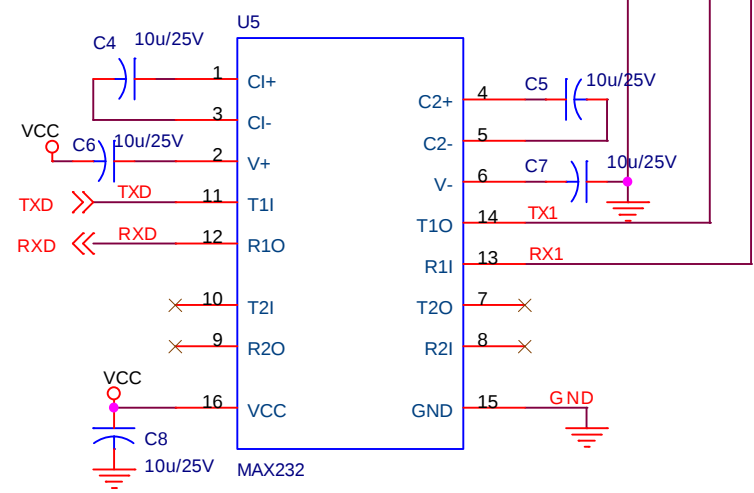
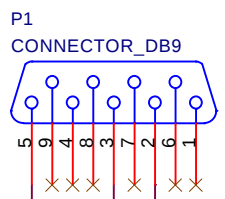
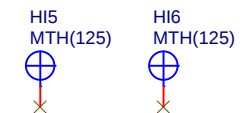
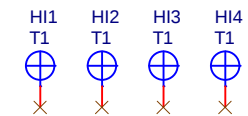
TL800-8F0-NET+ARM-A4(2003-10-23 suyo) BOT MASK



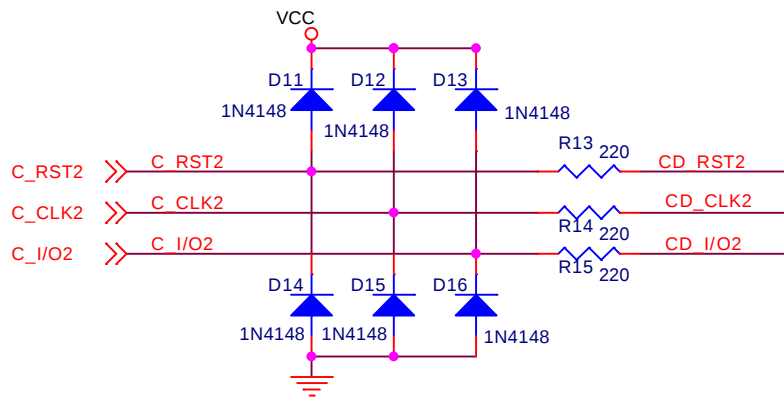
TL800-8F0-NET+ARM-A4(2003-10-23 suyo) COMP SIDE



Title		
SMART CARD		
Size	Document Number	Rev
A	SMART CARD READER V3.0-SMART30.DSN	<RevCode>
Date:	Wednesday, July 10, 2002	Sheet 1 of 4



Title		
POWER & RS232C		
Size	Document Number	Rev
A	SMART CARD READER V3.0-SMART30.DSN	<RevCode>
Date:	Wednesday, July 10, 2002	Sheet 3 of 4



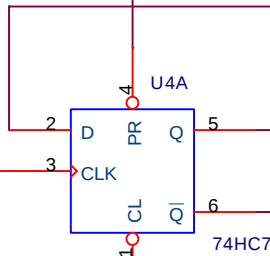
CLK2

CLK2

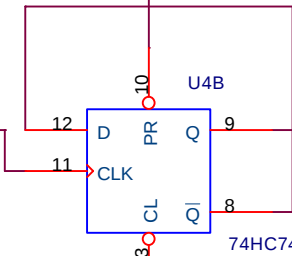
U3C

74HC14

C_S_CLK2



C_S_CLK2

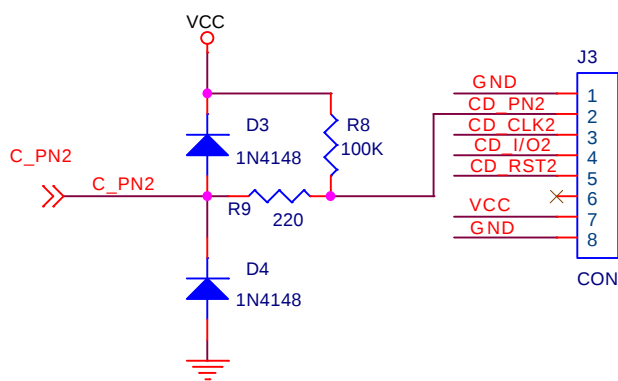


C_CLK2

C_CLK2

註: CLK2 = 14.31818 MHz
C_CLK = 3.579545 MHz
...almost =3.5712 MHz

3571200/372=9600...baud-rate



J3

GND
CD_PN2
CD_CLK2
CD_I/O2
CD_RST2
VCC
GND

CON8-Amphenol_C707_10M006_0002(SIM-LOCK)

Title		
SAM I/F		
Size	Document Number	Rev
A	SMART CARD READER V3.0-SMART30.DSN	<RevCode>
Date:	Wednesday, July 10, 2002	Sheet 4 of 4