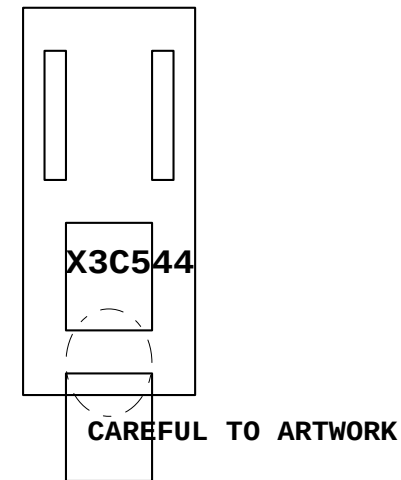


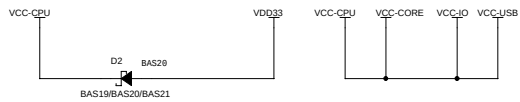
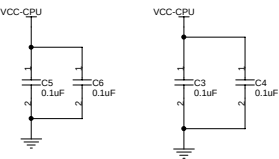
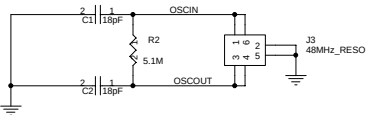
X3on544 UFC BOARD SCHEMATIC VER 0.97F

SHEET 1 : COVER SHEET
SHEET 3 : X3C544,Power

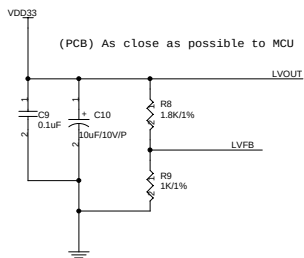


Title		
<Title>		
Size	Document Number	Rev
A	<Doc>	<RevCode>
Date:	금요일, 12월 12, 2003	Sheet 1 of 3

X-TAL / RESONATOR CPU CLOCK



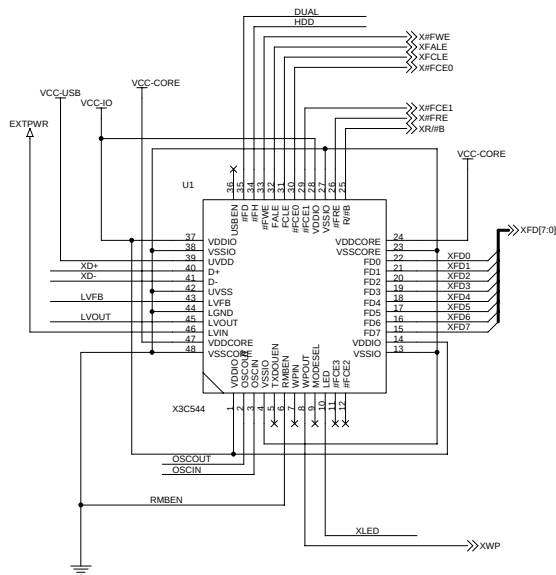
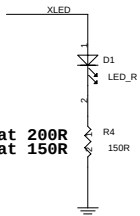
(PCB) R30 MUST BE 0603



(PCB) As close as possible to MCU

(SILK)
TXDOEN(5) TRUE(*) HIGH(NC) LOW
RMBEN(6) FALSE FALSE
WPEN(7) FALSE(*) TRUE
F_DUAL(35) FALSE TRUE(*)
F_HDD(34) FALSE TRUE(*)

about 1.7mA at 200R
about 2.2mA at 150R



(PCB) LOCATE 2 TP POINTS AS CLOSE AS POSSIBLE
TO USB LINE & DON'T CROSS D+/D- LINE

