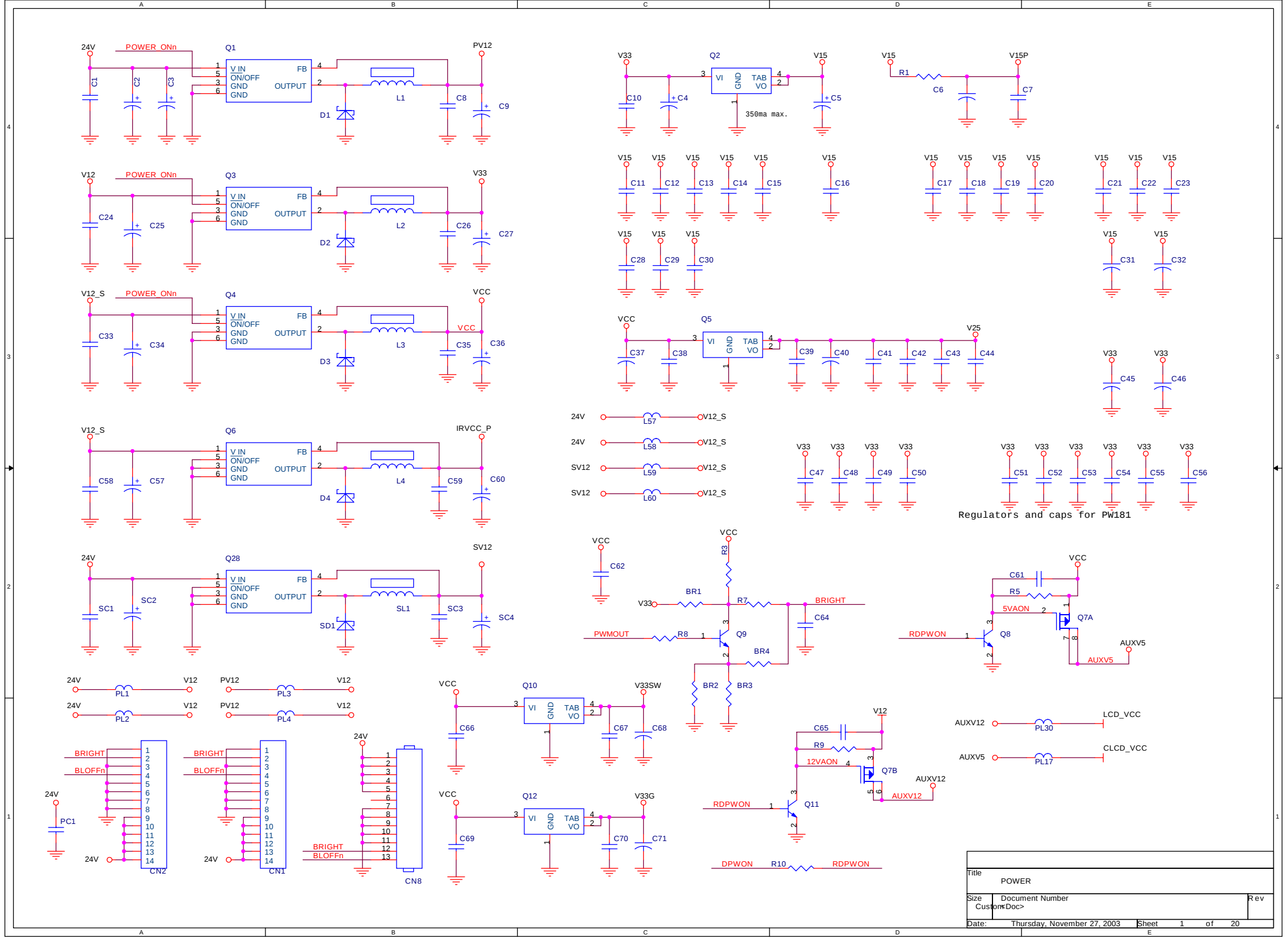
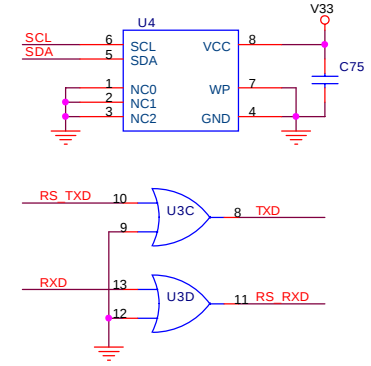
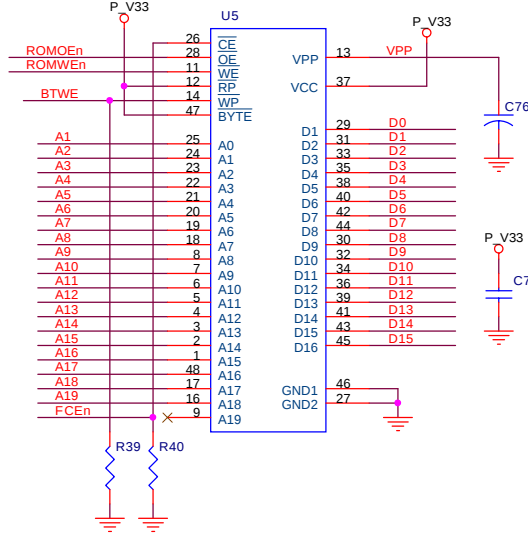
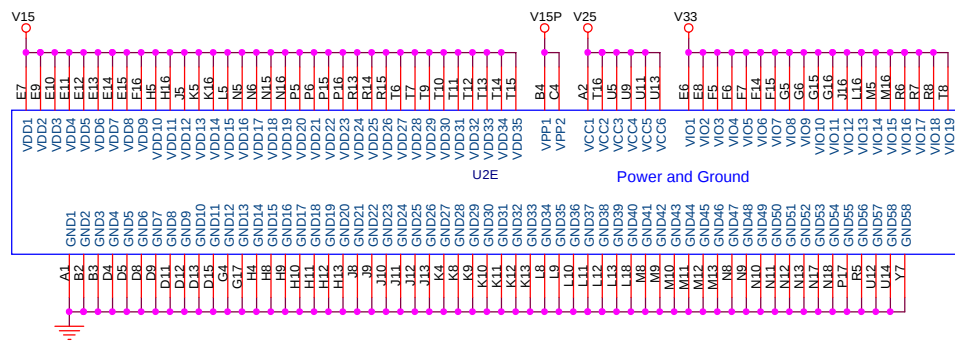
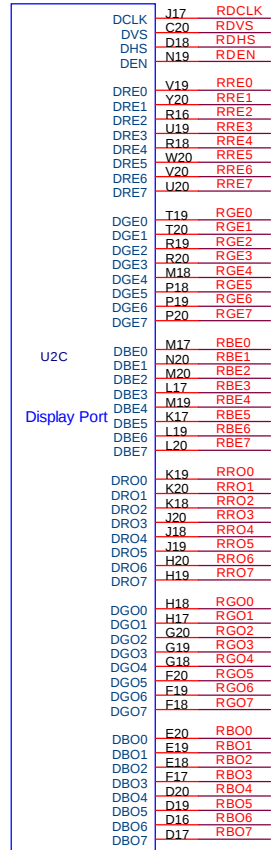
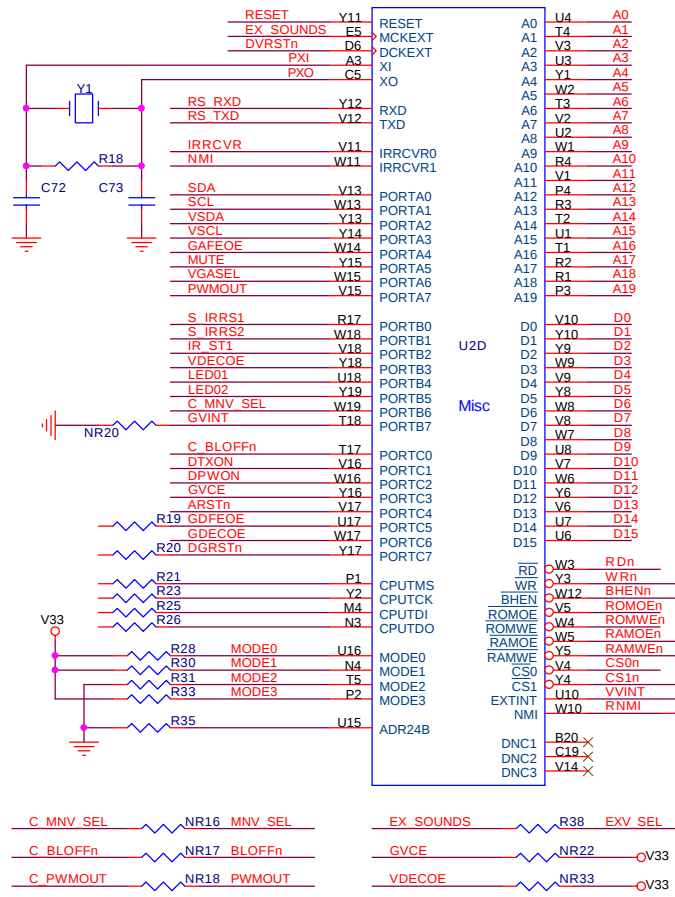
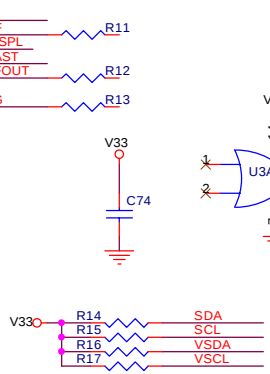
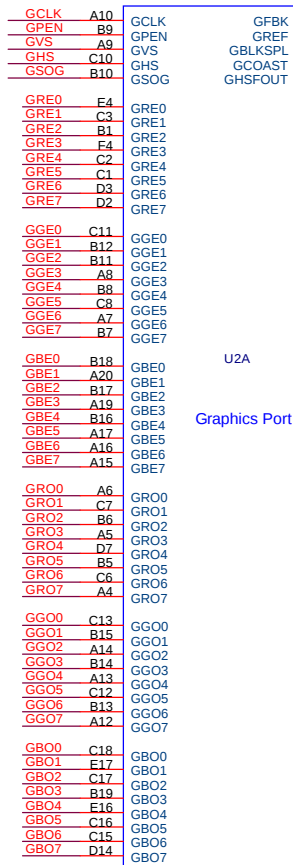


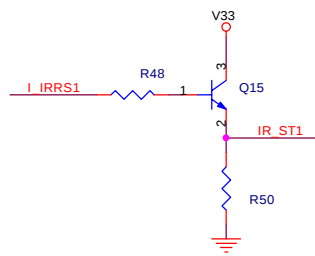
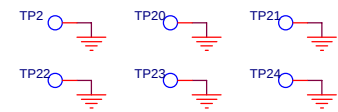
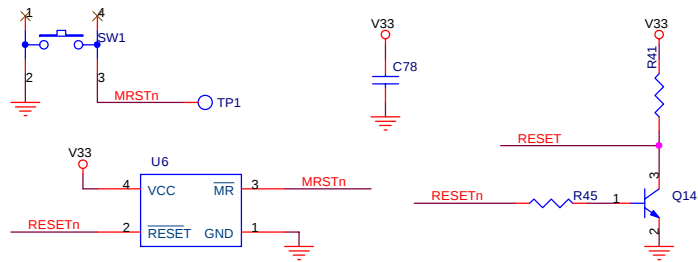


Regulators and caps for PW181

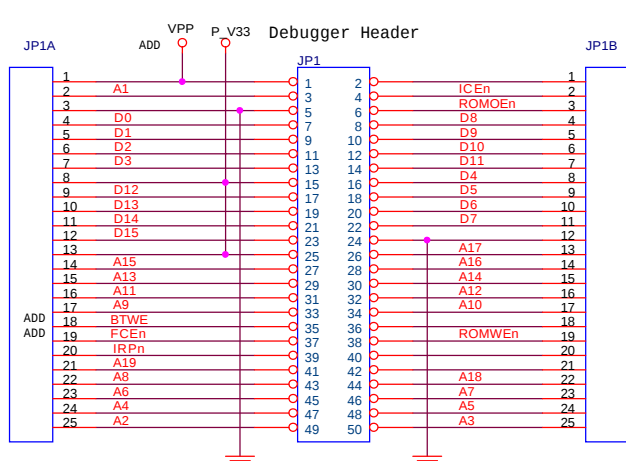
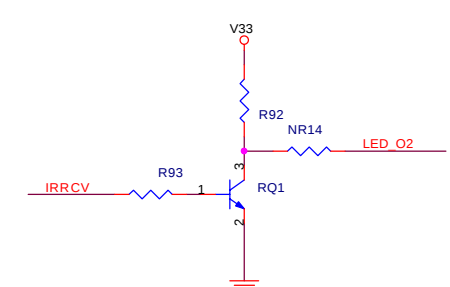
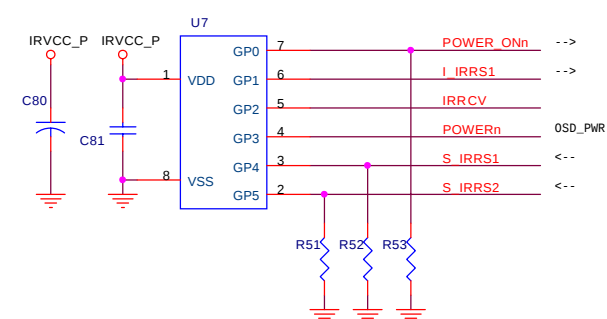
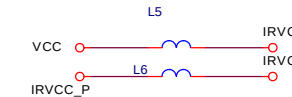
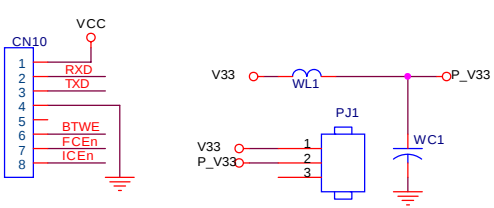
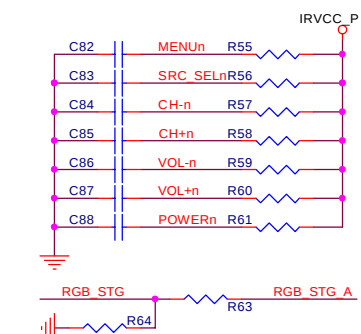
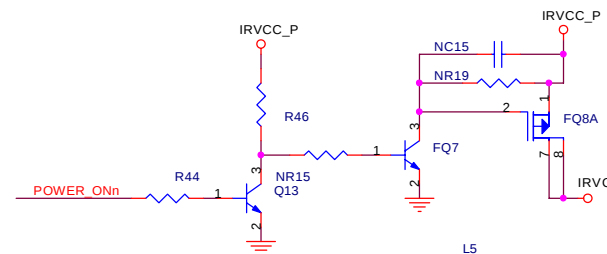
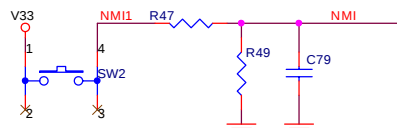
Title			POWER
Size	Document Number	Rev	
Custom Doc>			
Date:	Thursday, November 27, 2003	Sheet	1 of 20



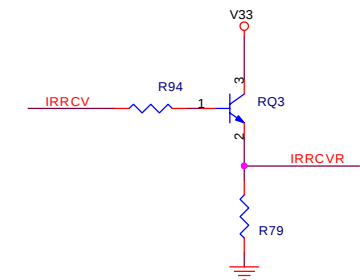
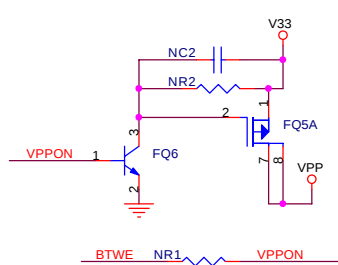
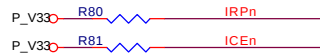
3.3V, 100mA : 330mW (When MCLK is 160MHz)
 2.5V, 20mA : 20mW
 1.5V, 900mA : 1350mW

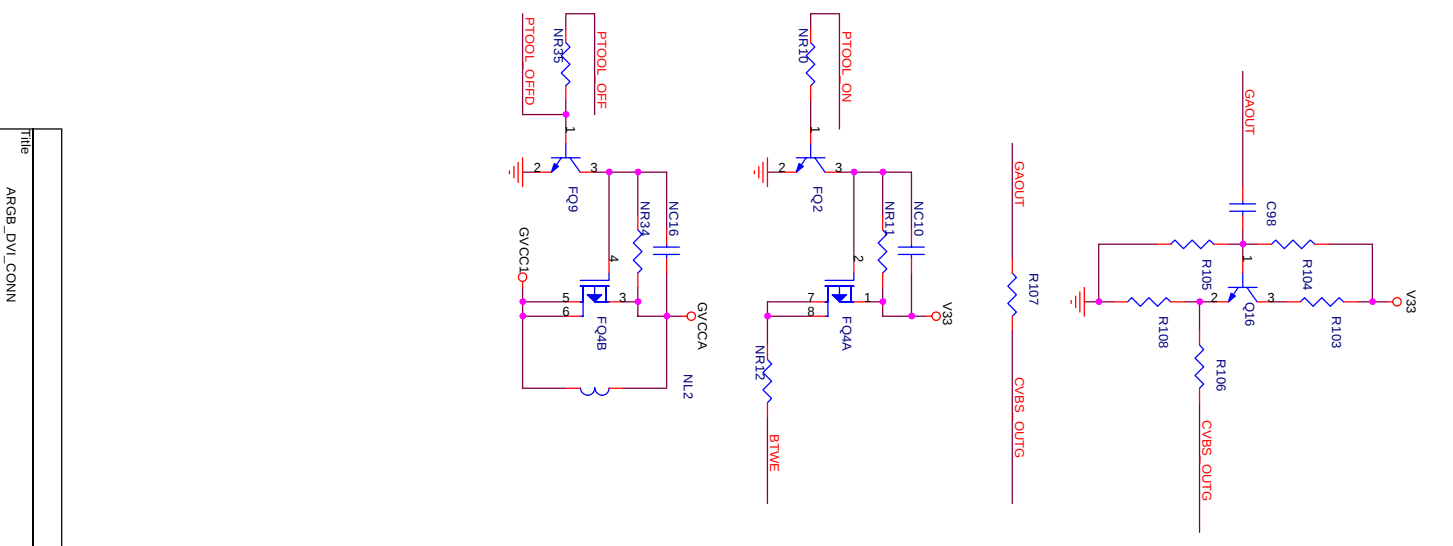
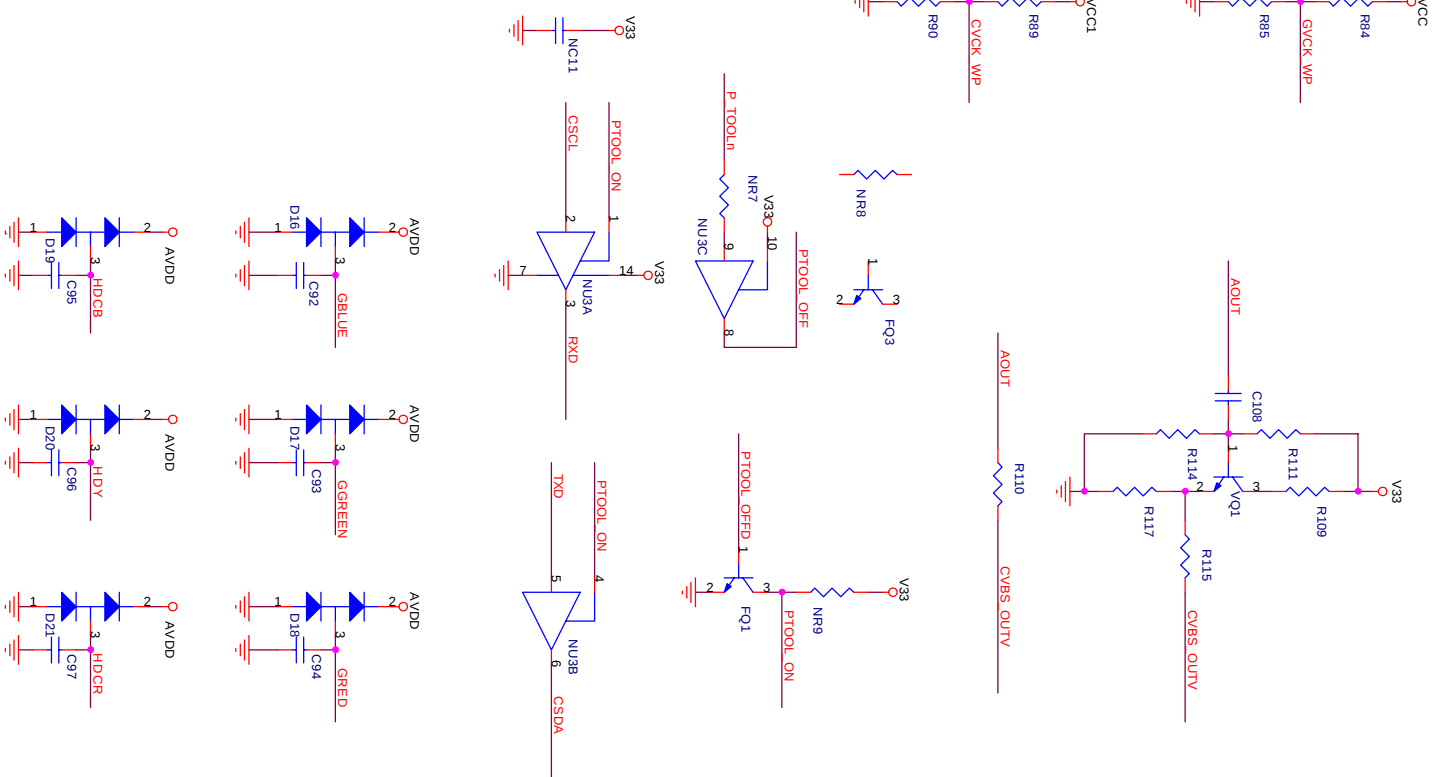
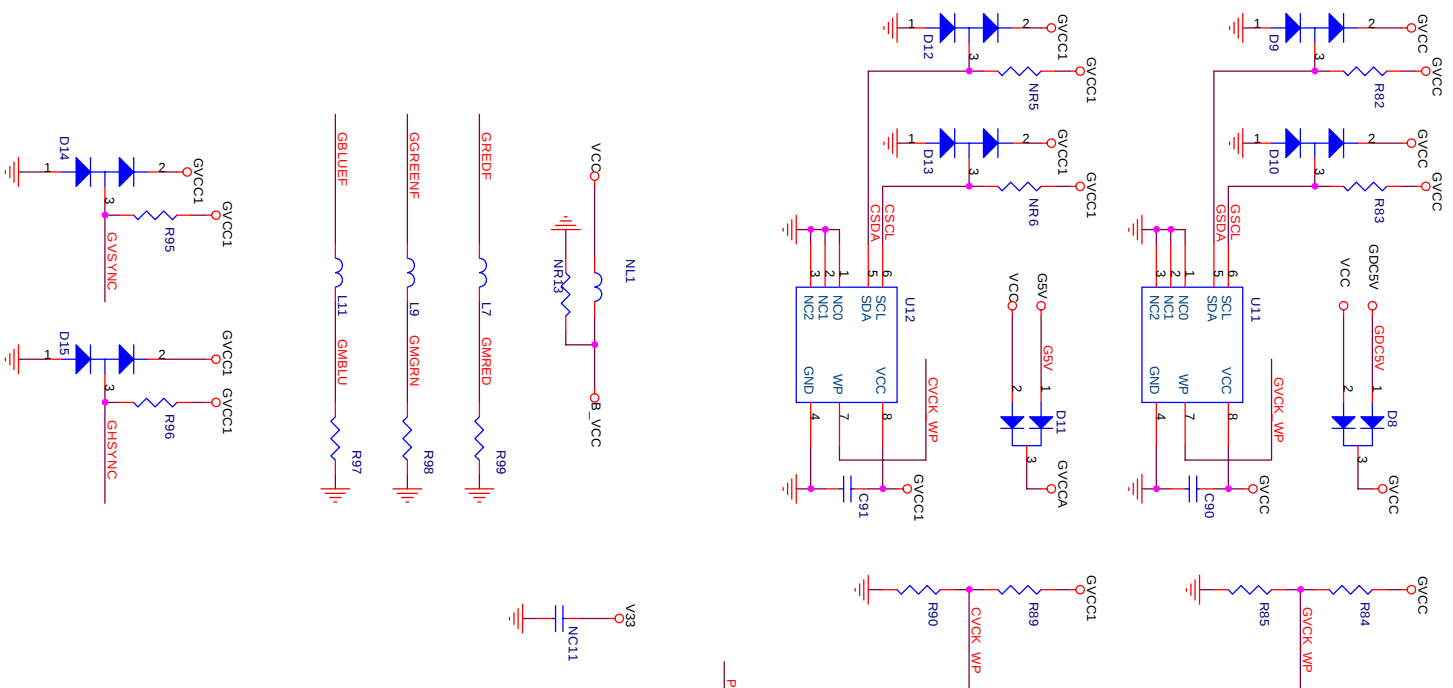


TEST POINT
GND

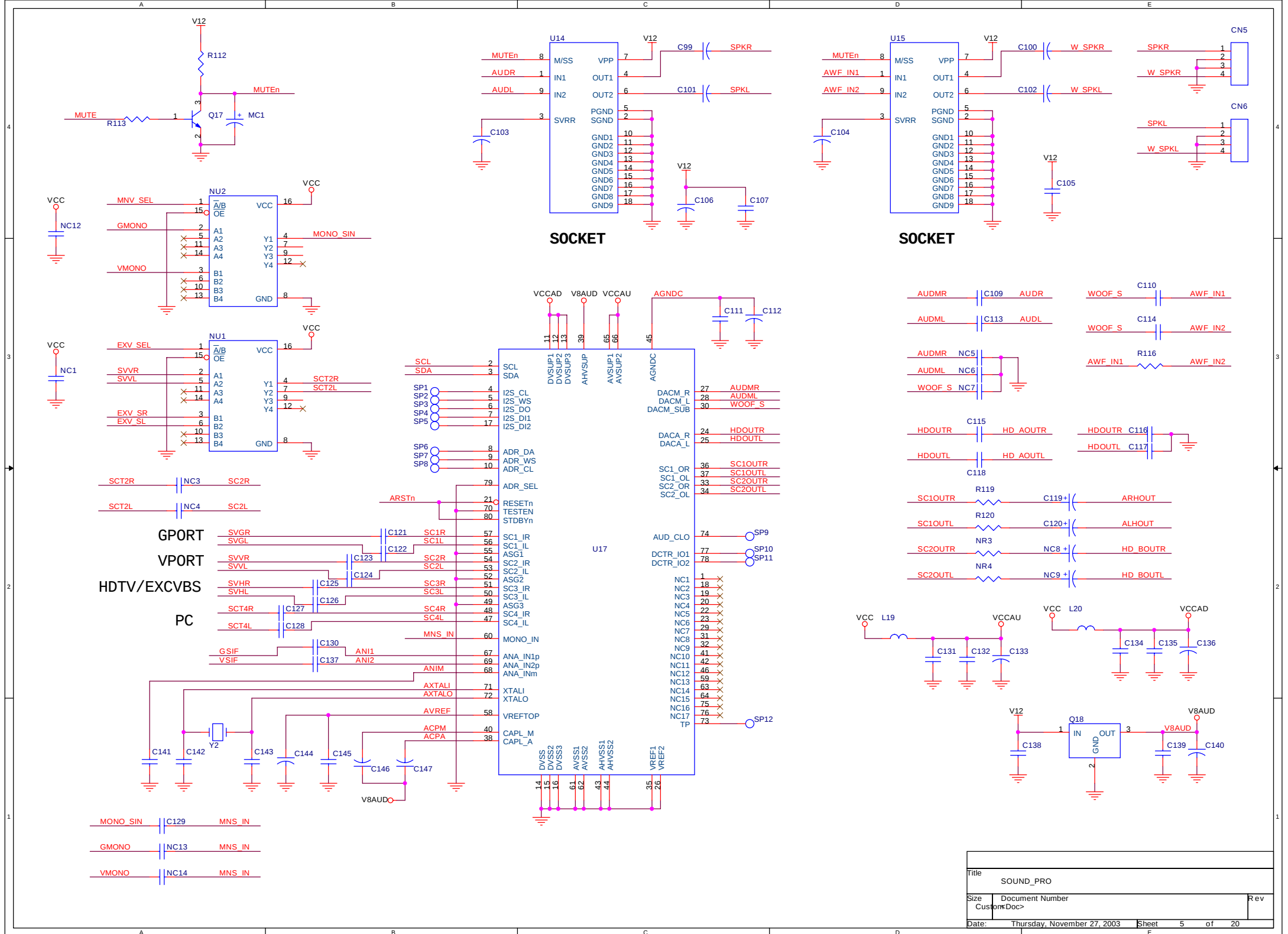


Remove for Production

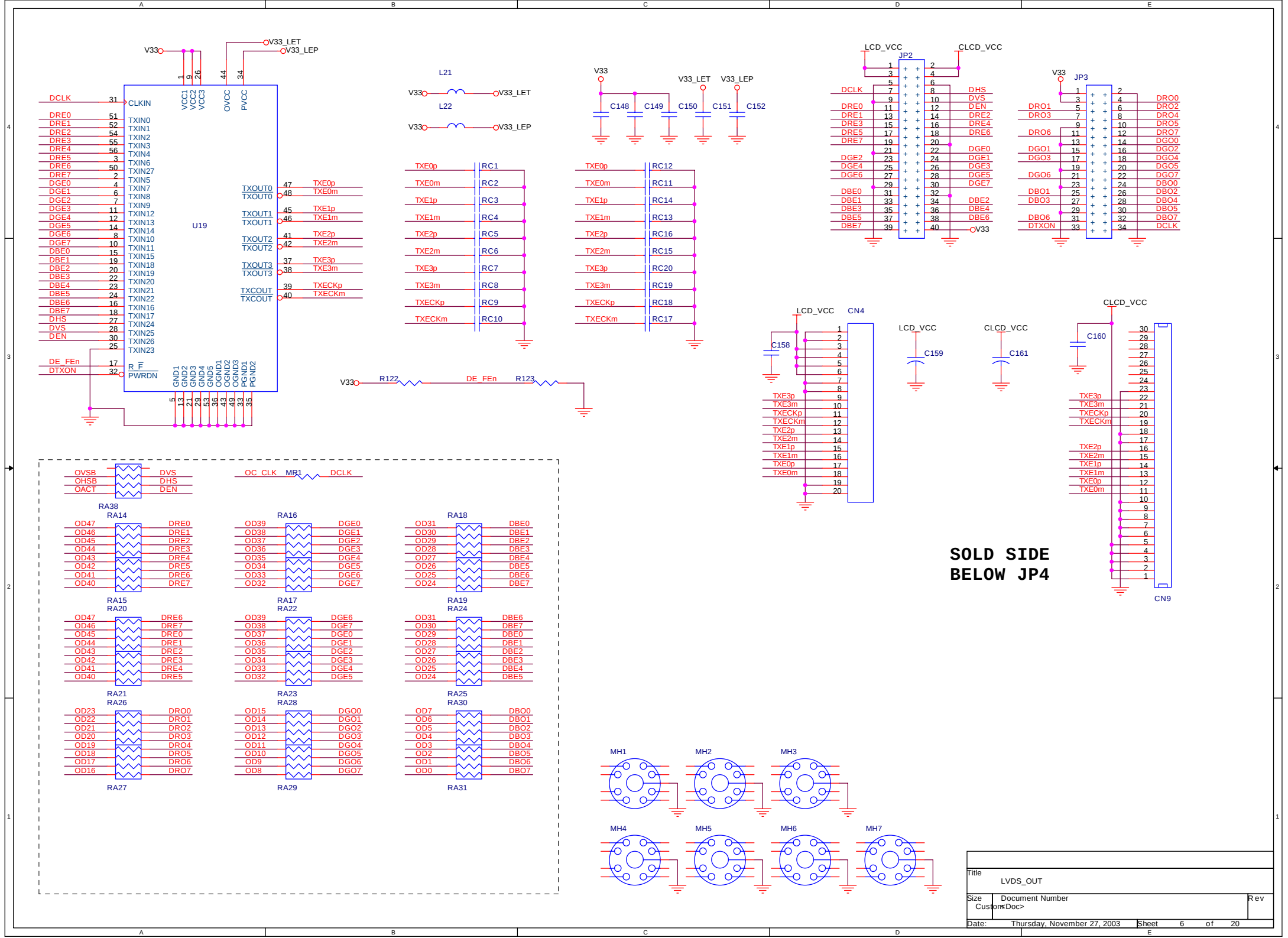


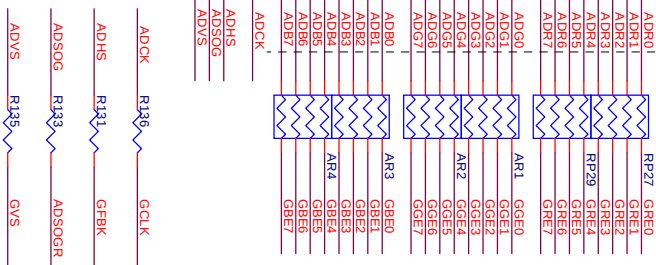
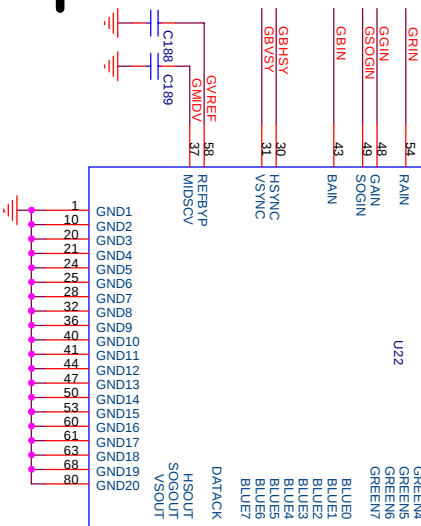
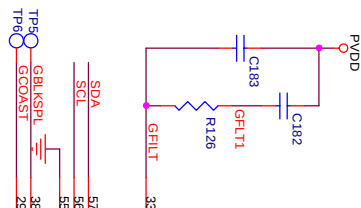
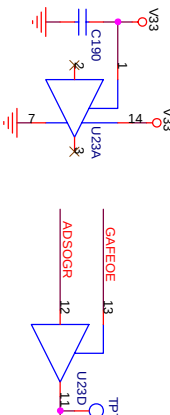
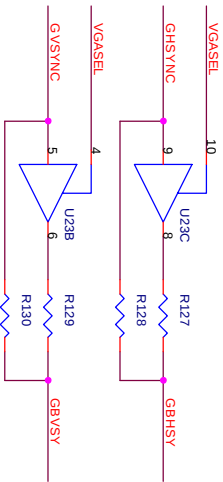
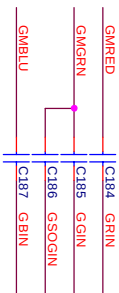
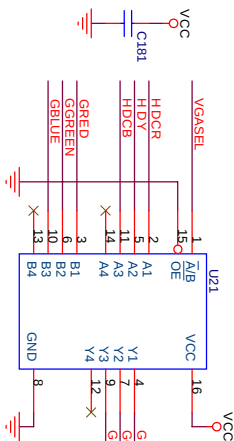
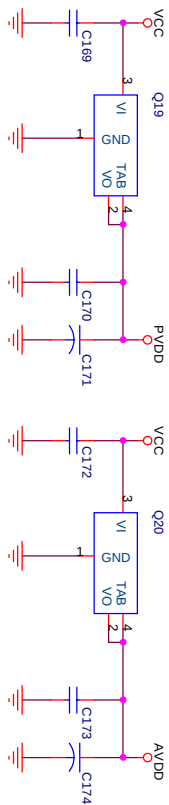
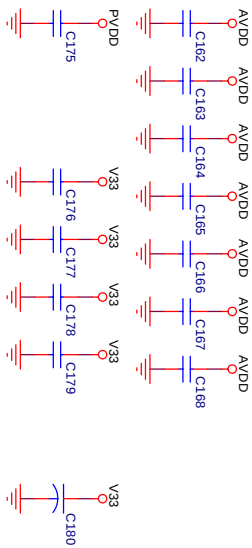


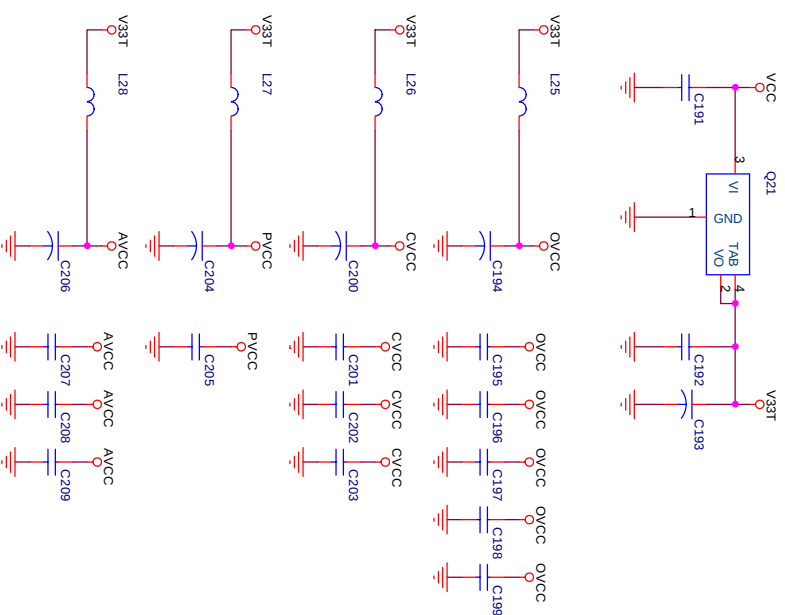
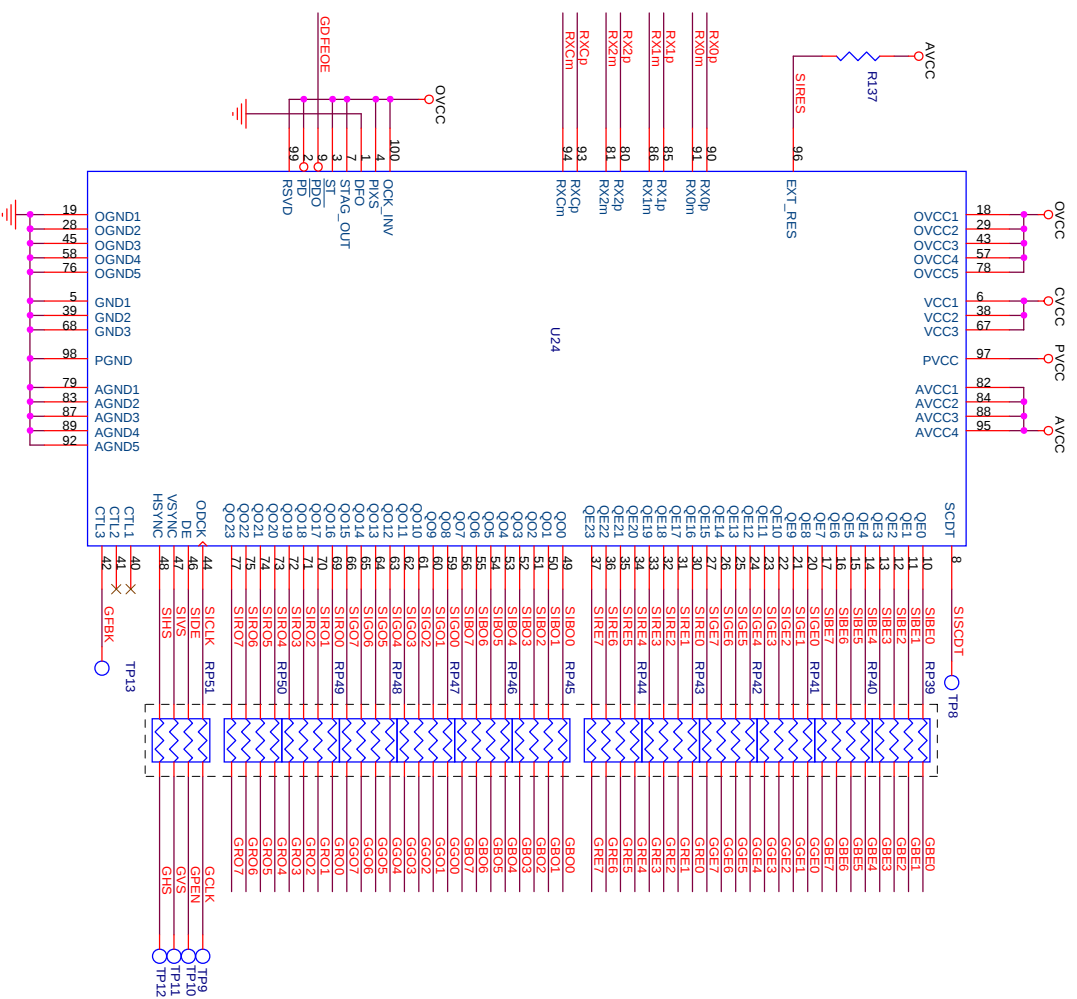
Title		
ARGB_DVI_CONN		
Site	Document Number	Rev
C:\msnDoc>		
Date:	Thursday November 27, 2003	Sheet 4 of 20



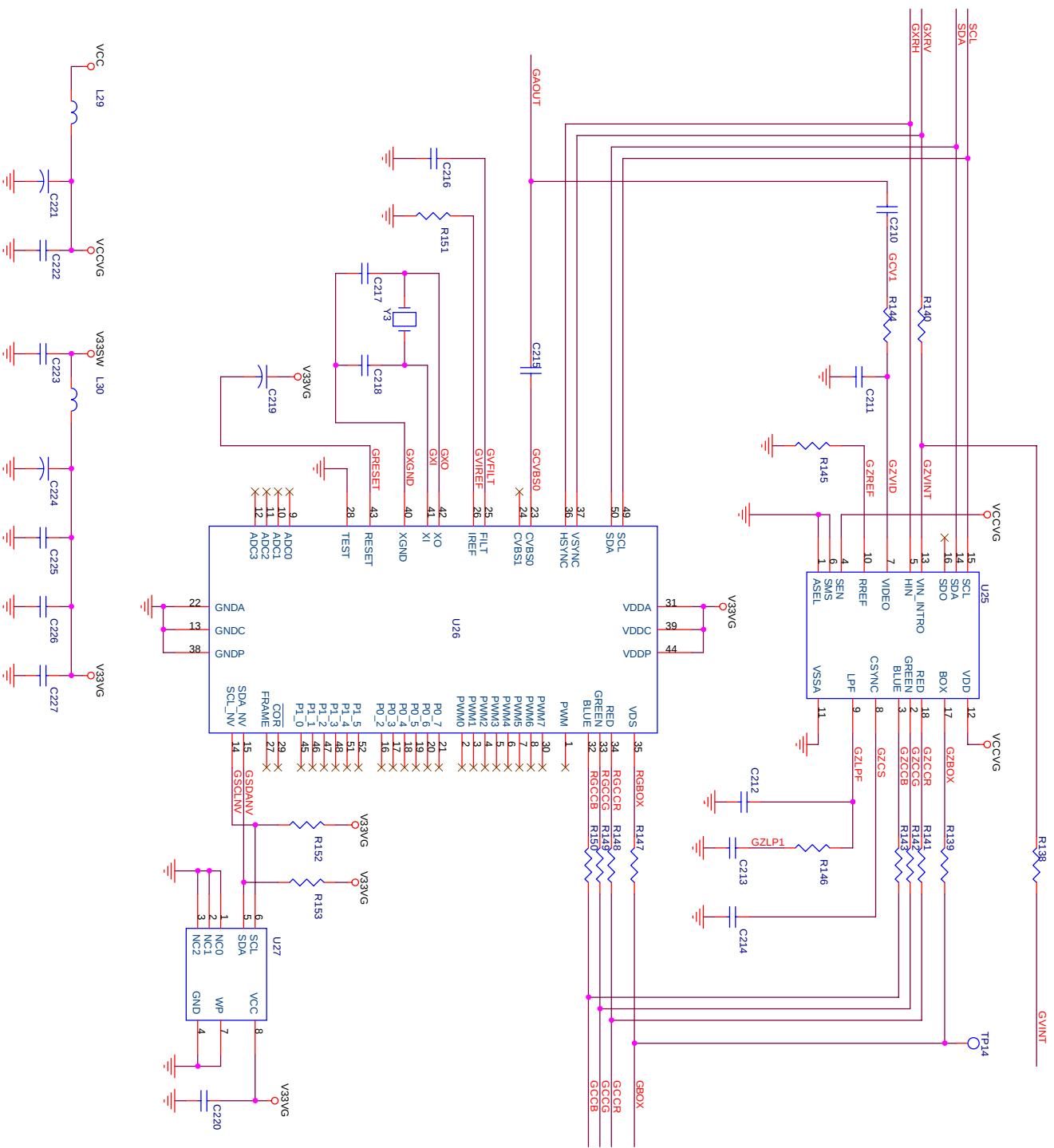
Title			SOUND_PRO	
Size	Document Number	Rev		
Custom Doc>				
Date:	Thursday, November 27, 2003	Sheet	5	of 20

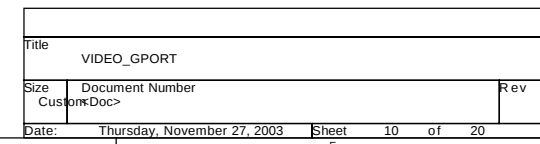


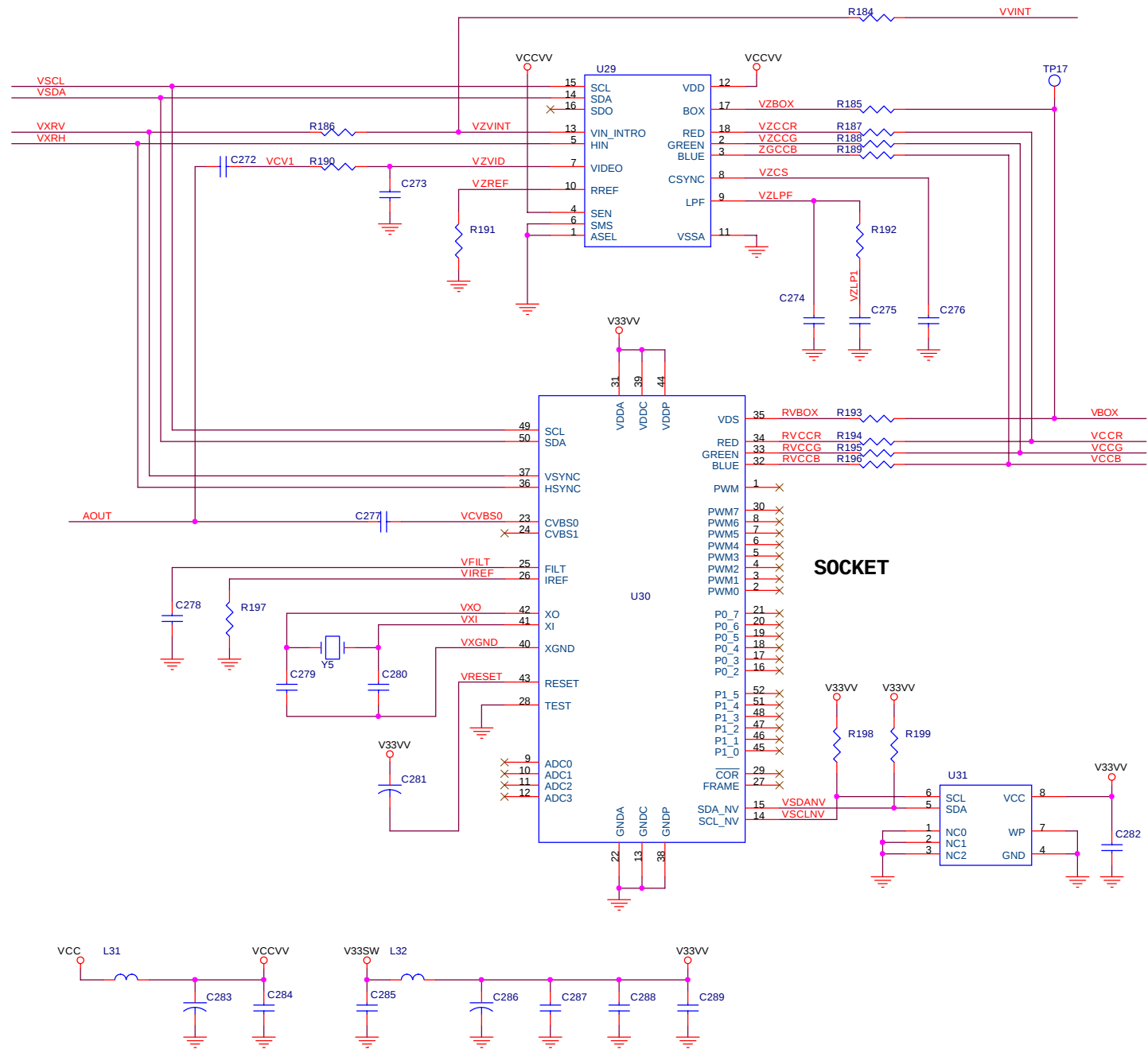


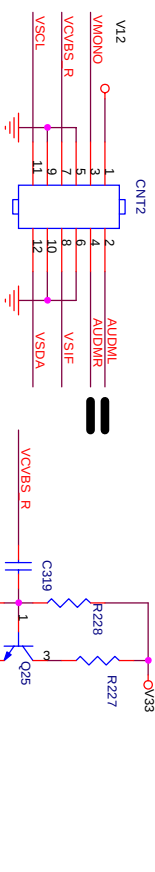
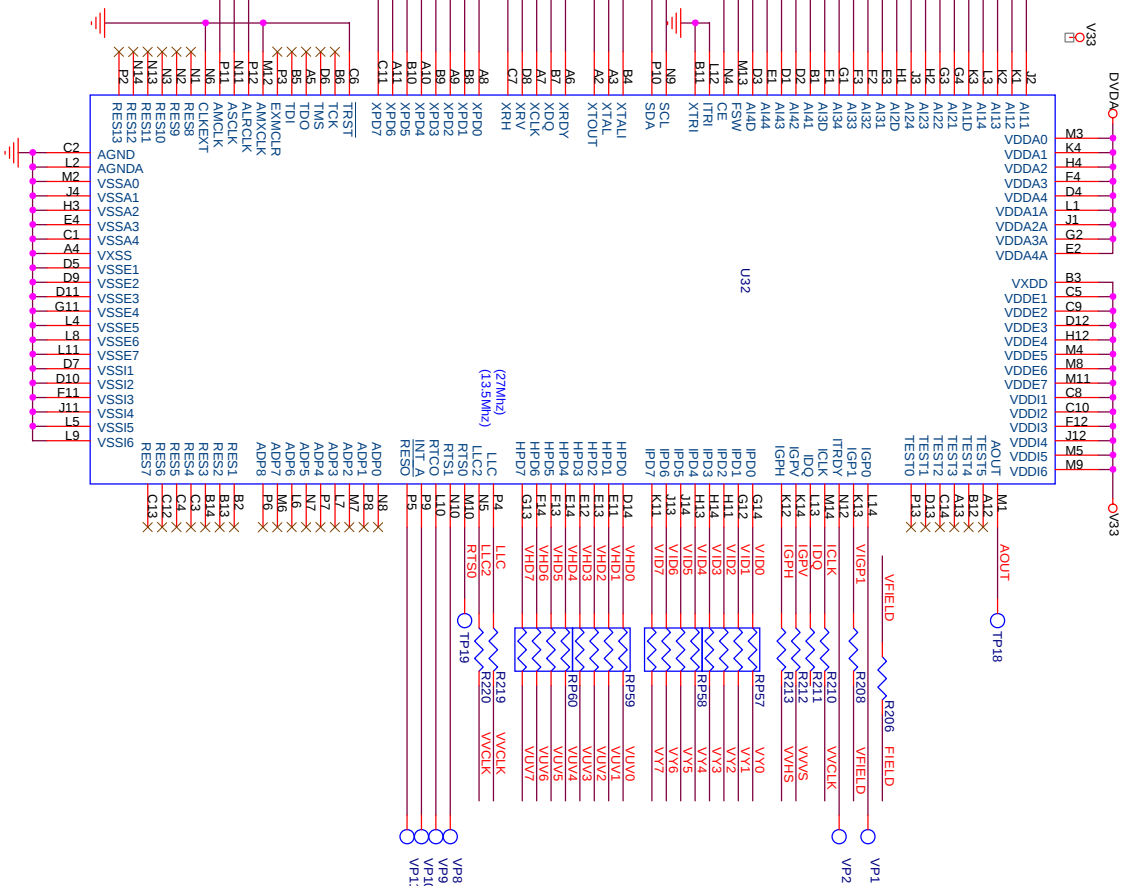


Title		
DVI_RCV		
Size	Document Number	Rev
Cus FormDoc>		
Date:	Thursday, November 27, 2003	Sheet 8 of 20

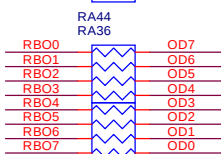
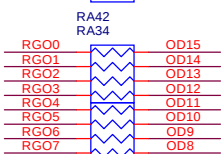
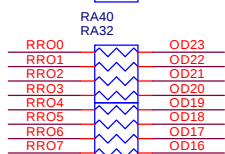
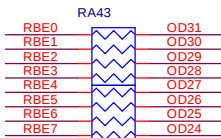
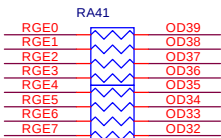
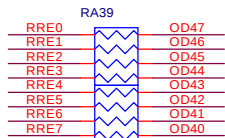
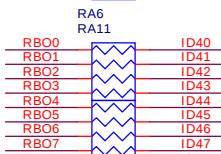
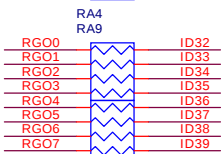
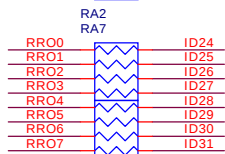
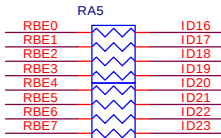
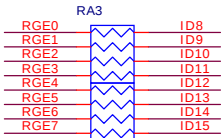
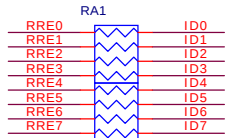
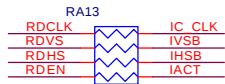




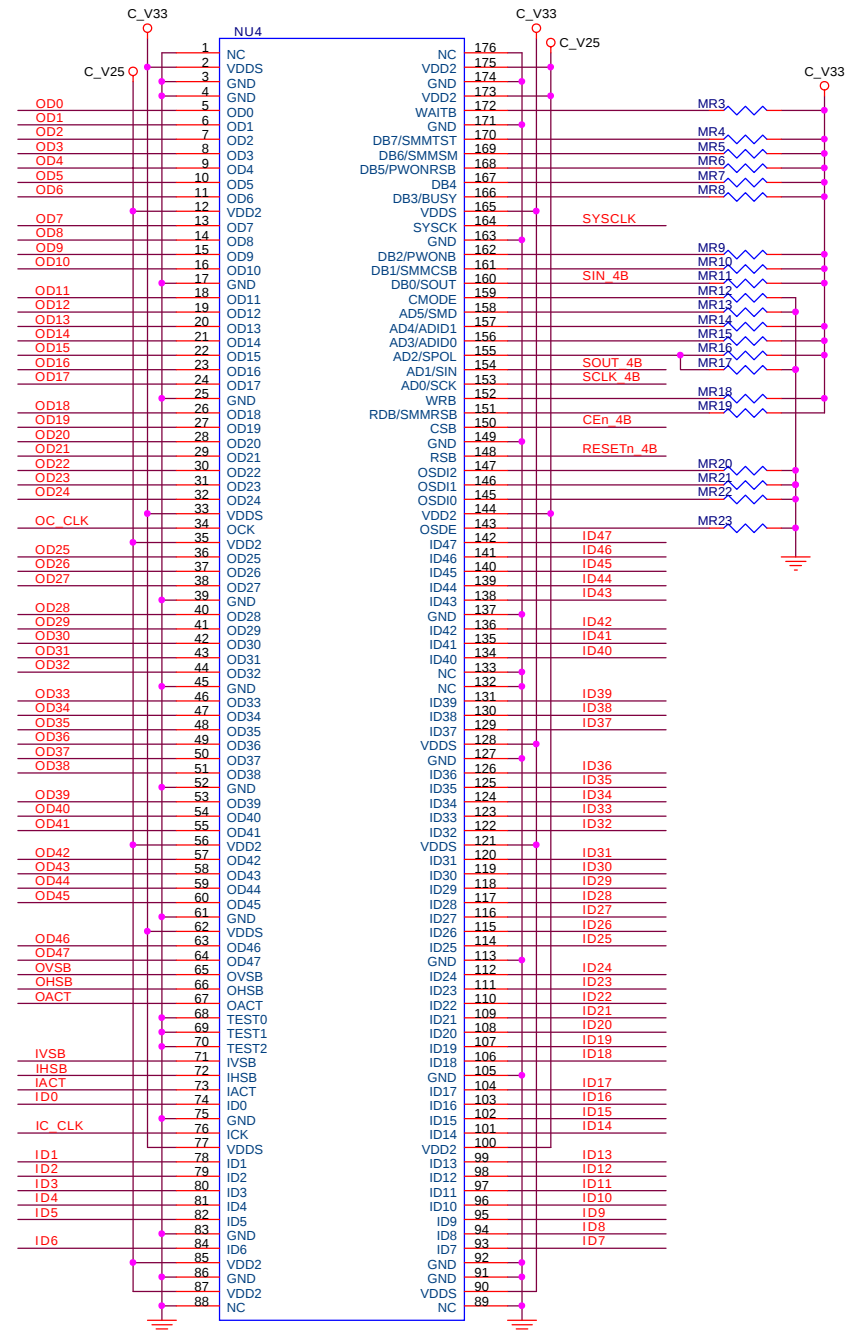
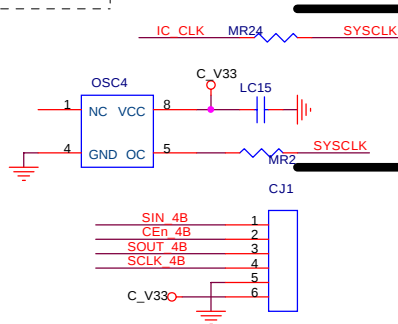
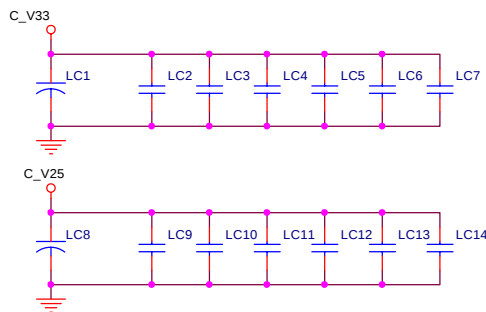
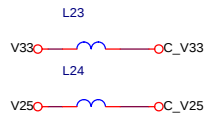


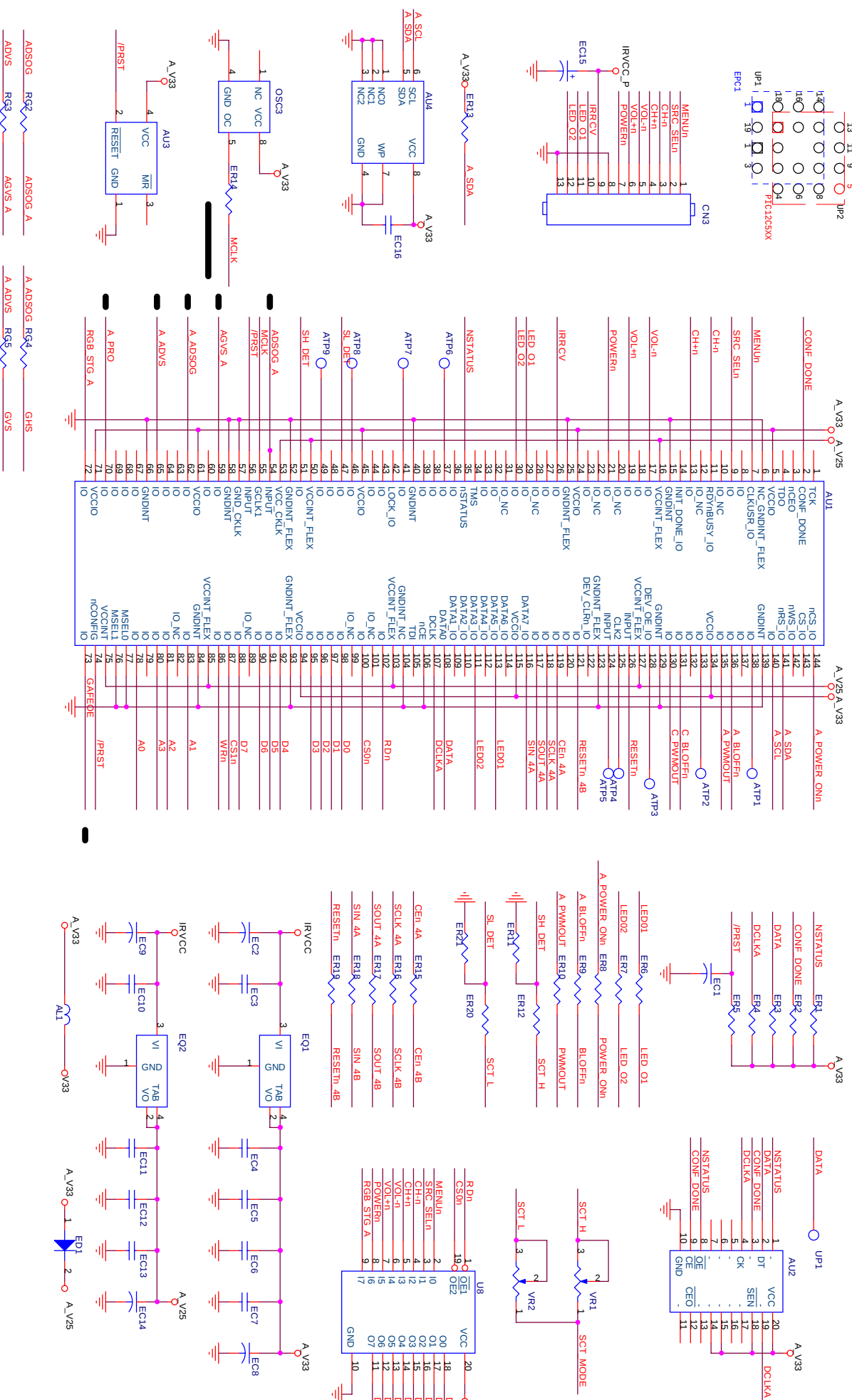
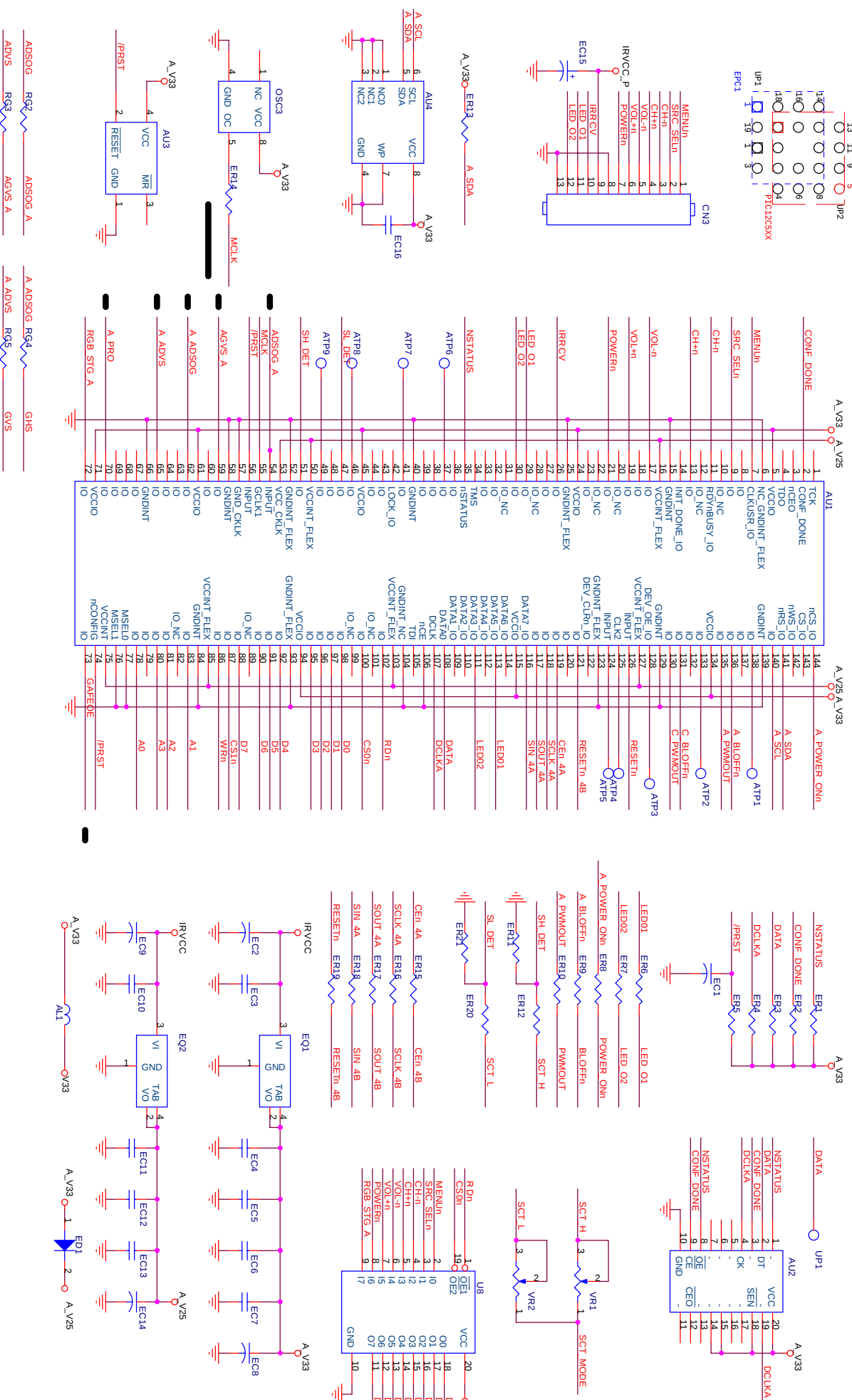
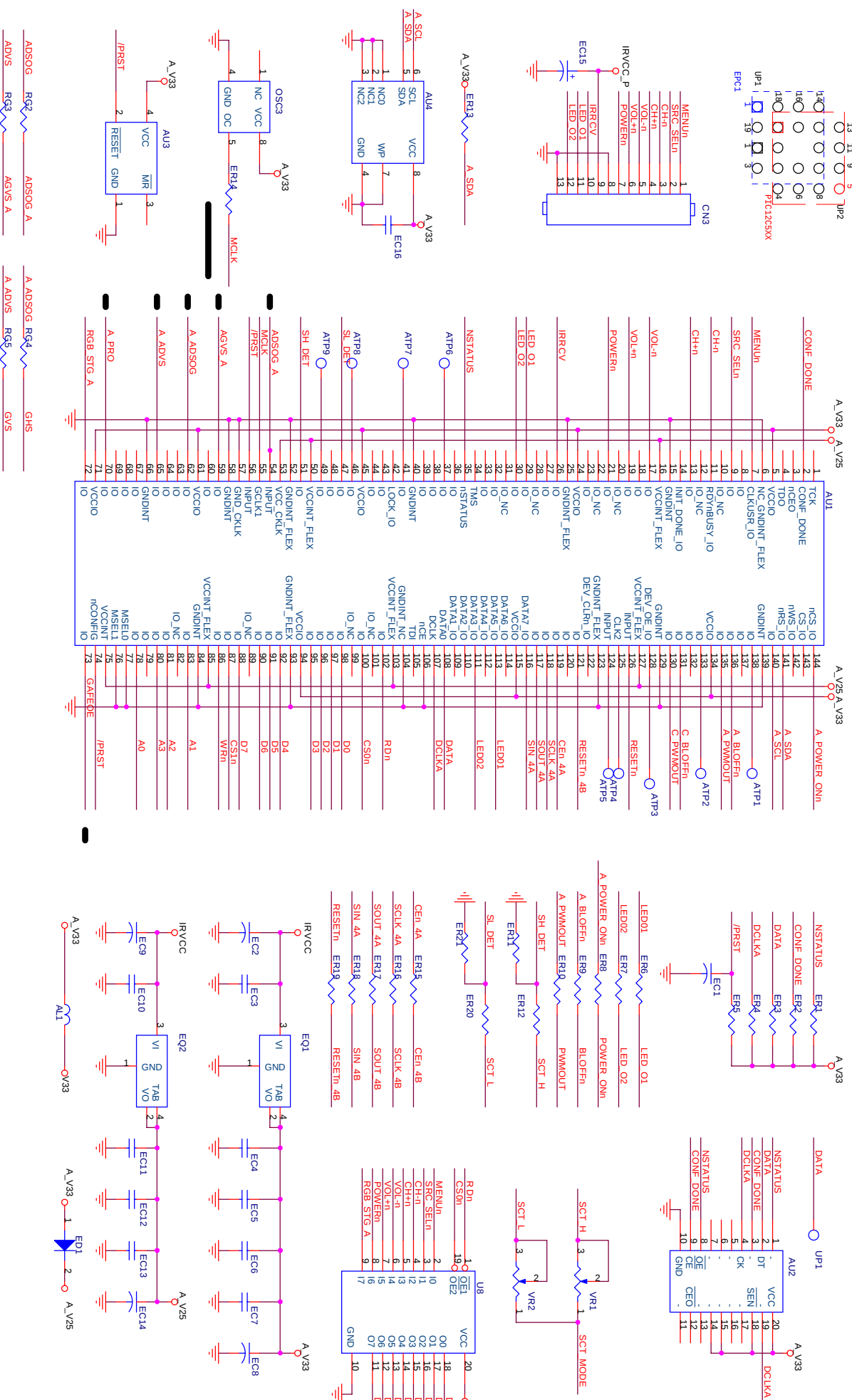
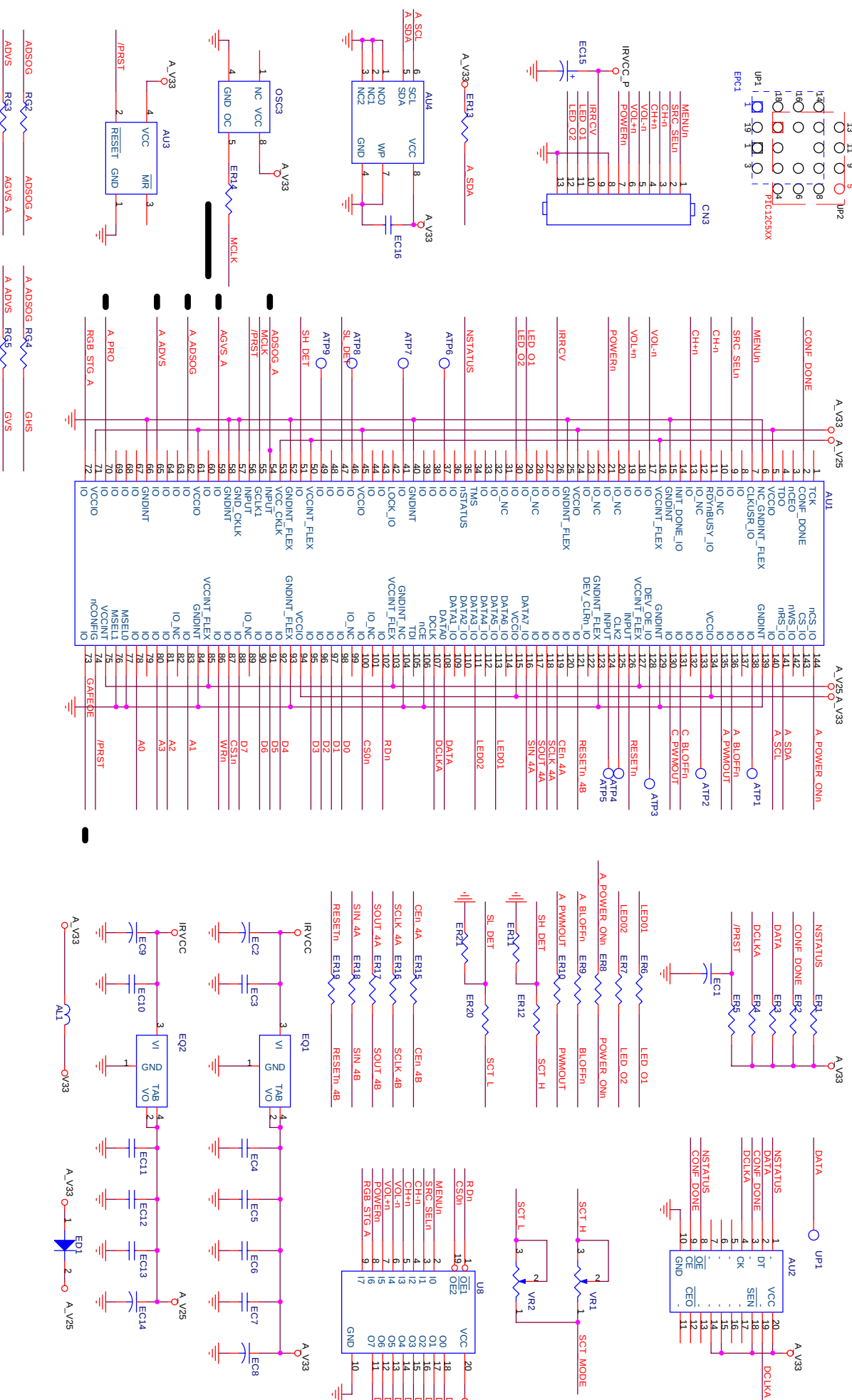


Title			
VIDEO_YPORT			
Size	Document Number	Rev	
Cus pnrDoc#			
Date:	Thursday, November 27, 2003	Sheet	12 of 20

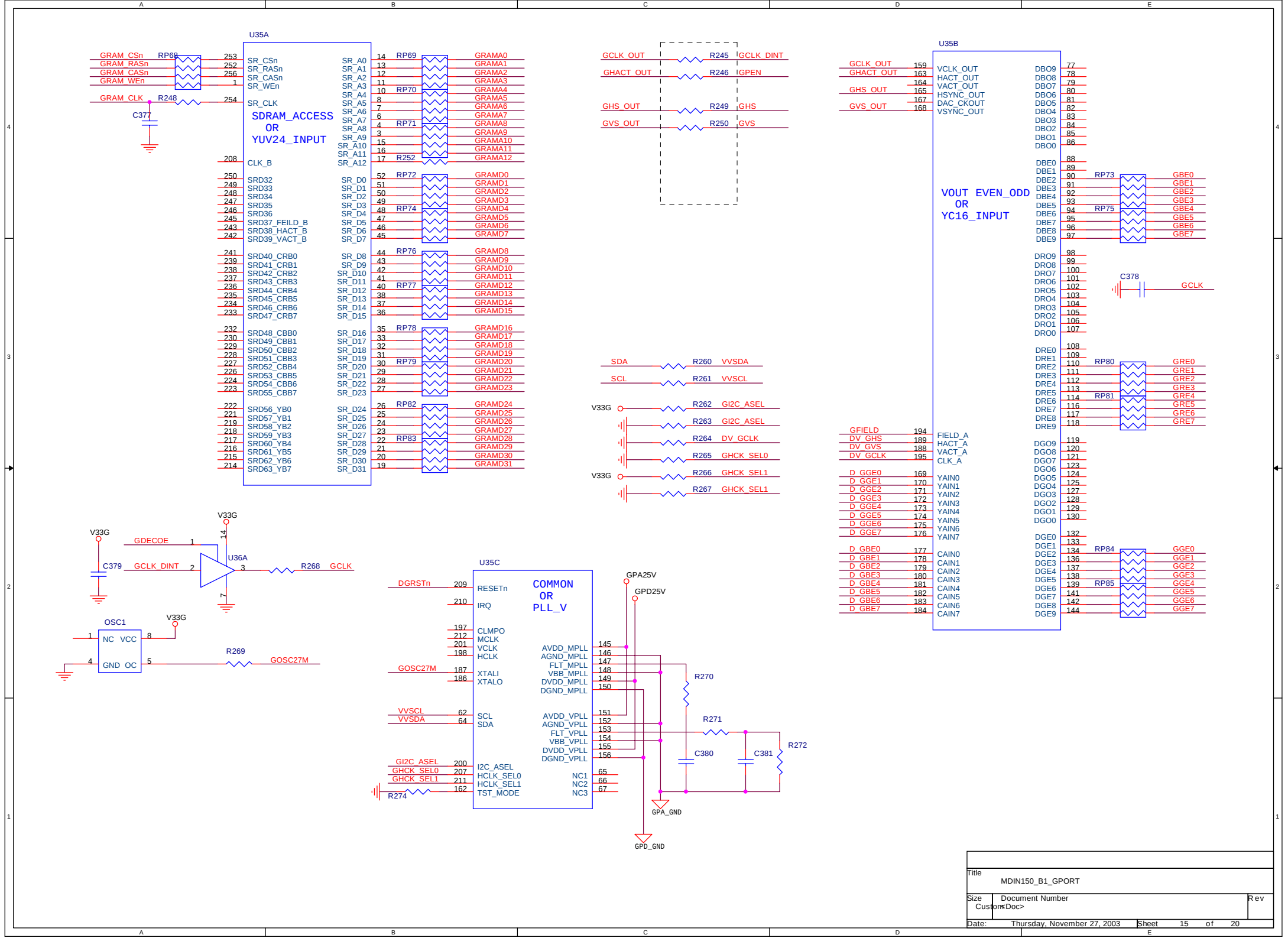


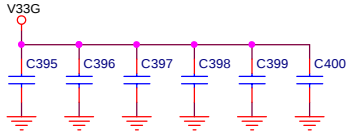
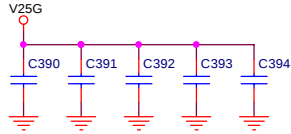
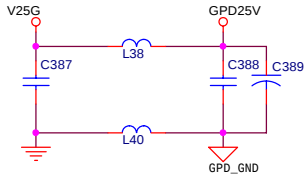
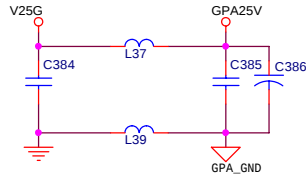
SOLD SIDE or BELOW NU4



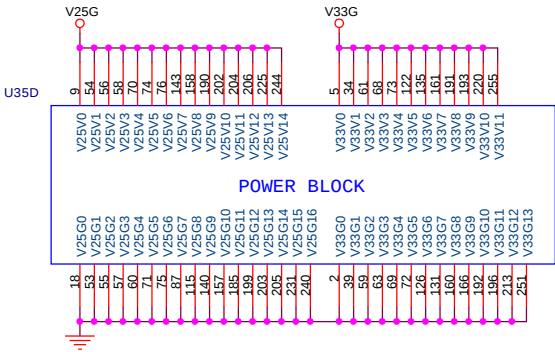
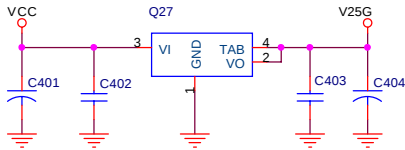


Title			
AL TERA			
Size	Document Number	Rev	
CUS bmtDOO			
Date:	Thursday, November 27, 2003	Sheet	14 of 20

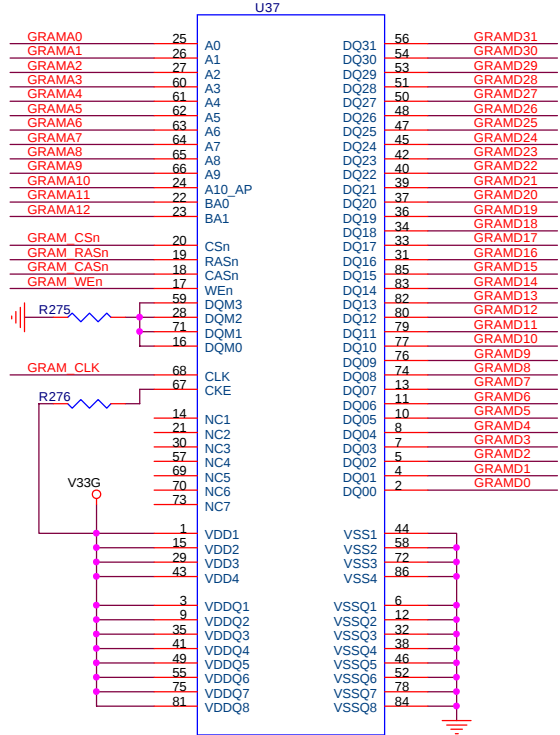


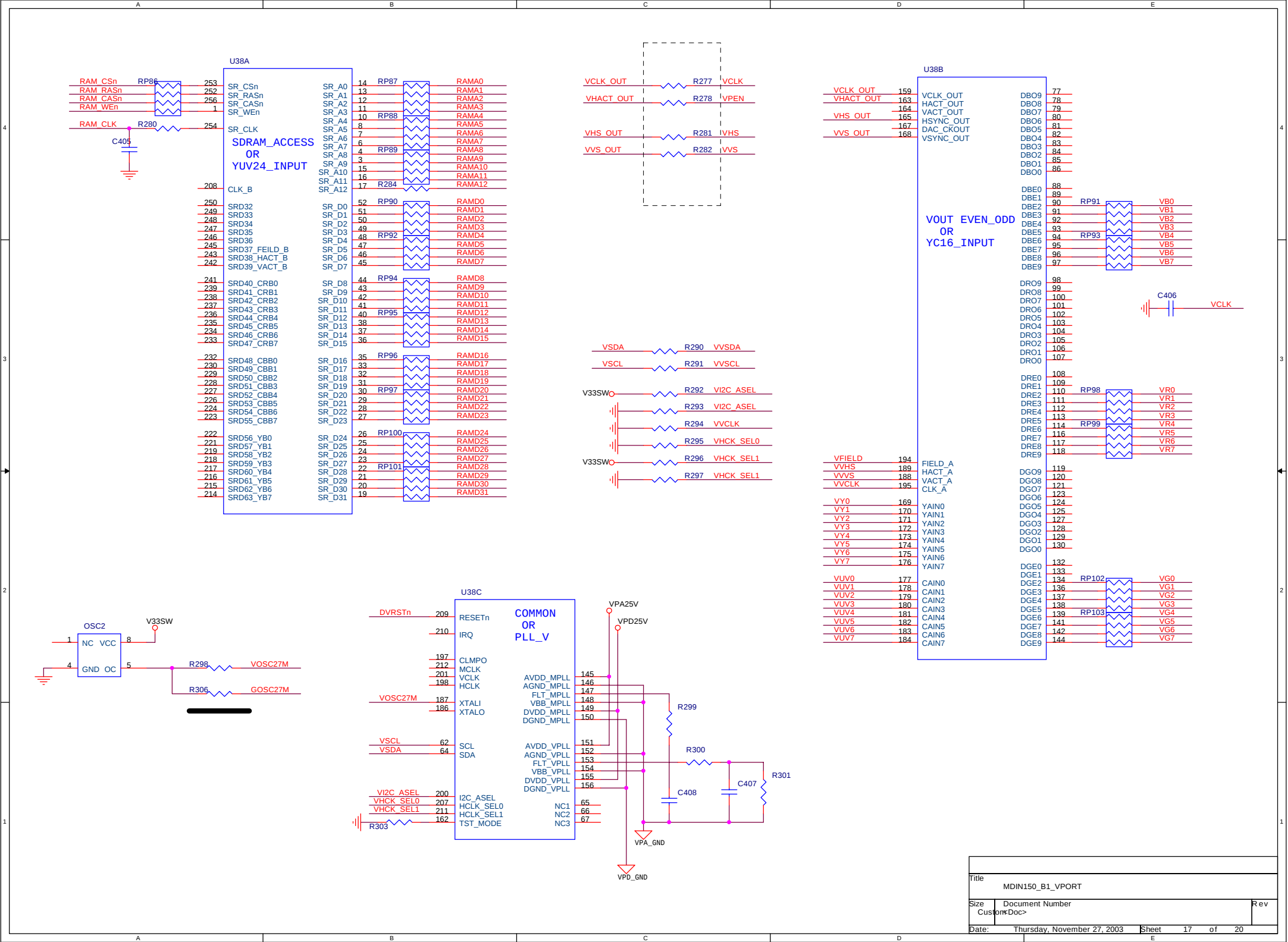


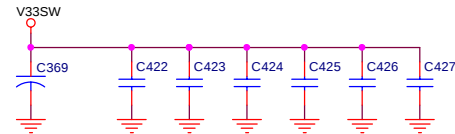
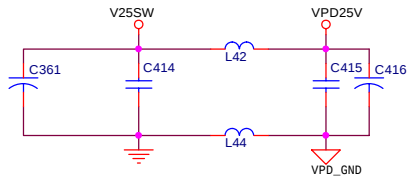
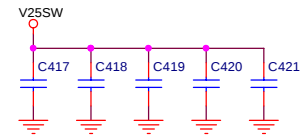
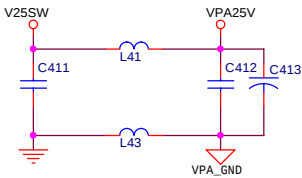
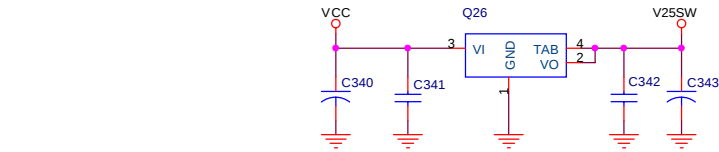
Regulator, Filters, and Caps for MDIN150



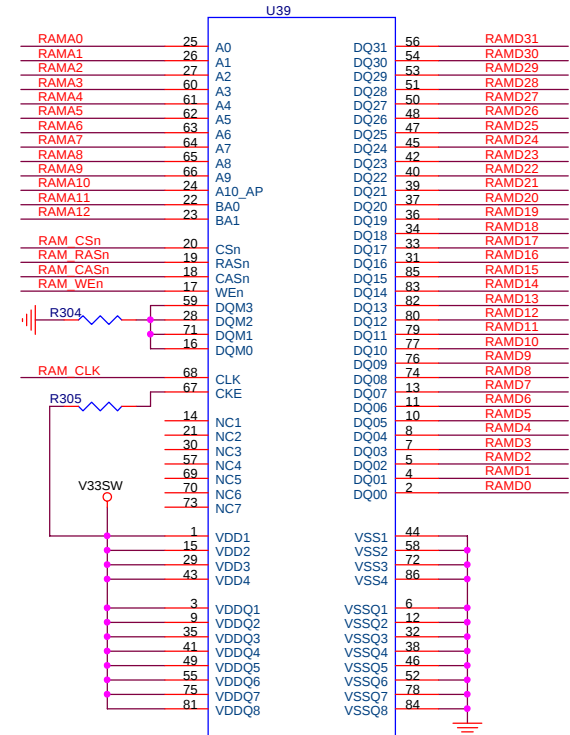
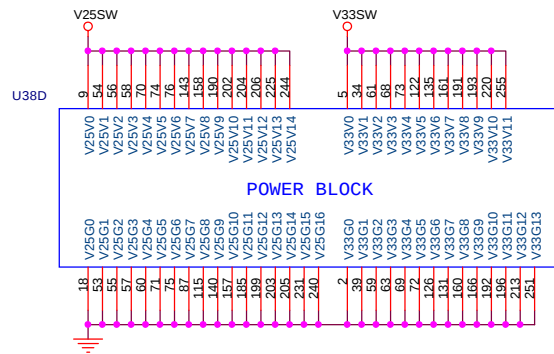
POWER BLOCK

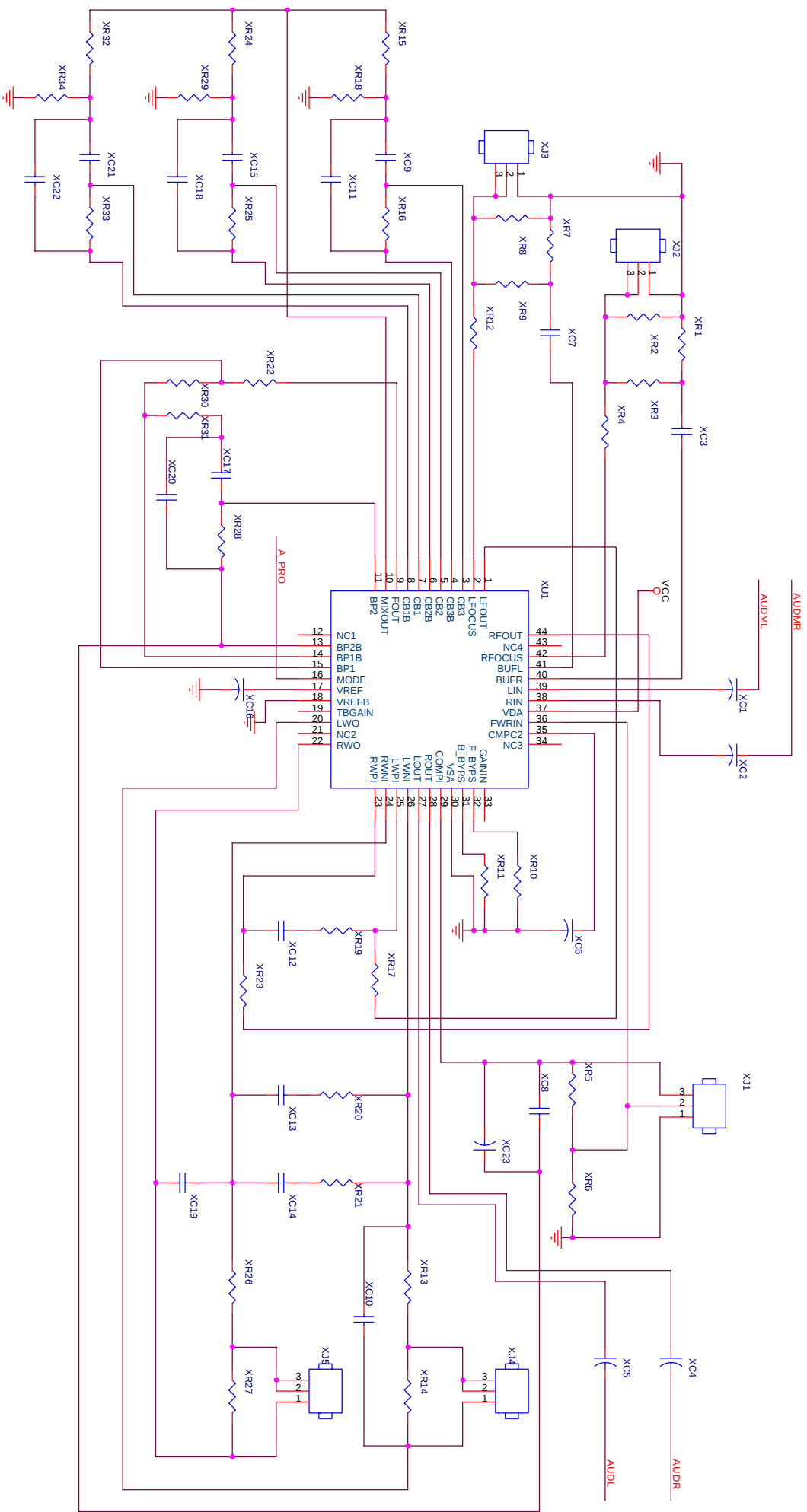




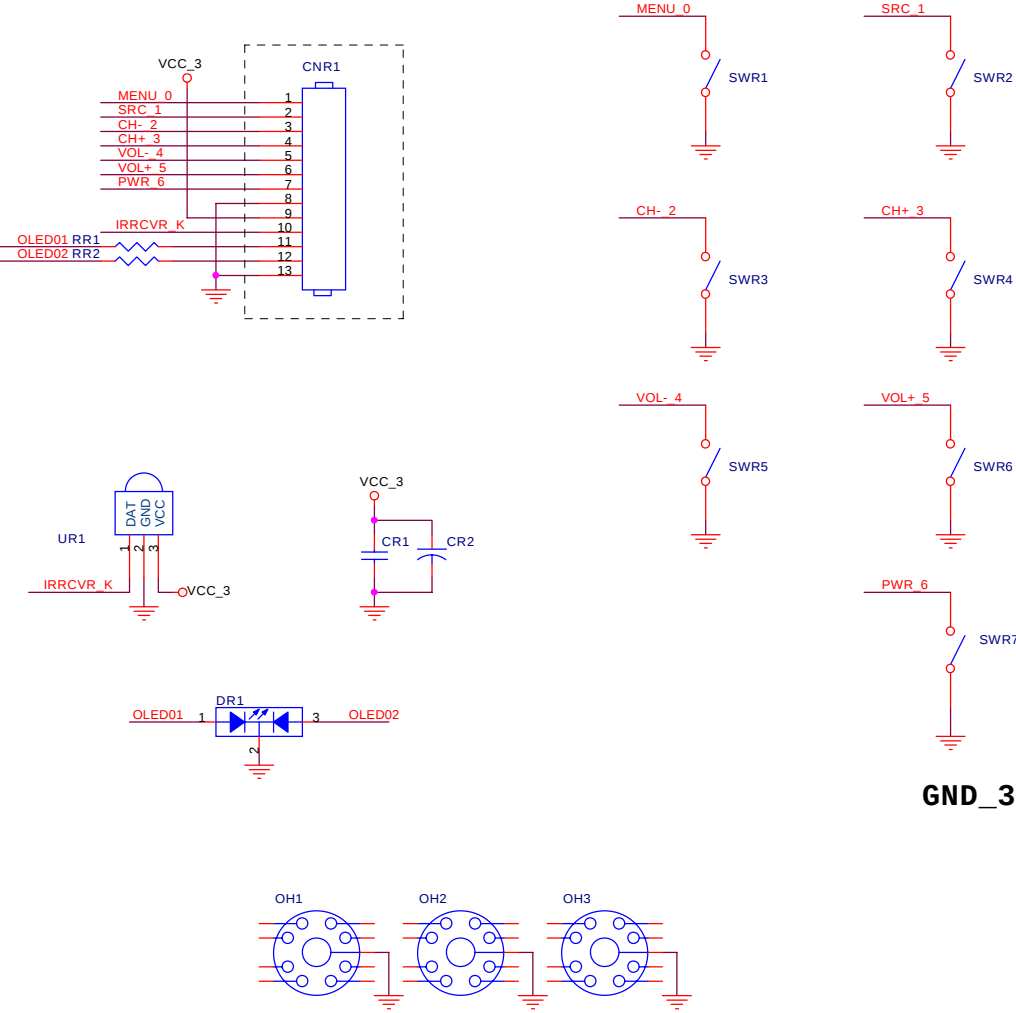


Regulator, Filters, and Caps for MDIN150



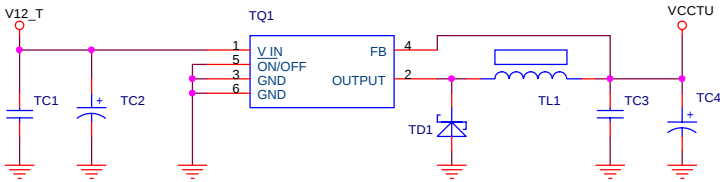
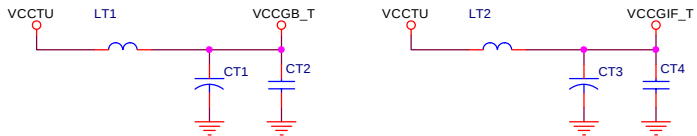
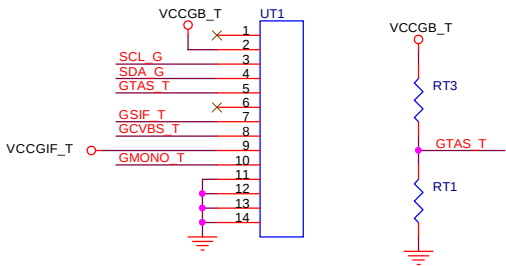
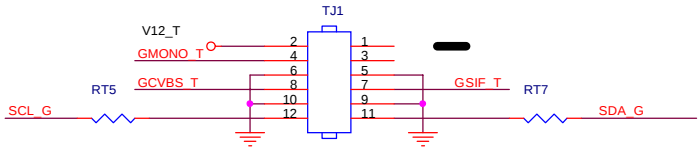


OSD CONN BOARD



GND_3

G - PORT



TUNER CONN BOARD

V - PORT

