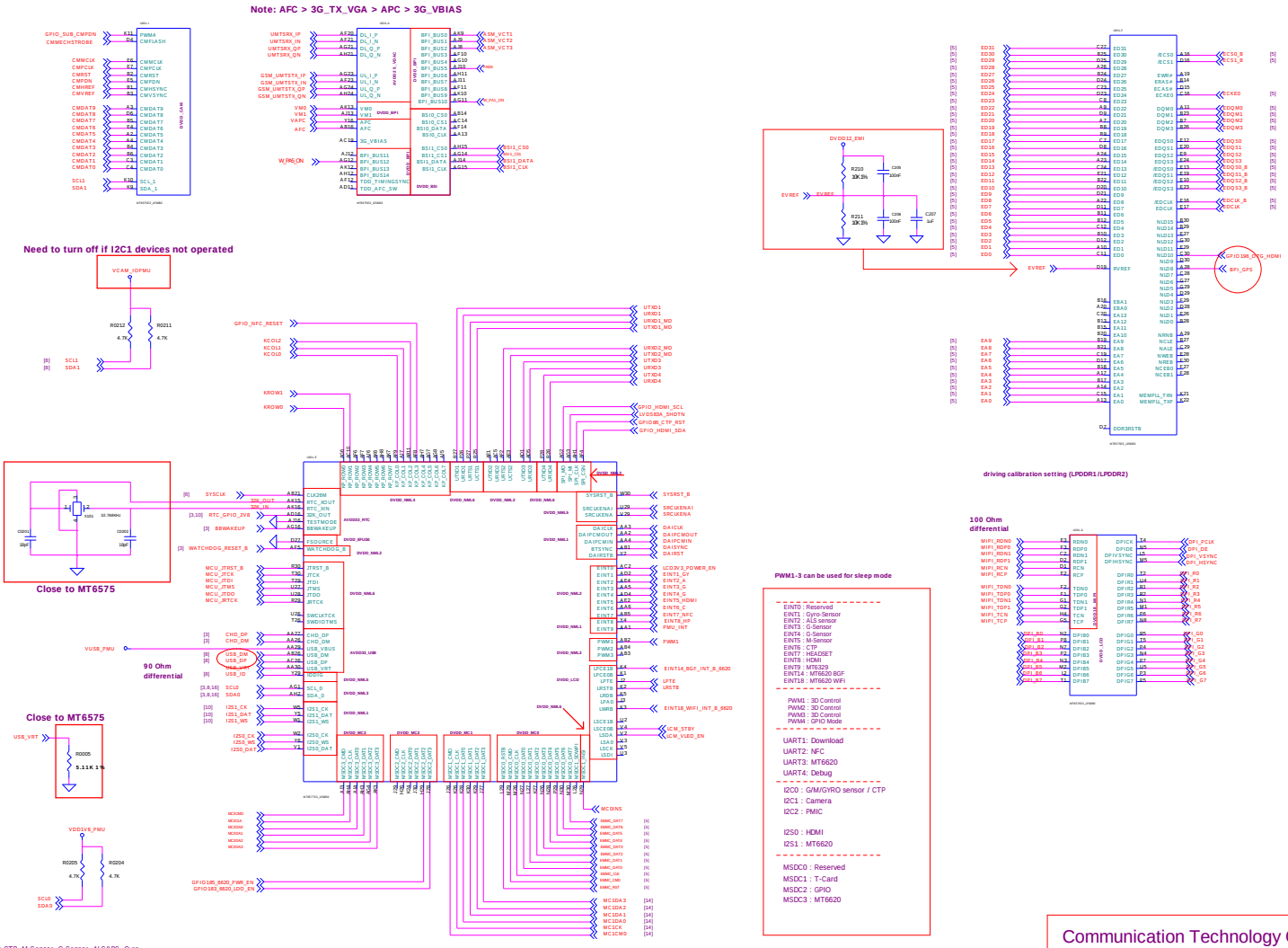
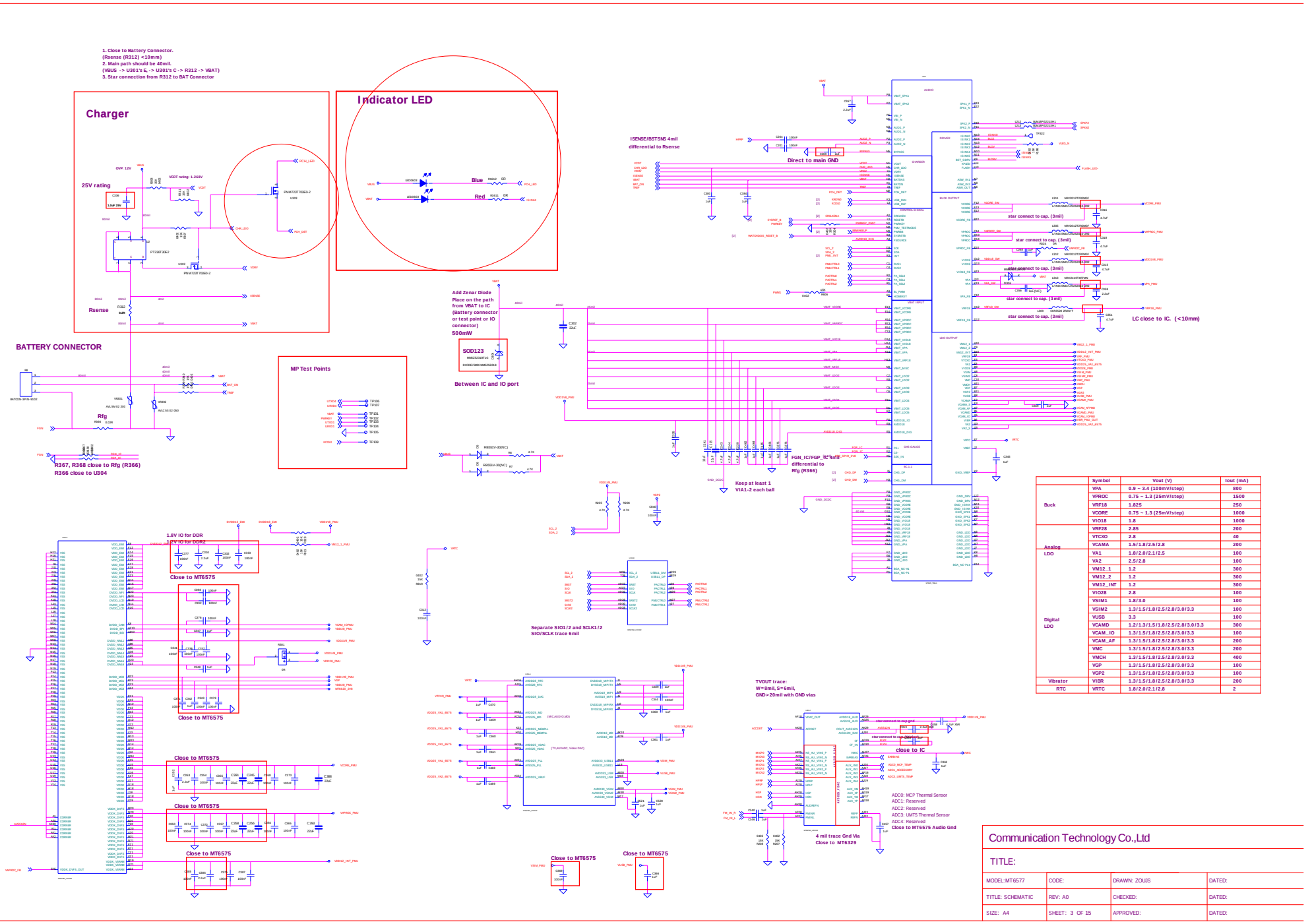


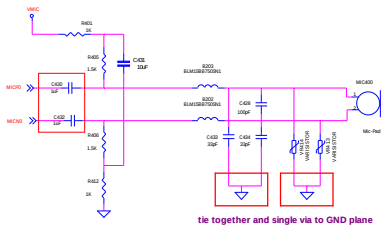
Communication Technology Co.,Ltd			
TITLE: Baseband			
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 2 OF 11	APPROVED:	DATED:



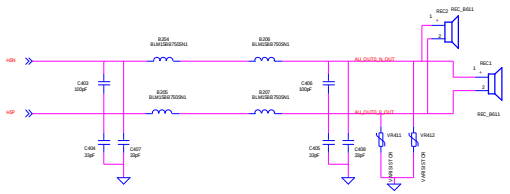
	Symbol	Vout (V)	Iout (mA)
Buck	VPA	0.9 ~ 3.4 (100mV/step)	800
	VPROC	0.75 ~ 1.3 (25mV/step)	1500
	VRF18	1.825	250
	VCORE	0.75 ~ 1.3 (25mV/step)	1000
	VIO18	1.8	1000
Analog LDO	VRF28	2.85	200
	VTCXO	2.8	40
	VCAMA	1.5/1.8/2.5/2.8	200
	VA1	1.8/2.0/2.1/2.5	100
	VA2	2.5/2.8	100
	VM12_1	1.2	300
	VM12_2	1.2	300
	VM12_INT	1.2	300
	VIO28	2.8	100
Digital LDO	VSIM1	1.8/3.0	100
	VSIM2	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100
	VUSB	3.3	100
	VCAMD	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	300
	VCAM_IO	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100
	VCAM_AF	1.3/1.5/1.8/2.5/2.8/3.0/3.3	200
	VMC	1.3/1.5/1.8/2.5/2.8/3.0/3.3	200
	VMCH	1.3/1.5/1.8/2.5/2.8/3.0/3.3	400
	VGP	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100
	VGP2	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100
Vibrator	VIBR	1.3/1.5/1.8/2.5/2.8/3.0/3.3	200
RTC	VRTC	1.8/2.0/2.1/2.8	2

Communication Technology Co.,Ltd			
TITLE:			
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 3 OF 15	APPROVED:	DATED:

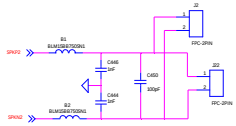
Handset Microphone 1

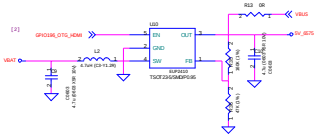


HANDSET RECEIVER



SPEAKER

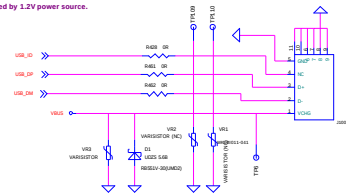




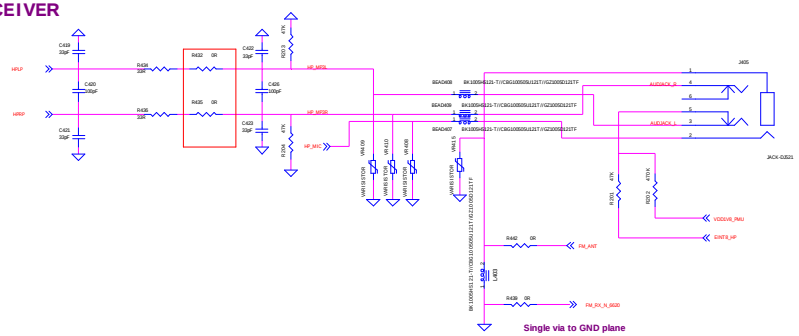
USB HS IF

If mini-A connector insert = > CID < 0V = > Low
If mini-B connector insert = > CID > 1.2V = > High

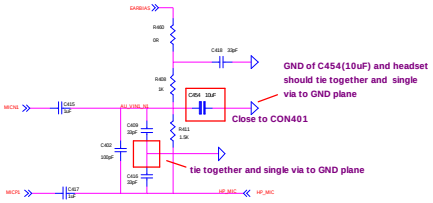
IDPULLUP pin is replaced by 1.2V power source.



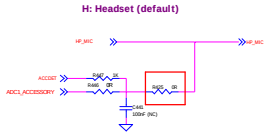
Earphone RECEIVER



Earphone MICPHONE

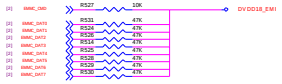


H: Headset (default)

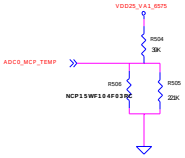


Communication Technology Co.,Ltd			
TITLE: AUDIO_USB			
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 4 OF 11	APPROVED:	DATED:

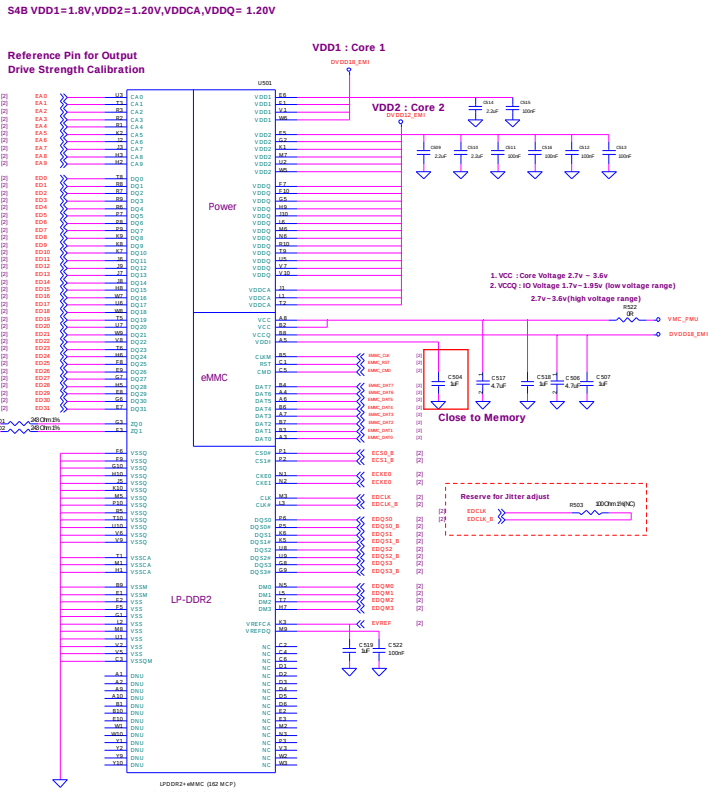
eMMC 增加上拉电阻

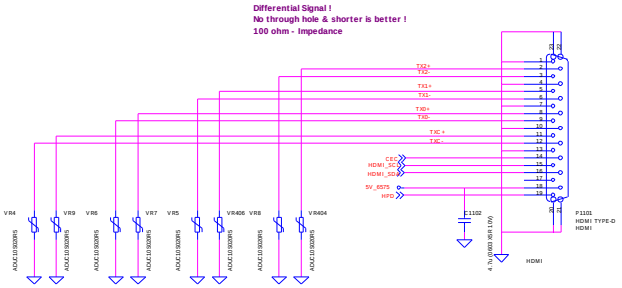


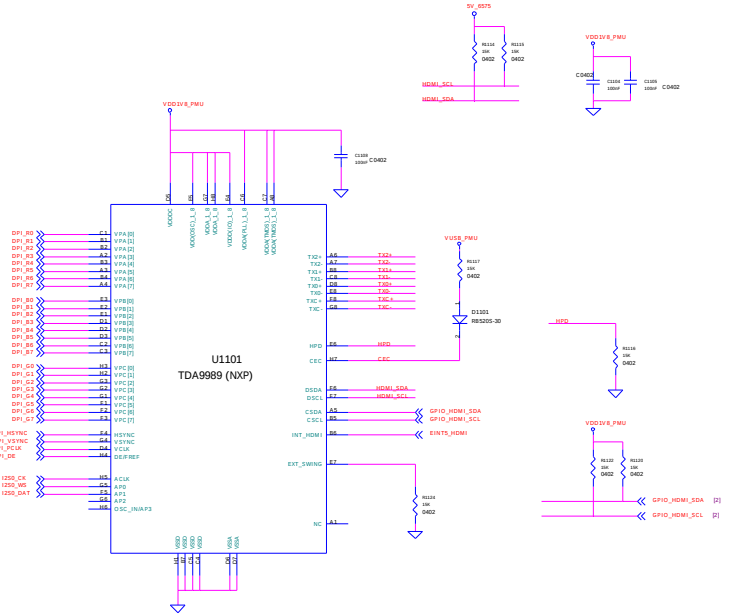
Thermal Sensor - MCP



MCP



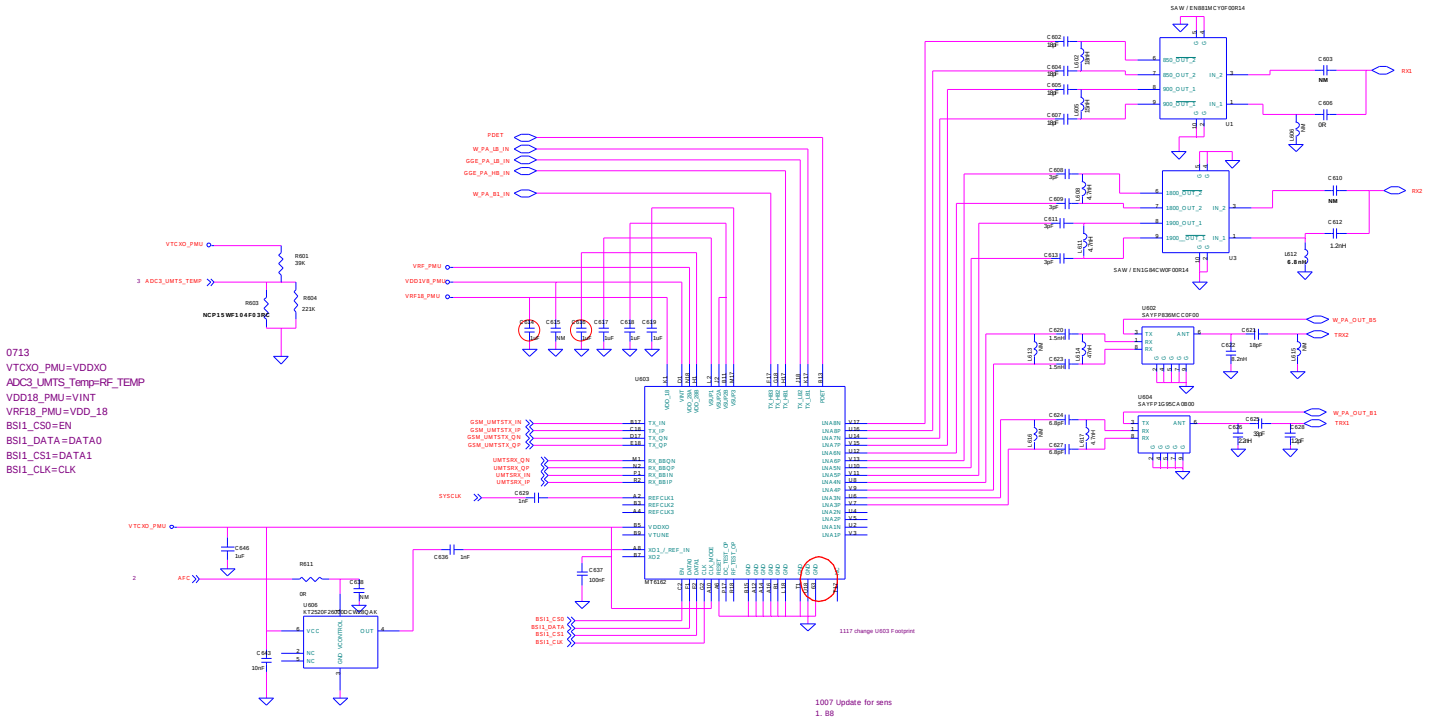




Communication Technology Co.,Ltd			
TITLE:	05 Memory (eMMC + x32LPDDR2)		
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 5 OF 11	APPROVED:	DATED:

RF_MT6162_RX

7/7 Add matching components and Use RFWS matching circuit

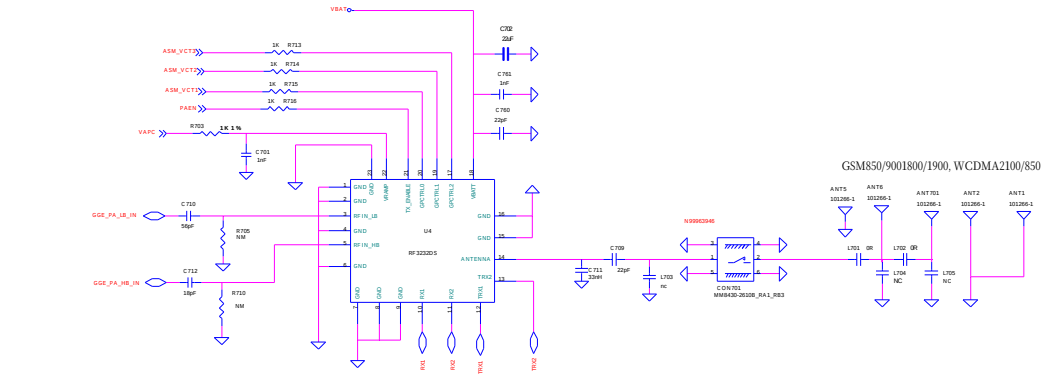


0713
VTCXO_PMU=VDDXO
ADC3_UMTS_Temp=RF_TEMP
VDD18_PMU=VINT
VRF18_PMU=VDD_18
BSI1_CS0=EN
BSI1_DATA=DATA0
BSI1_CS1=DATA1
BSI1_CLK=CLK

1117 Update Note
1. Change U603 Footprint to MT6162/AQFN62/P0.333/B0.27/6.2X6.2
2. Change U603 GND_PIN from Pin1 to Pin63
3. Add B2 Common Mode trap
4. VDD_28 change to VRF_PMU
5. Remove R605
6. Add R616, R631, R632 0201 Short 0ohm for Current Measure
7. C614, C616 from NM change to 1uF
8. VBAT in RF change to VBAT_RF, add 0805 0ohm
9. VTCXO_PMU add LDO

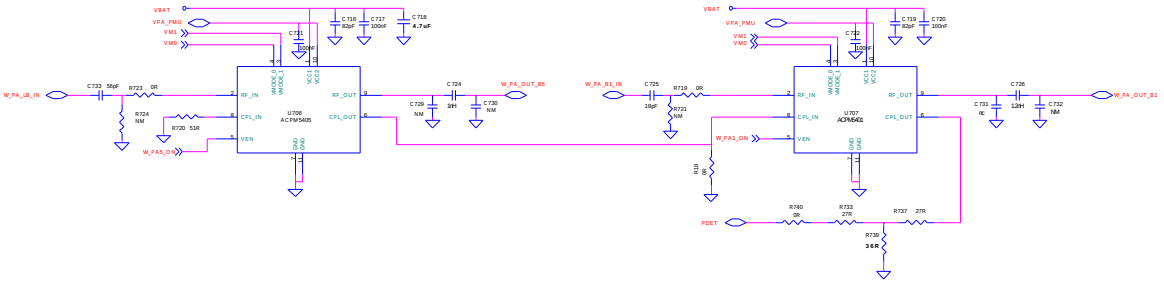
1007 Update for sens
1. B8
C634 from 8.2nH to 5.6nH
2. B2 use B7955, C639,
C642 from 8.2pF to 6.8pF,
L621 from 5.6nH to 6.8nH, C641 from 3.6nH to 15nH
3. B1
C624 and C627 from 8.2pF to 6.8pF, C625 from 1.2nH to 1.1nH
4. DCS
C608, C609 from 39pF to 3pF

Communication Technology Co.,Ltd			
TITLE:	RF_MT6162_RX		
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 6 OF 11	APPROVED:	DATED:



RF3232DS Control Truth Table

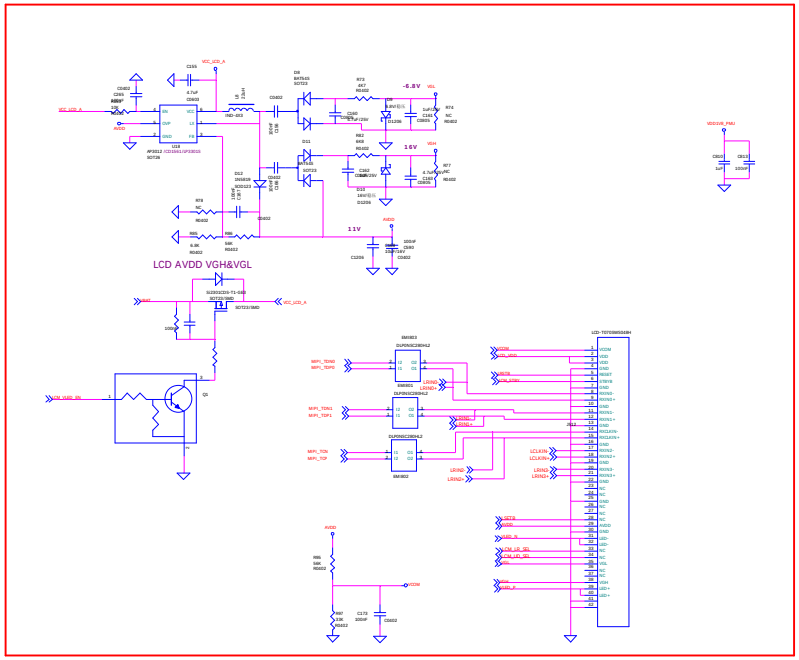
	TX_EN	Vct2	Vct1	Vct0
Standby	0	0	0	0
RX1	0	1	0	0
RX2	0	0	1	0
TRX1	0	0	1	1
TRX2	0	0	0	1
LOW BAND	1	0	1	0
HIGH BAND	1	0	1	1

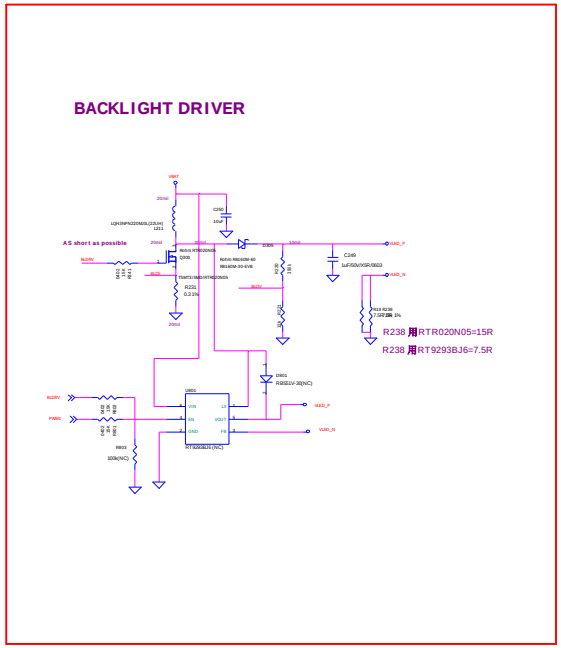


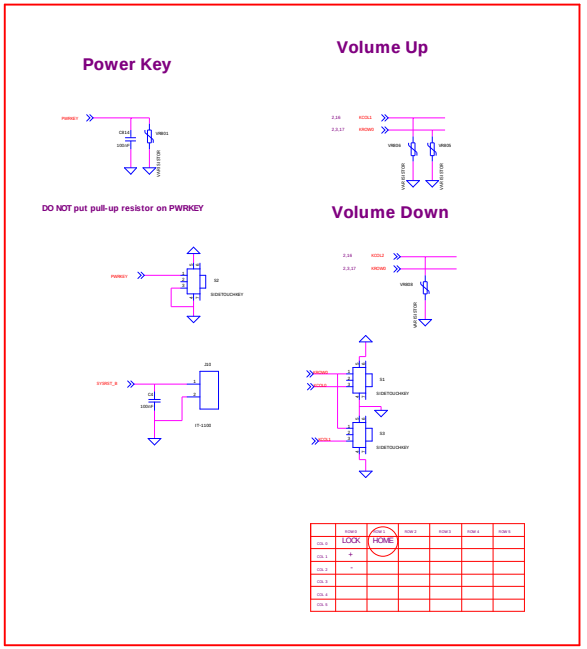
0713 Update Net Name

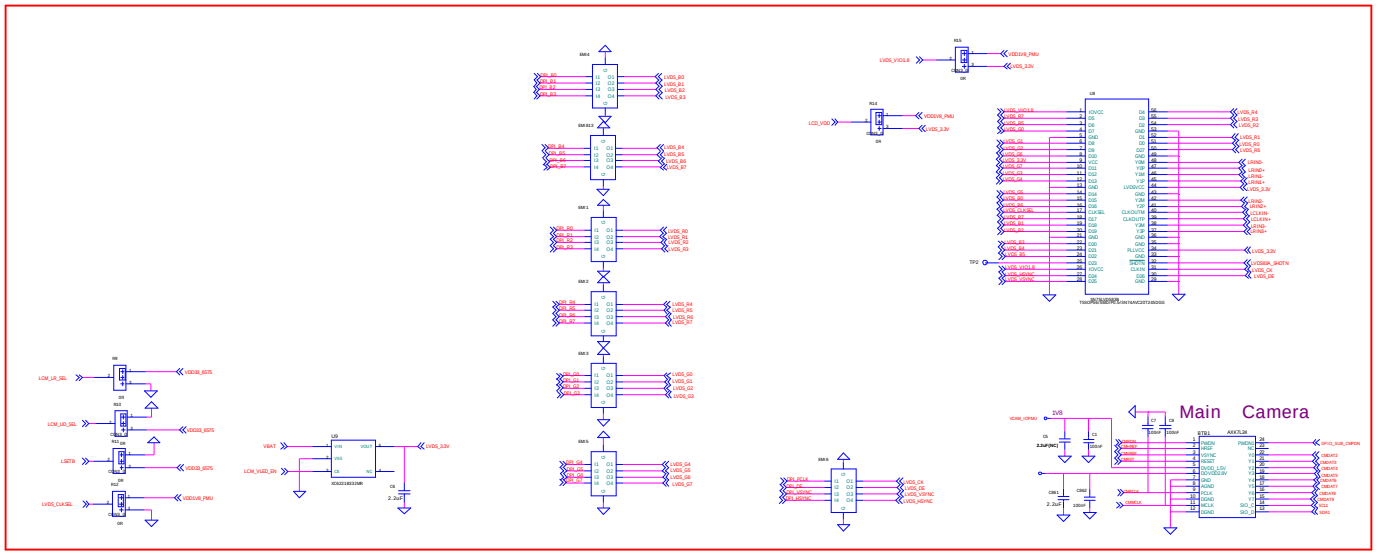
ADC6_UMTS_POWER	=GGE_PA_VDET
EDGE_MODE	=GGE_PA_MODE
ADC6_UMTS_POWER	=GGE_PA_VDET
VAPC	=GGE_PA_VRAMP
BANDSW	=GGE_PA_BS
PAEN	=GGE_PA_ENABLE
VM1	=W_PA_VMODE1
VM0	=W_PA_VMODE0
W_PA5_ON	=W_PA_B5_EN
W_PA8_ON	=W_PA_B8_EN
W_PA1_ON	=W_PA_B1_EN
W_PA2_ON	=W_PA_B2_EN

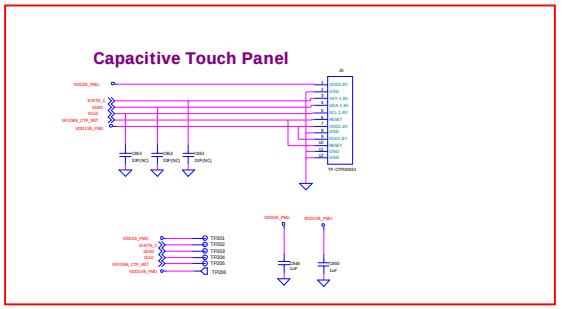
Communication Technology Co.,Ltd			
TITLE: RF_TX_ASM			
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 7 OF 11	APPROVED:	DATED:



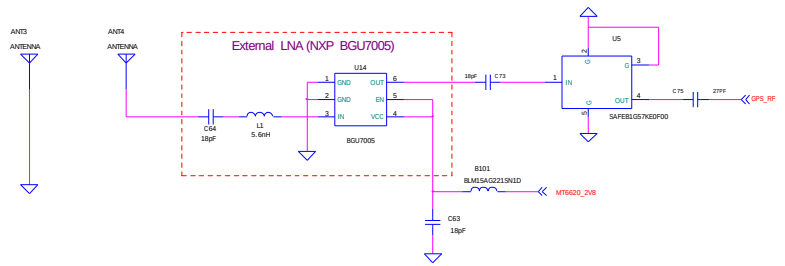


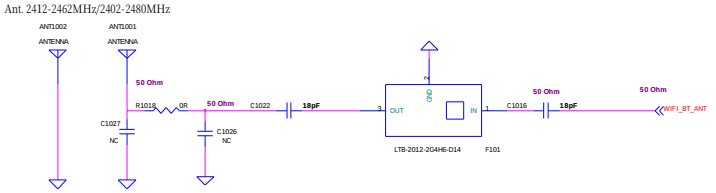


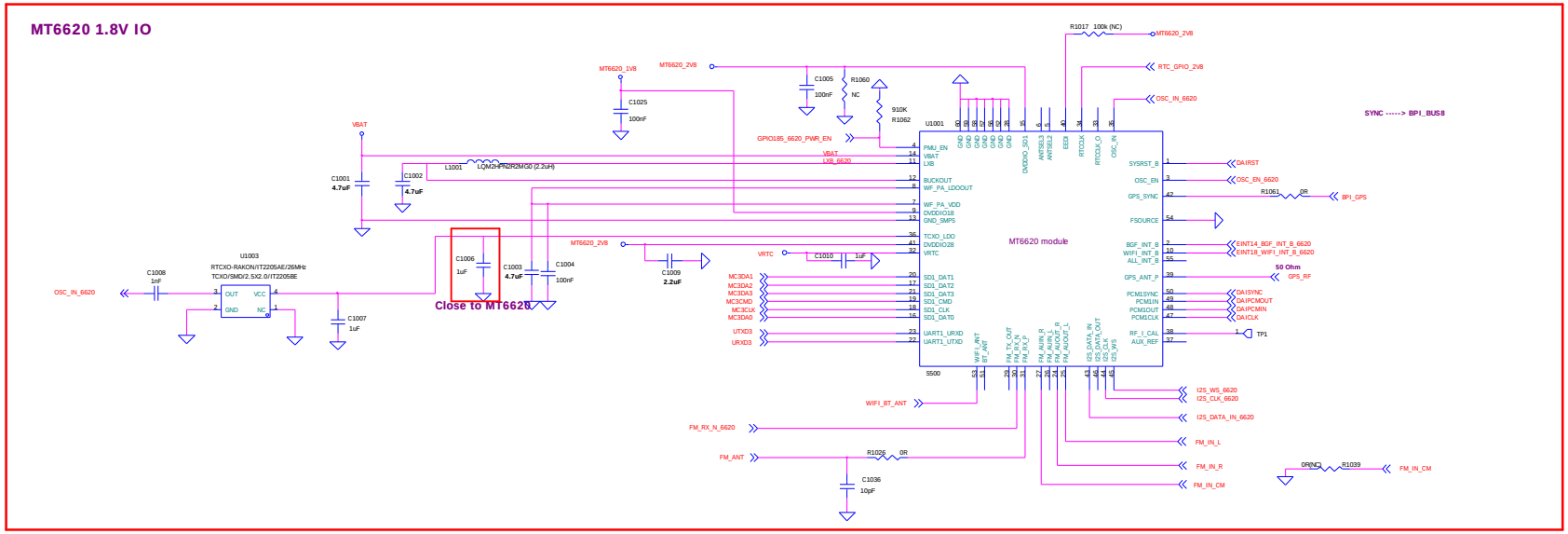


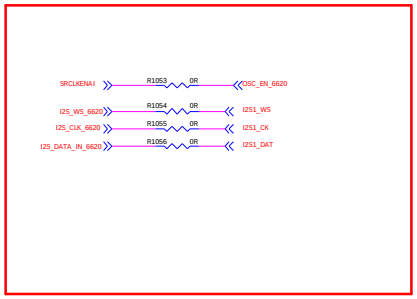


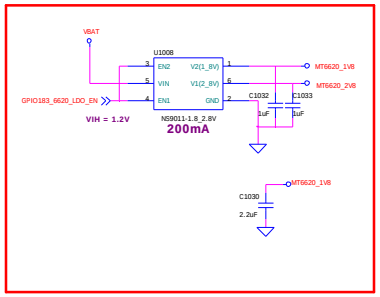
Communication Technology Co.,Ltd			
TITLE:	LCD_CTP_CAMERA		
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 8 OF 11	APPROVED:	DATED:











		XTEST	FEDI	ANTSEL_3	
clock setting	2.8V TCXO or OSC	0	0	0	Default
	1.8V TCXO or OSC	0	1	0	
	XTAL	0	0	1	
	external clock mode	0	1	1	

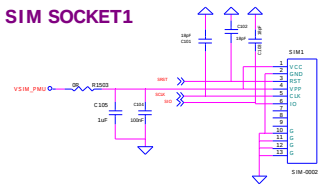
		XTEST	ANTSEL_2	ANTSEL_1	
WIFI host interface	WIFI :SDIO1	0	0	0	Default
	WIFI :SDIO2	0	0	1	
	WIFI :SPI	0	1	0	
	WIFI :reserved	0	1	1	

		XTEST	ANTSEL_0		
BT/common host interface	UART1	0	0		Default
	SDIO2	0	1		

Communication Technology Co.,Ltd			
TITLE:	MT6620_BT_FM_GPS_WIFI		
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 9 OF 11	APPROVED:	DATED:

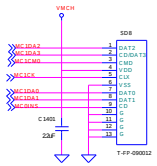
SIM1

SIM SOCKET1



MSDC1

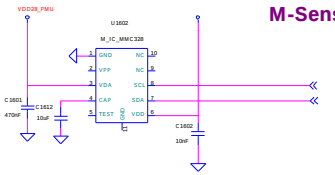
SD CARD



CD:
H: Card remove
L: Card insert

Communication Technology Co.,Ltd			
TITLE: SIM& TF			
MODEL:MT6577	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 11OF11	APPROVED:	DATED:

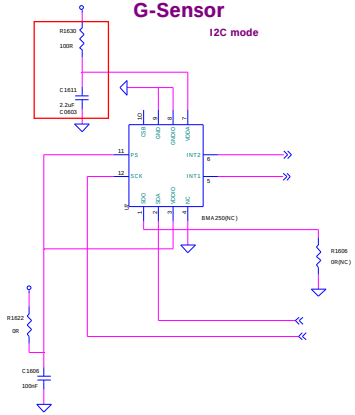
M-Sensor



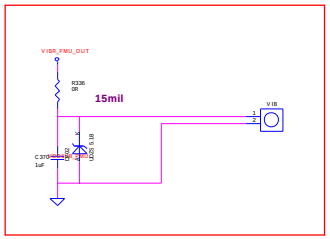
I2C address: 0x0F (Write:0x1E, Read:0x1F)

G-Sensor

I2C mode



VIBRATOR



Communication Technology Co.,Ltd			
TITLE: SENSORS			
MODEL:MT6575	CODE:	DRAWN: ZOUJS	DATED:
TITLE: SCHEMATIC	REV: A0	CHECKED:	DATED:
SIZE: A4	SHEET: 11OF15	APPROVED:	DATED: