

JBL WHEN AVR 1394 BOARD

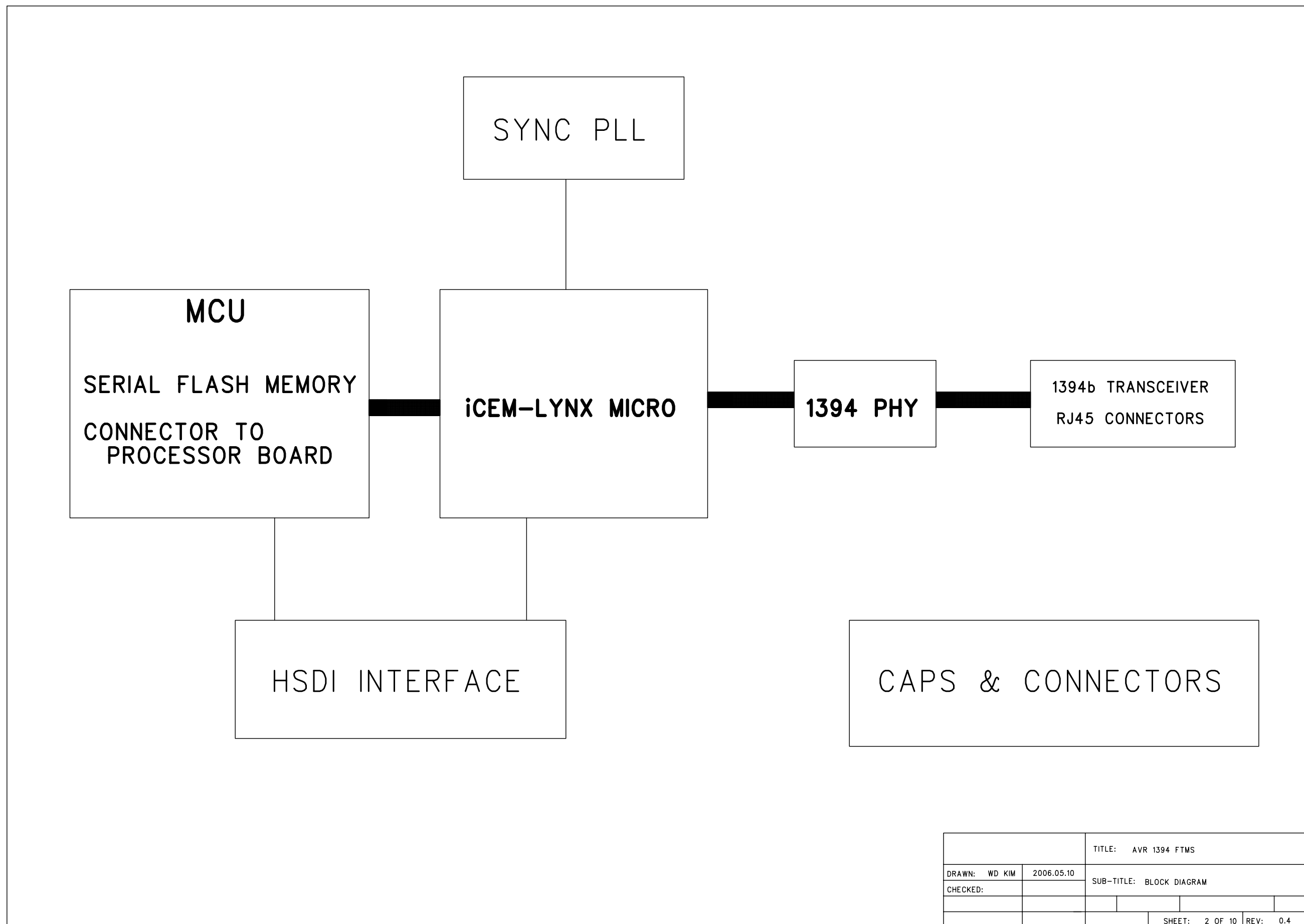
Schematic Diagram

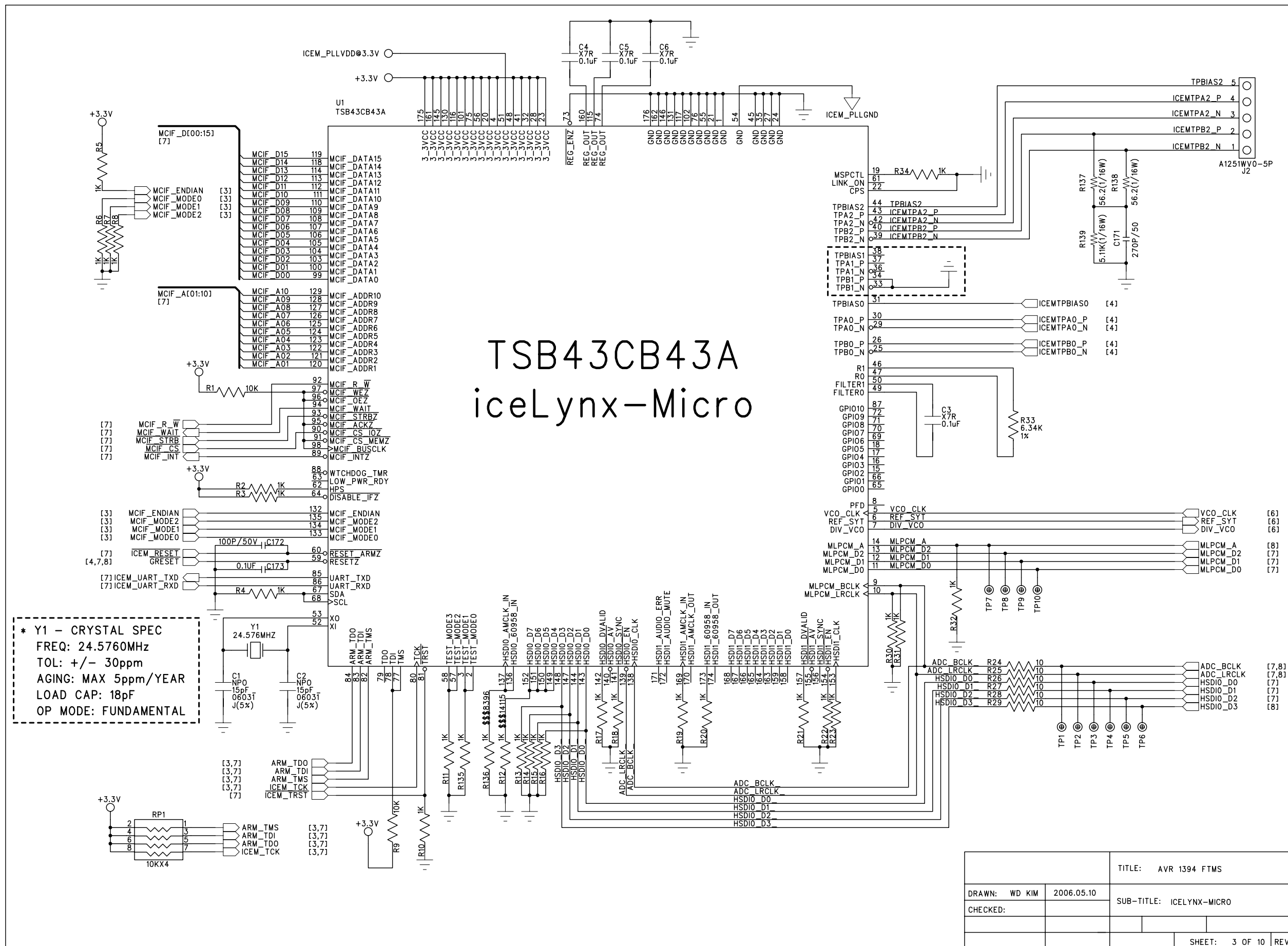
Rev: MP 0.1

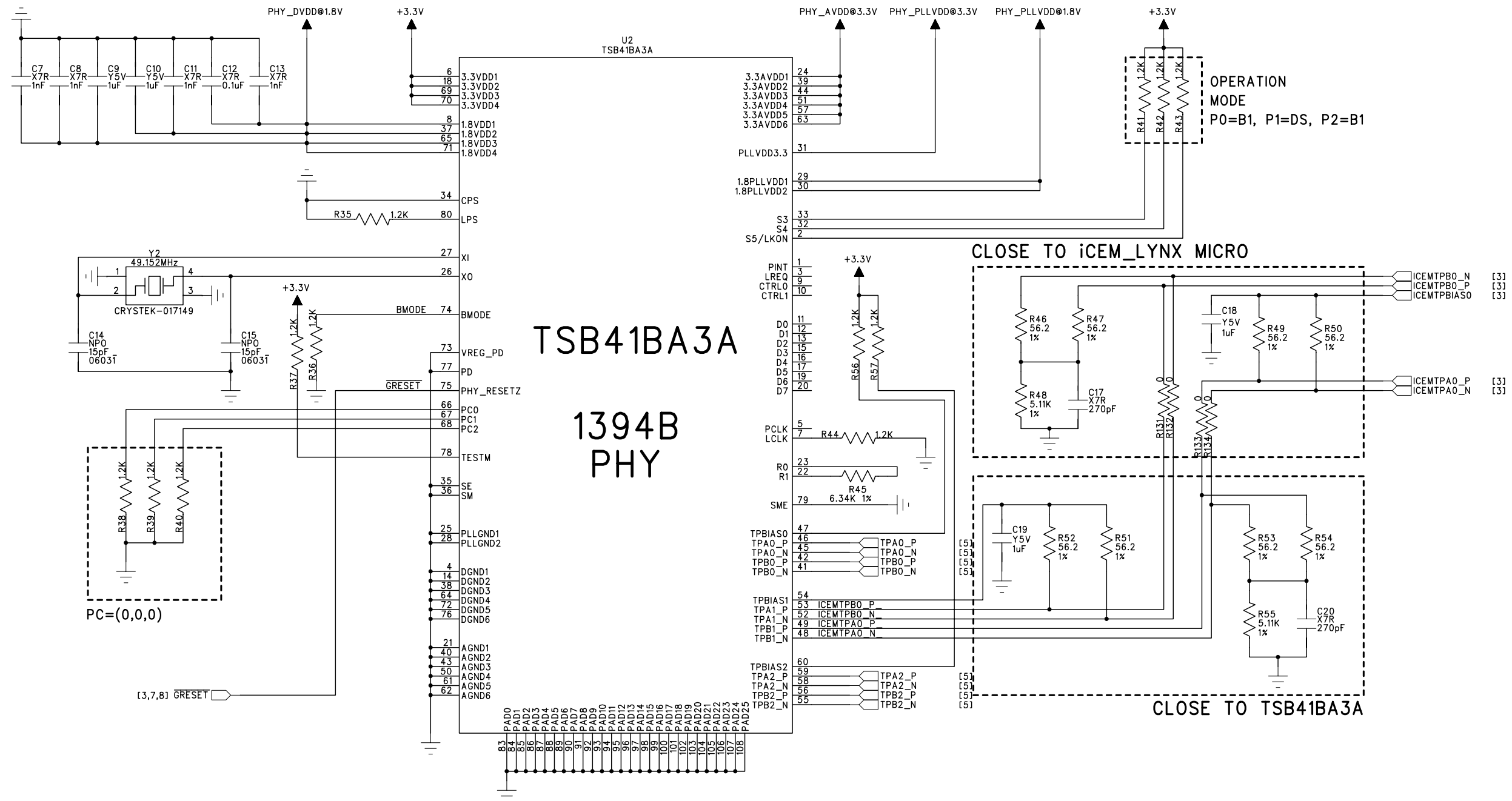
NOTE 1: ALL DISCRETE PARTS ARE 0603 SIZE UNLESS OTHERWISE NOTICED

NOTE 2: ALL RESISTORS WITH TOLERANCE 5% UNLESS OTHERWISE NOTICED

		TITLE: AVR 1394 FTMS	
DRAWN: WD KIM	2006.05.10	SUB-TITLE: COVER PAGE	
CHECKED:			
		SHEET: 1 OF 10	REV: 0.4



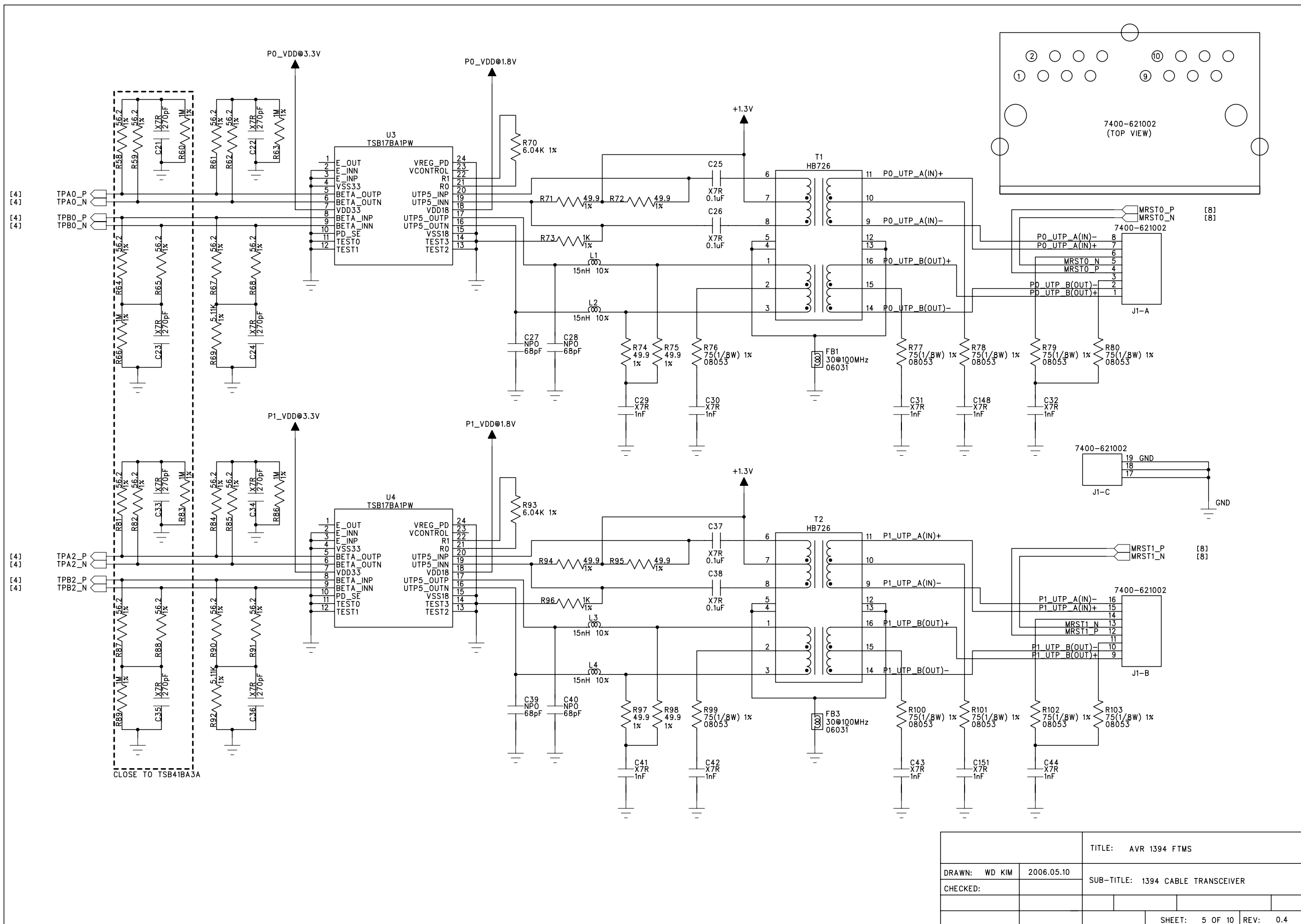


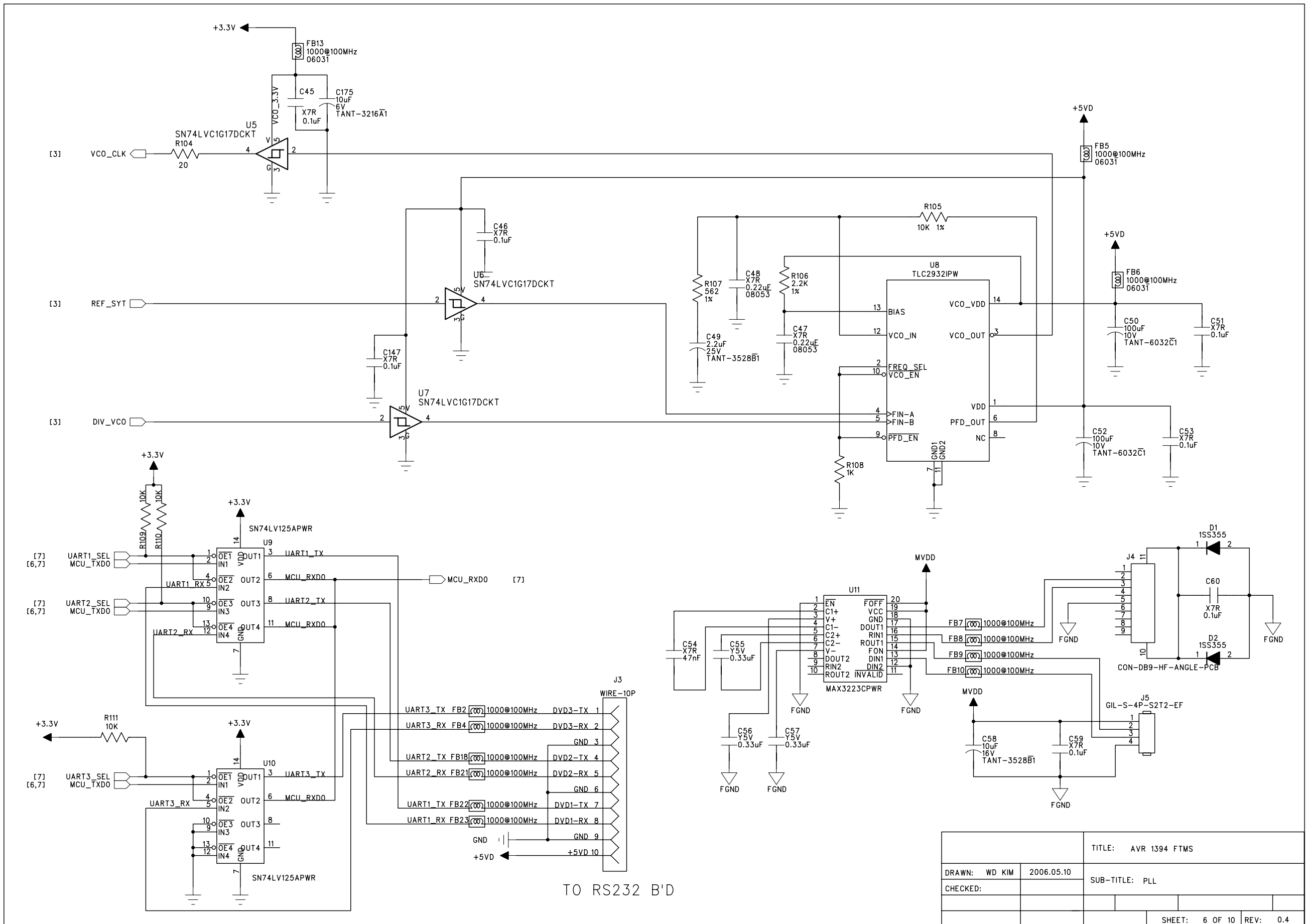


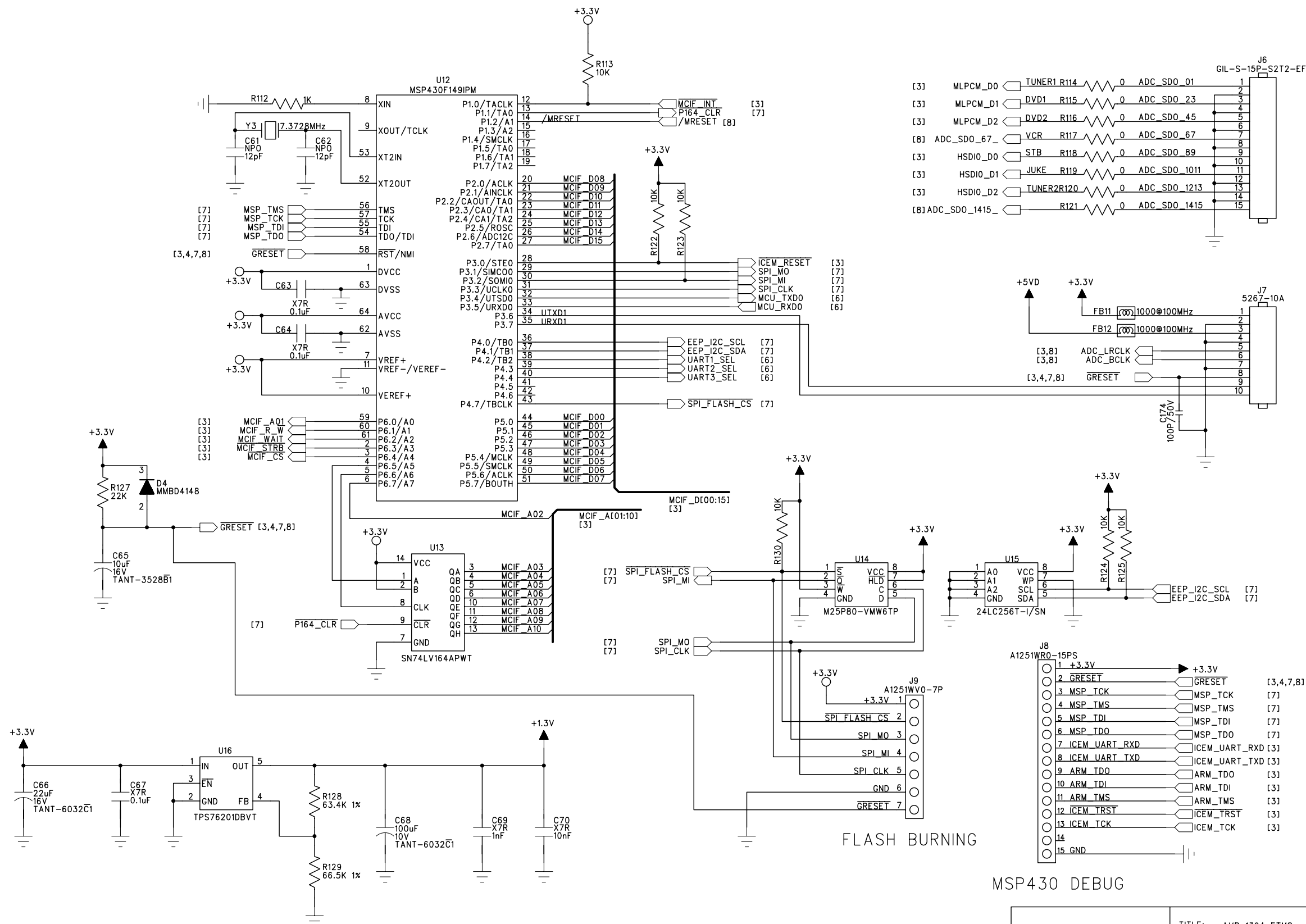
	MODE No.	S5_LINK_ON	S4	S3	S2_PC0	S1_PC1	S0_PC2	PORT-2	PORT-1	PORT-0	PWR CLASS
AVR	24	1	1	1	PC0	0	0	B1	DS	B1	PC0,0,0

<PHY SPEED/MODE SELECTION>

DRAWN: WD KIM		2006.05.10		TITLE: AVR 1394 FTMS	
CHECKED:				SUB-TITLE: 1394 PHY (TSB41BA3A)	
				SHEET: 4 OF 10	
				REV: 0.4	





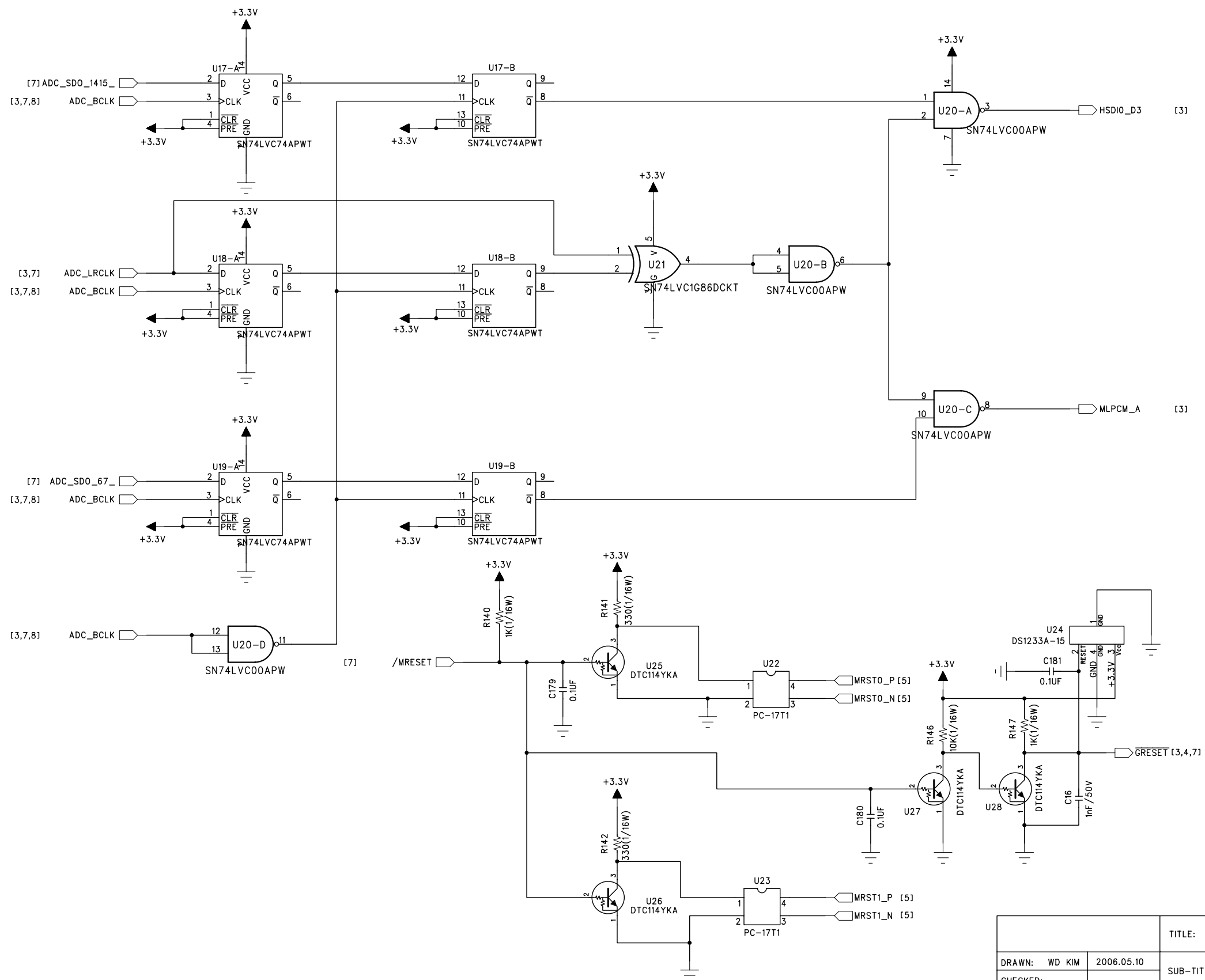


FROM PROCESSOR

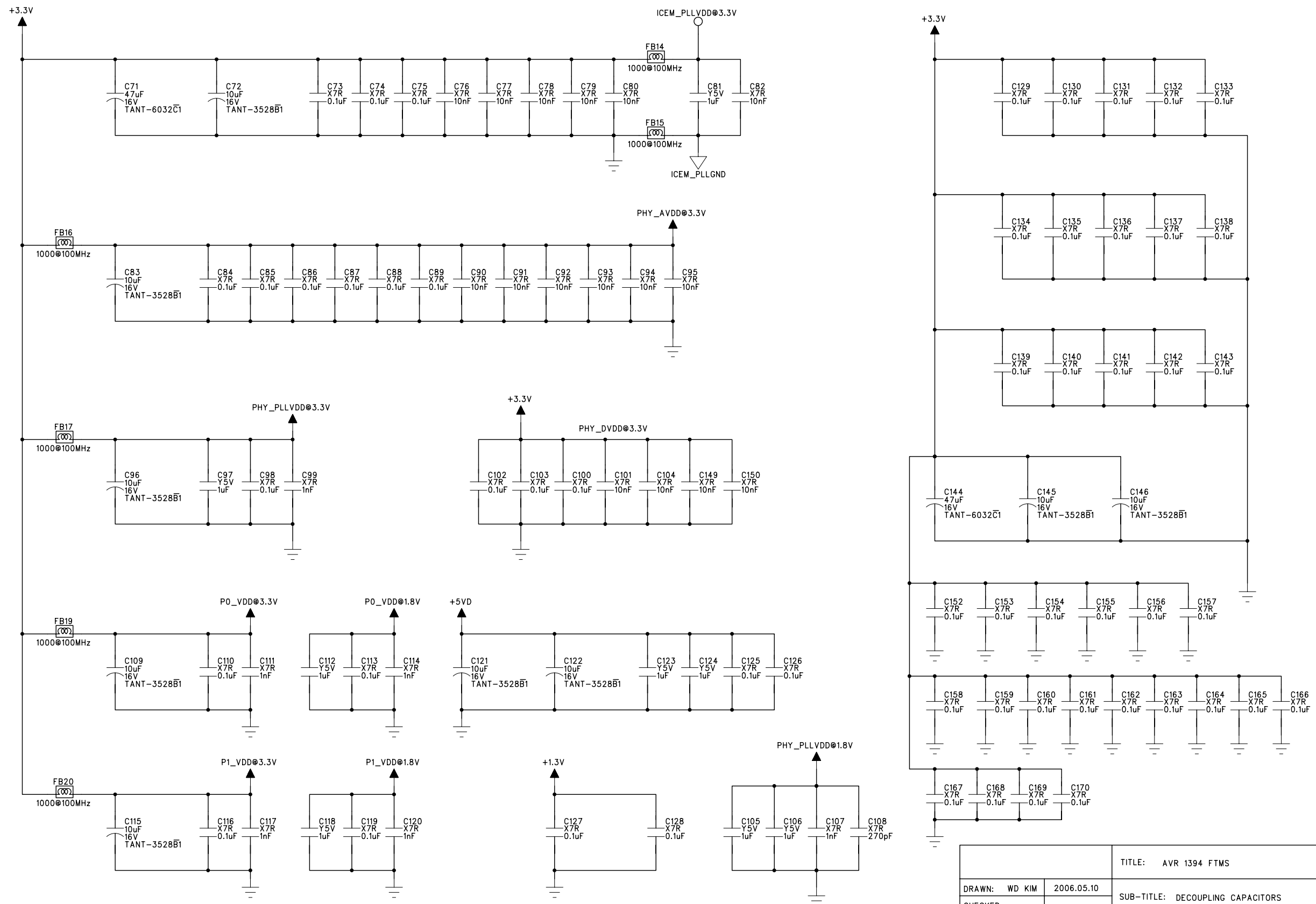
TO PROCESSOR

MSP430 DEBUG

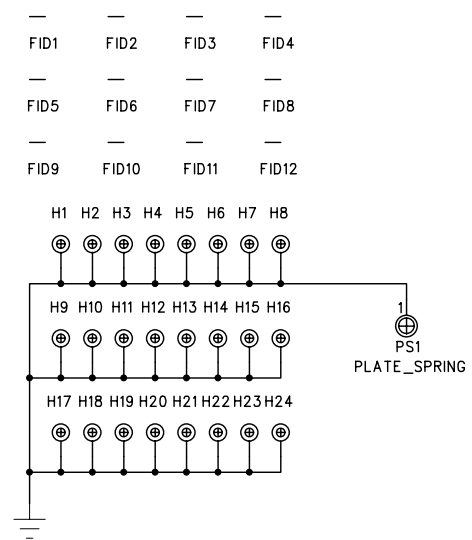
		TITLE: AVR 1394 FTMS	
DRAWN: WD KIM	2006.05.10	SUB-TITLE: MCU-MSP430F149	
CHECKED:			
		SHEET: 7 OF 10 REV: 0.4	



		TITLE: AVR 1394 FTMS	
DRAWN: WD KIM	2006.05.10	SUB-TITLE: AUDIO FORMATTER	
CHECKED:			
		SHEET: 8 OF 10	REV: 0.4



		TITLE: AVR 1394 FTMS	
DRAWN: WD KIM	2006.05.10	SUB-TITLE: DECOUPLING CAPACITORS	
CHECKED:			
		SHEET: 9 OF 10	REV: 0.4



		TITLE: AVR 1394 FTMS			
DRAWN : WD KIM	2006.05.10	SUB-TITLE: ETC			
CHECKED:					
				SHEET: 10 OF 10	REV: 0.4