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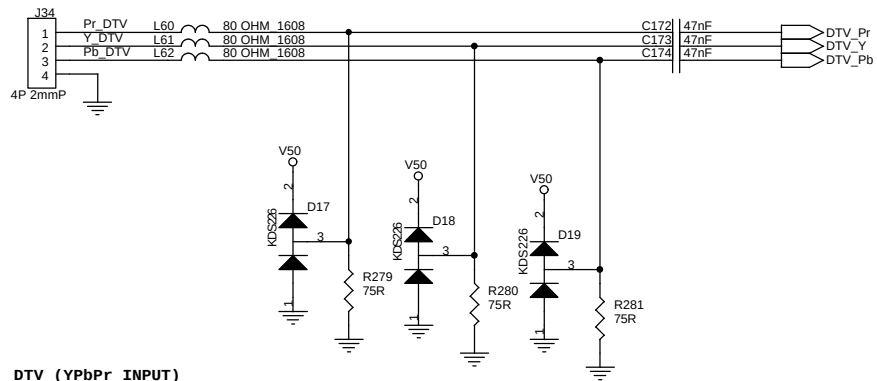
SCHEMATIC	SHEET
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Approval	Organization	Signature	Date
CM KIM	TECHSANINC, CO.LTD		

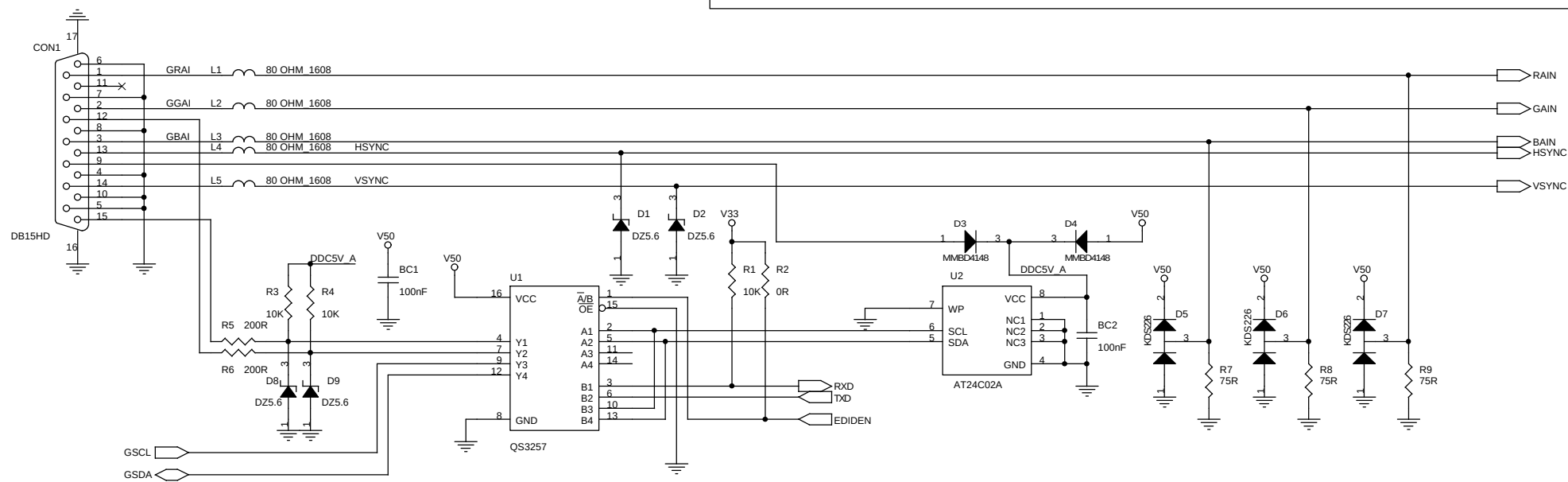
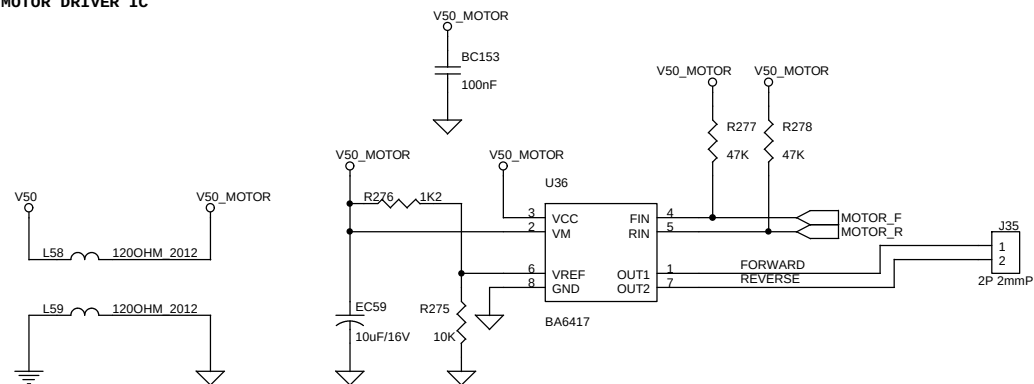
REVISION HISTORY

Date	Author	Ver	Comments
MAR 26/2003	CM KIM	0.0	Preliminary design.
APRIL 28/2003	CM KIM	0.1	Secondary design.
JUNE 04/2003	CM KIM	0.2	Third Design.
JUNE 30/2003	CM KIM	0.3	Forth Design.
JULY 10/2003	CM KIM	0.4	Fifth Design.

Pixelworks, Inc.		CONFIDENTIAL		7700 SW Mohawk Street Tualatin, OR 97062 (503) 612-6700	
Title		00. Contents, Revision History			
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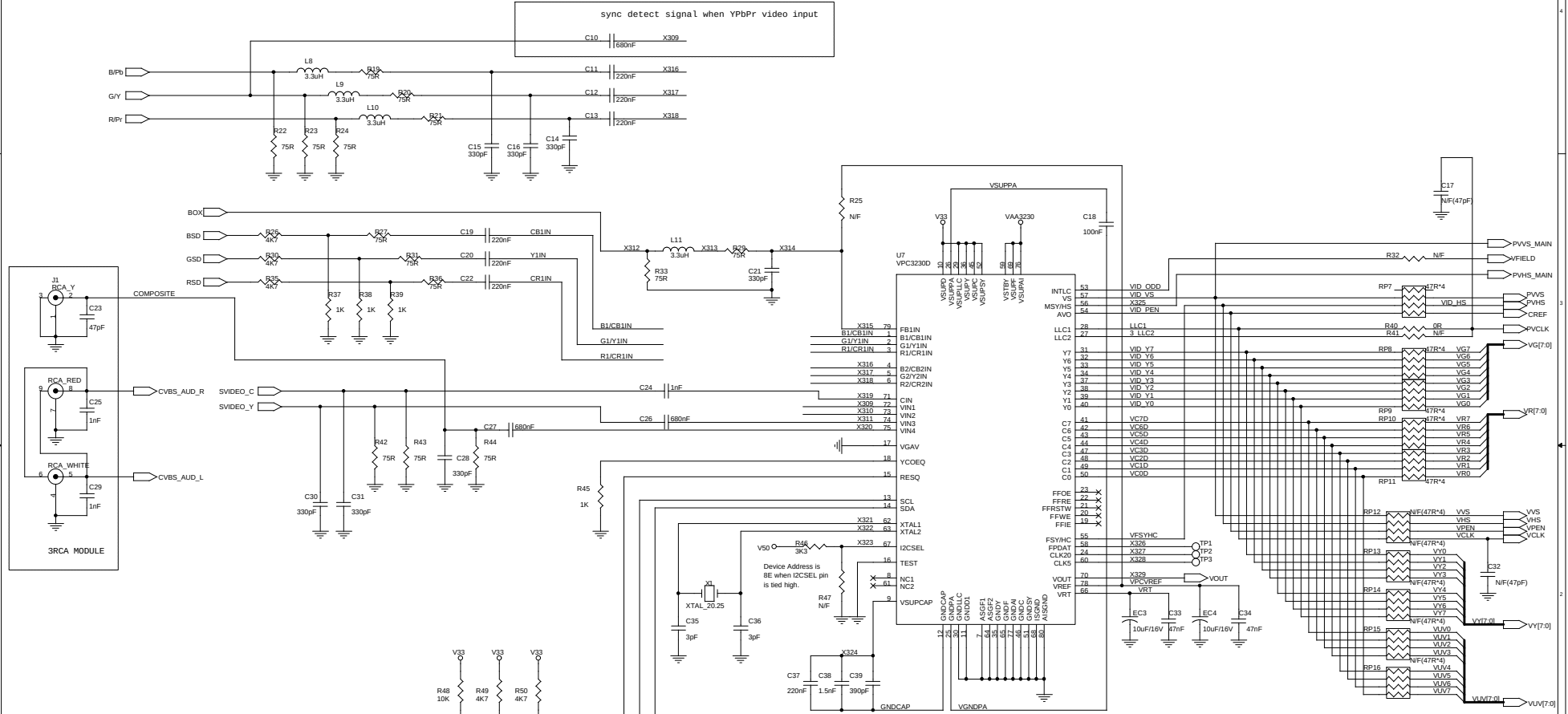


MOTOR DRIVER IC

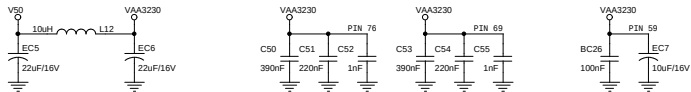
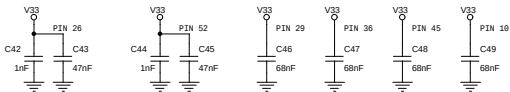


Pixelworks, Inc. CONFIDENTIAL		7700 SW Mohawk Street Tualatin, OR 97062 (503) 612-6700	
Title		01. ANALOG/DTV INPUT	
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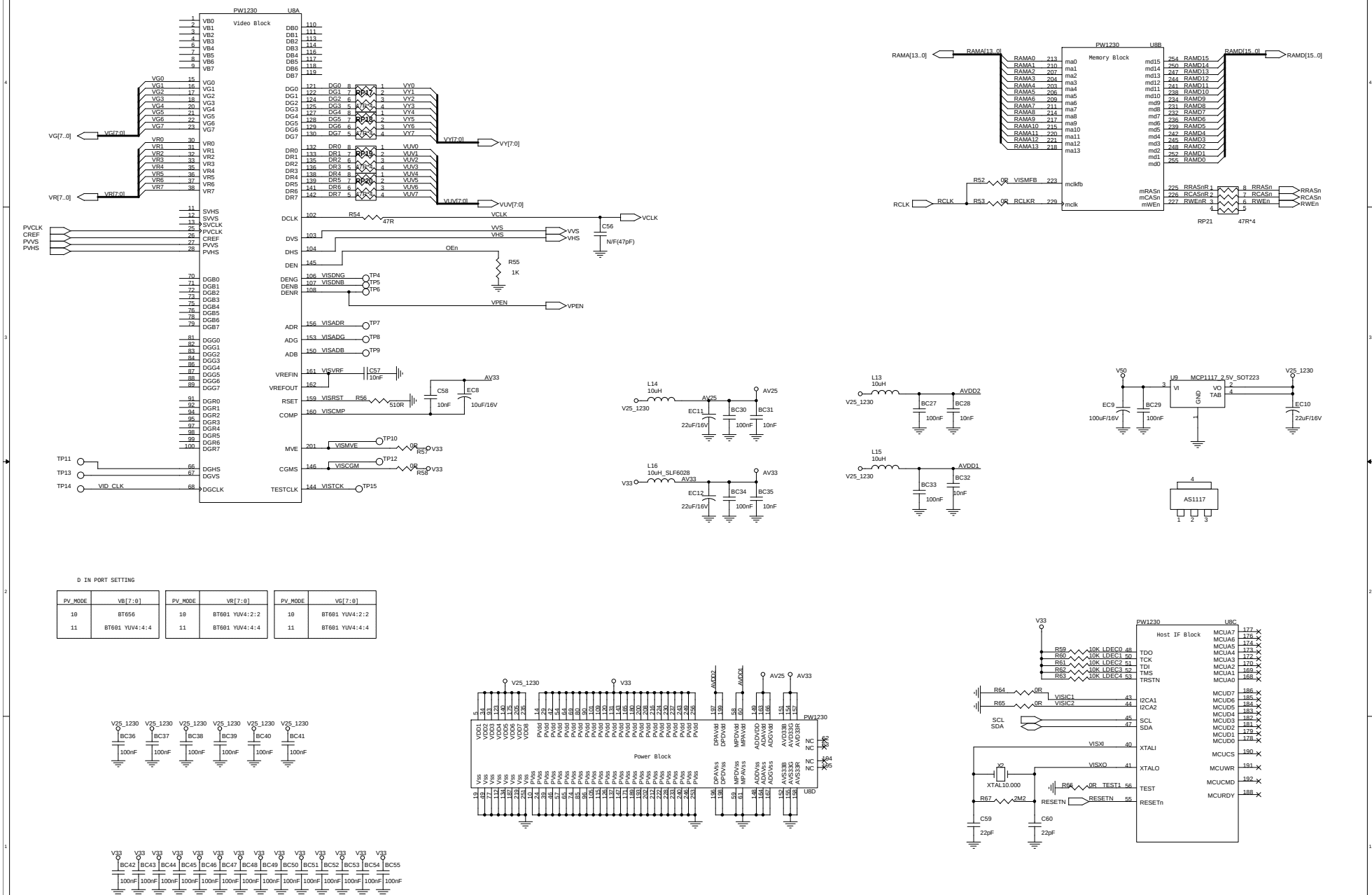
Micronas VPC3230 Video Decoder



VPC3230 -> Pw1230 -> Pw166B	VPC3230 -> Pw166B
FIX COMPONENTS	FIX COMPONENTS
RP7-11	RP12-16, R32
NOT FIX COMPONENTS	NOT FIX COMPONENTS
RP12-16, R32	RP7-11



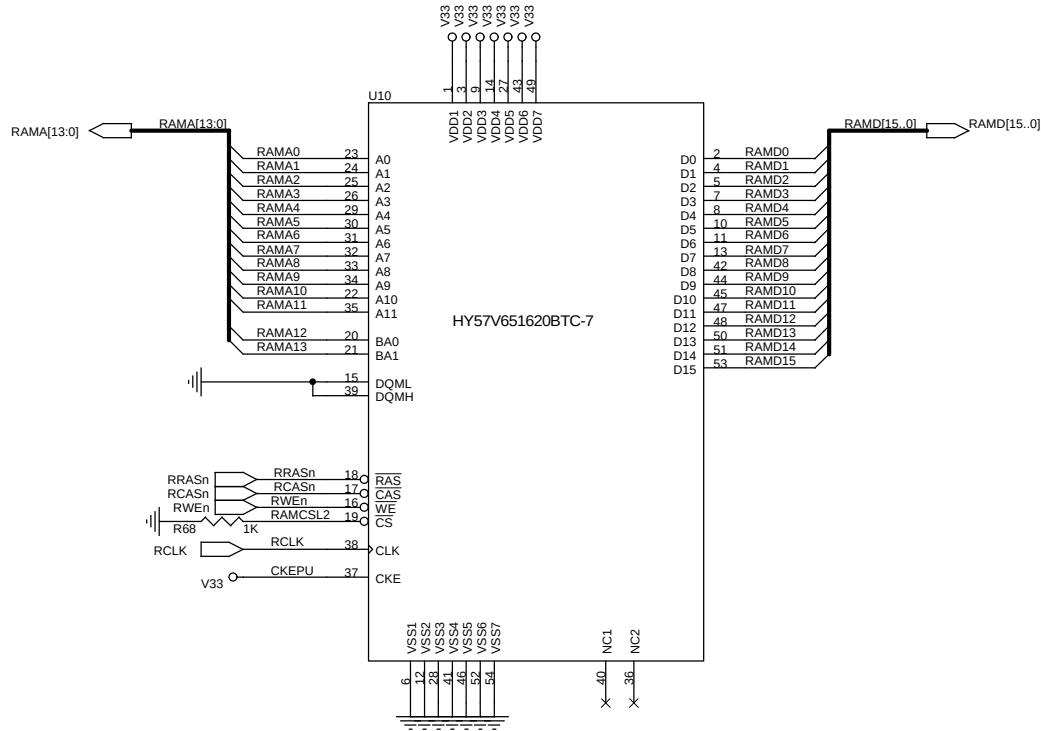
PW1230 Deinterlacer

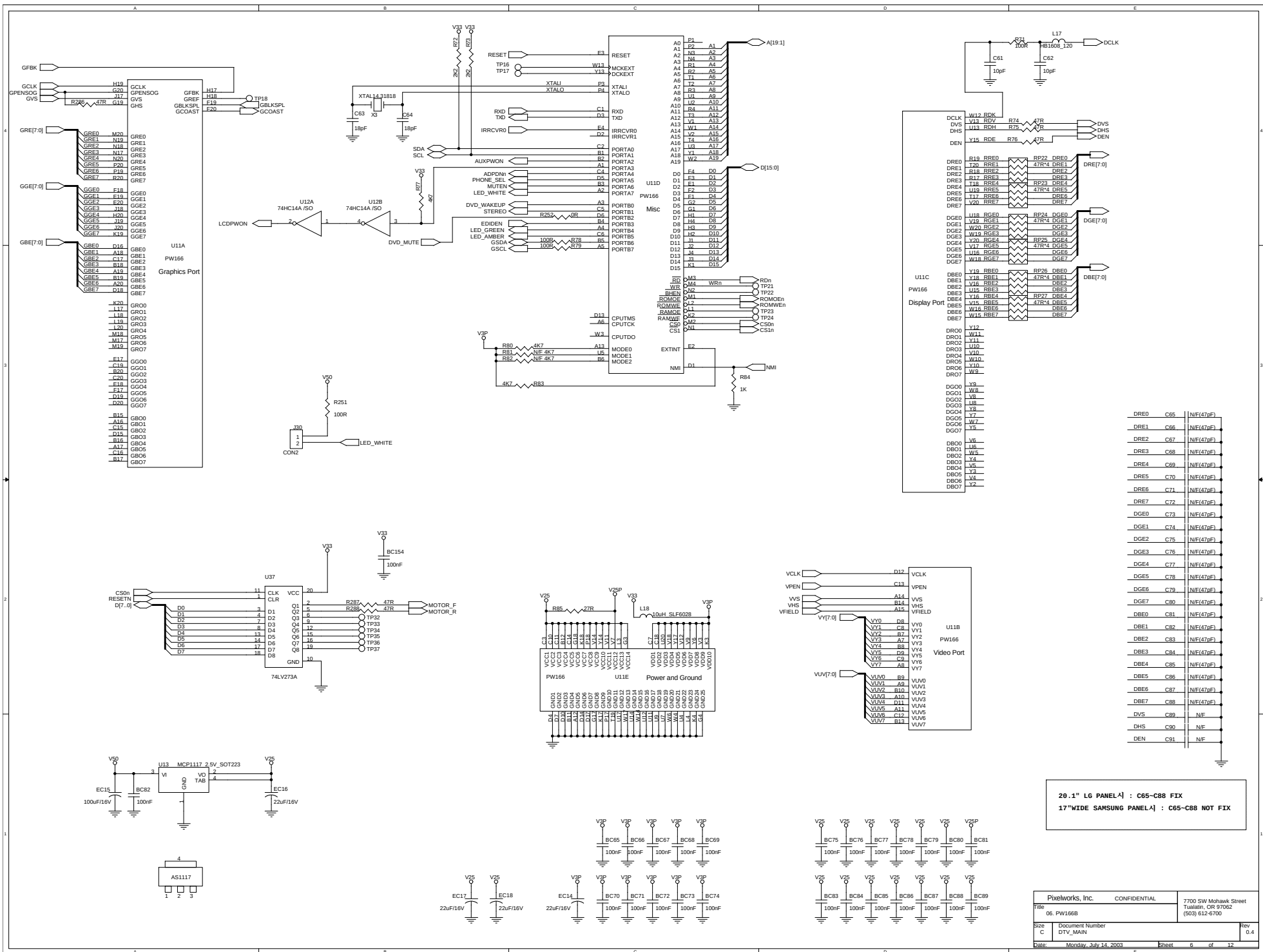


PV_MODE	VB[7:0]	PV_MODE	VR[7:0]	PV_MODE	VG[7:0]
10	BT656	10	BT601 YUV4:2:2	10	BT601 YUV4:2:2
11	BT601 YUV4:4:4	11	BT601 YUV4:4:4	11	BT601 YUV4:4:4

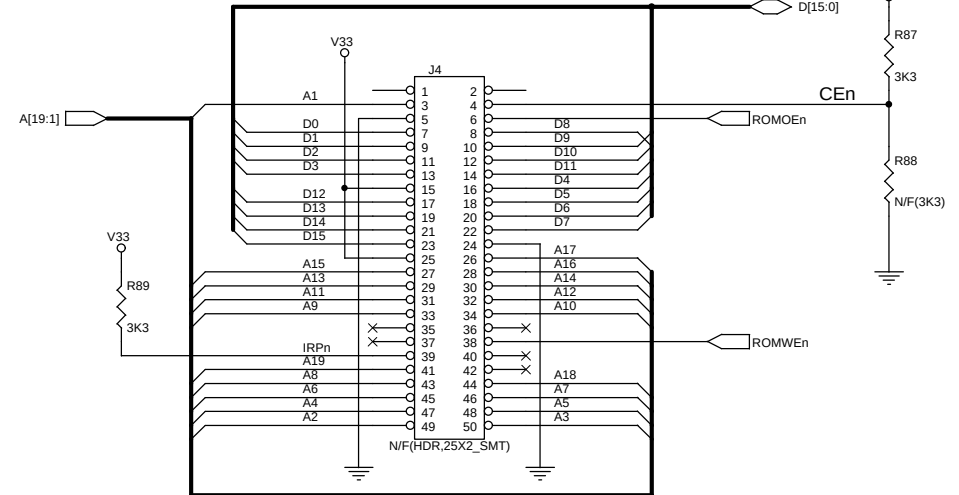
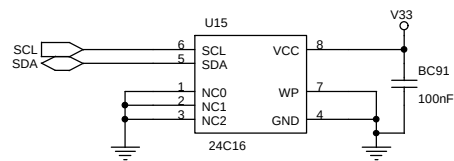
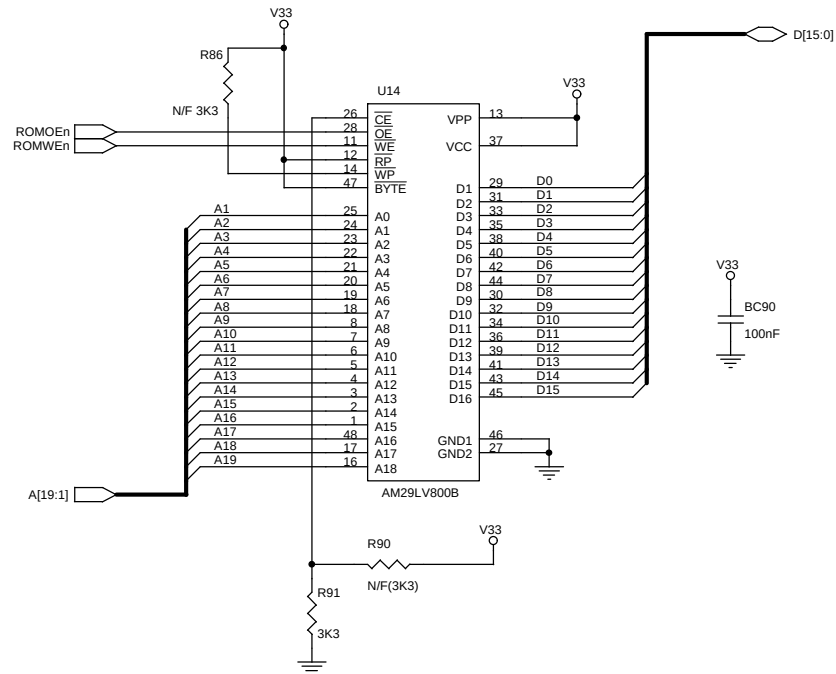
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Title 04_PW1230_DEINTERLACER					
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2Mx16 SDRAM





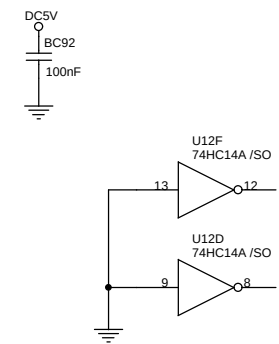
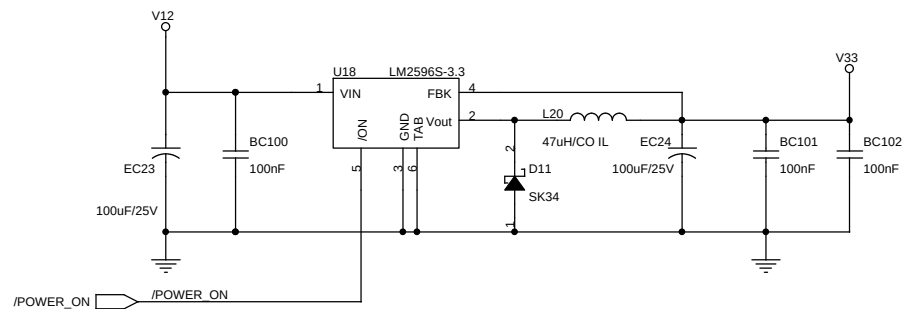
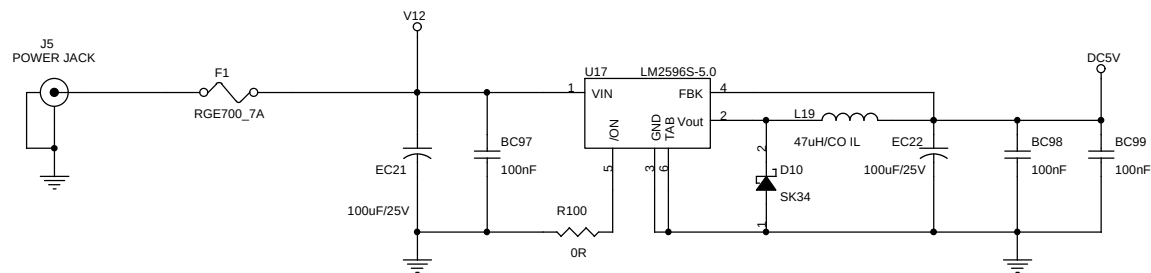
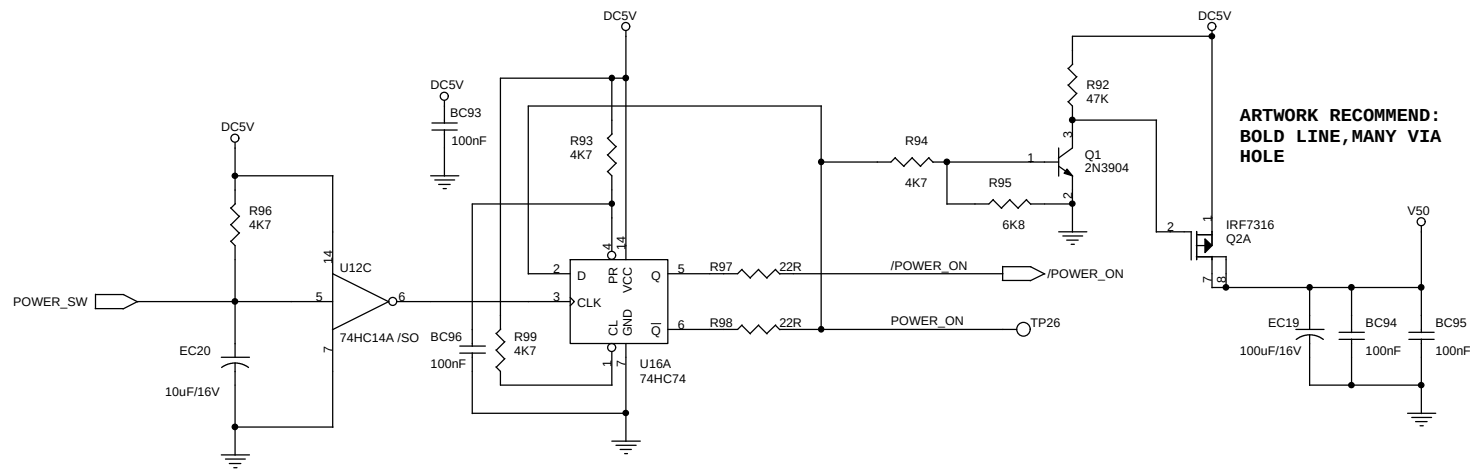
FLASH_ROM



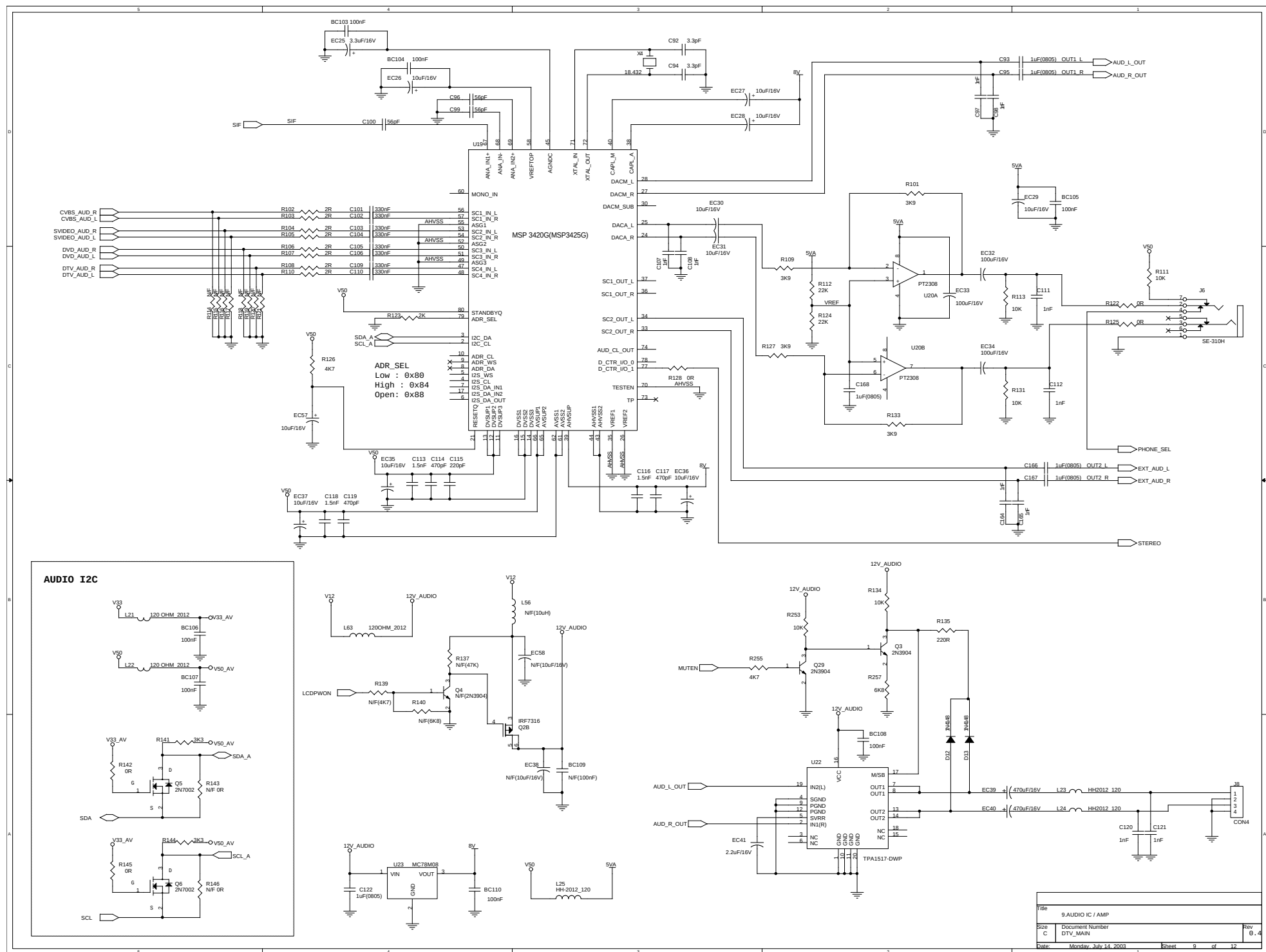
Flash R91(ON), R87(ON)
ICE R90(ON), R88(ON)

DEBUGGING PROM JET

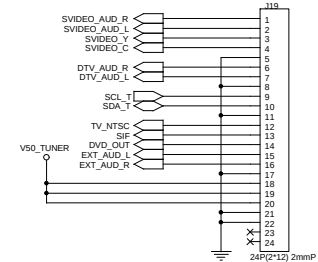
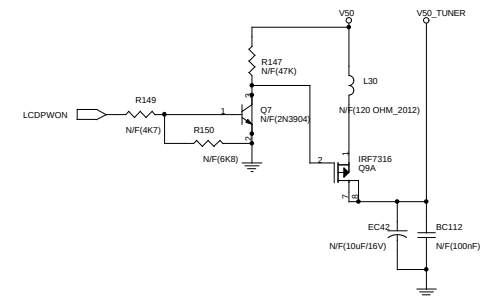
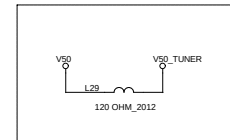
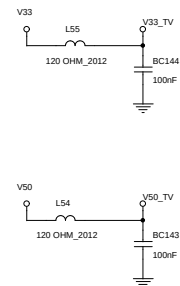
Title		
07.FLASH		
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Title 08. Power Input			
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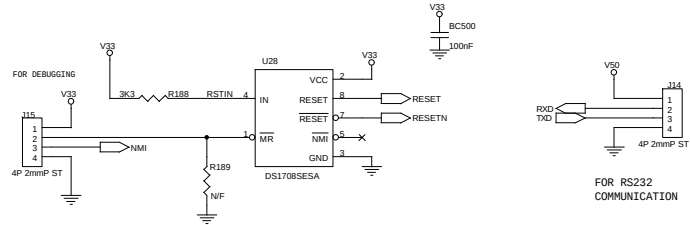
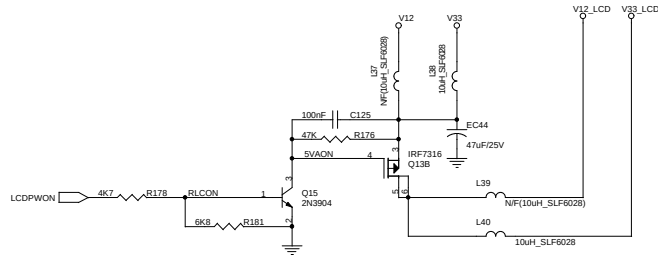
Title	9.AUDIO IC / AMP		
Size	Document Number	Rev 0.4	
C	DTV MAIN		
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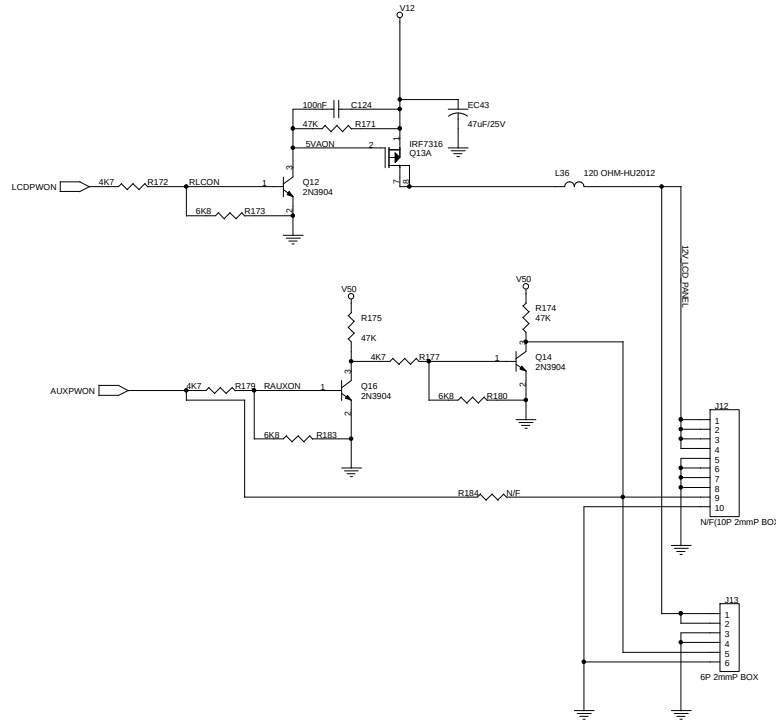
I/O BD INTERFACE

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Title 10. FRONT/CHIP					
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**LG L37(ON), L39(ON)
SAMSUNG L38(ON), L40(ON)**

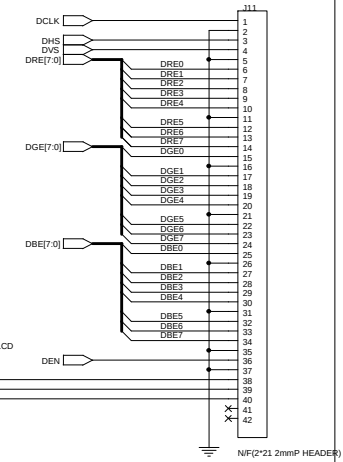


FOR RS232
COMMUNICATION



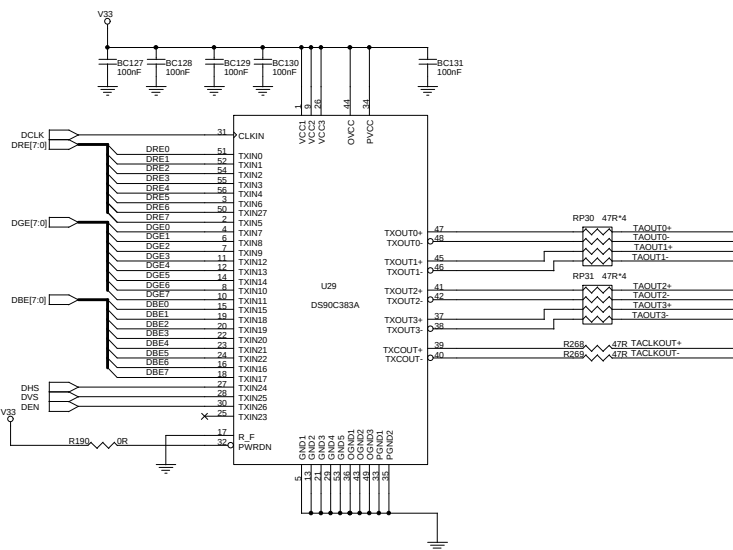
17"wide용시 N/F

LG 20.1" PANEL



40P TO 41P LG 20.1" PANEL
INTERFACE R/A

20.1"용시 N/F

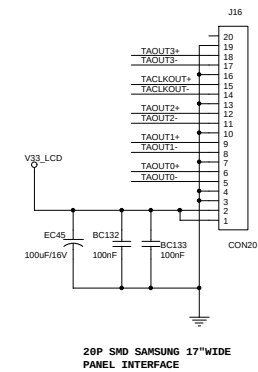


PW166B : EVEN PIXEL IS FIRST

16P TO 15P		6P TO 8P	
MAIN	28.1" INVERTER	MAIN	17"WIDE INVERTER
1	1	1	1
2	2	2	2
3	3	3	3
4	4	4	4
5	5	5	5
6	6	6	6
7	7	7	7
8	8	8	8
9	9	9	9
10	10	10	10
11	11	11	11
12	12	12	12
13	13	13	13
14	14	14	14

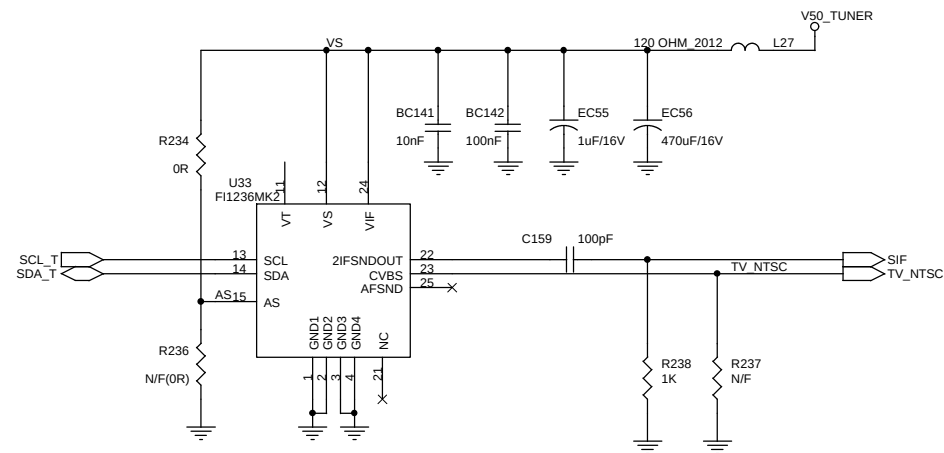
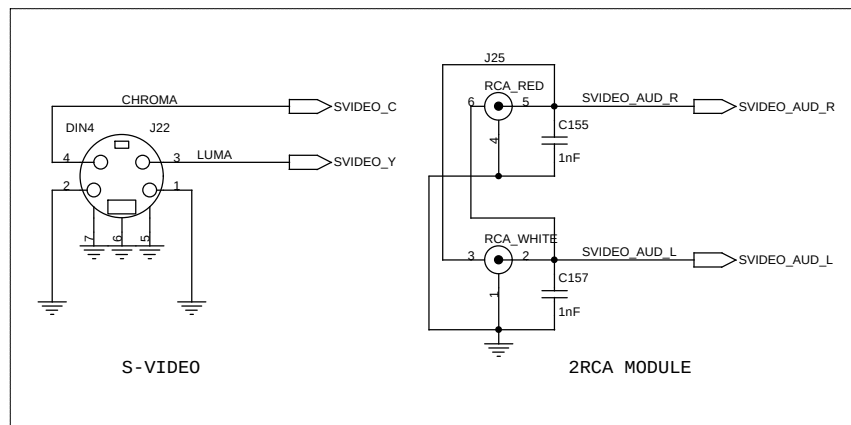
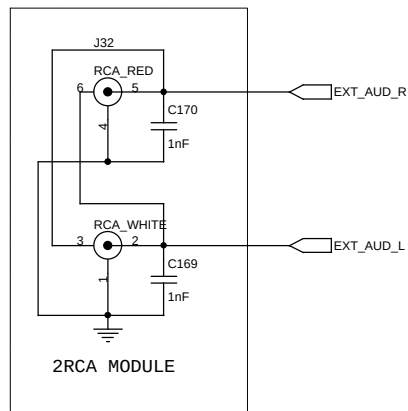
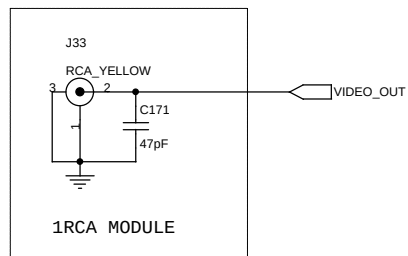
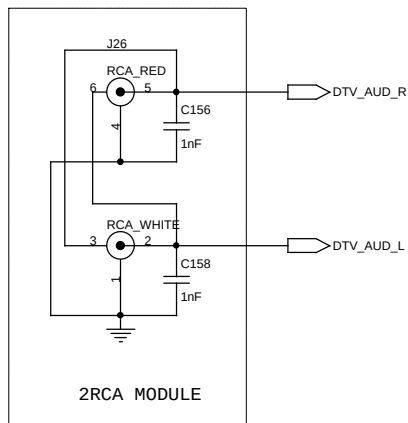
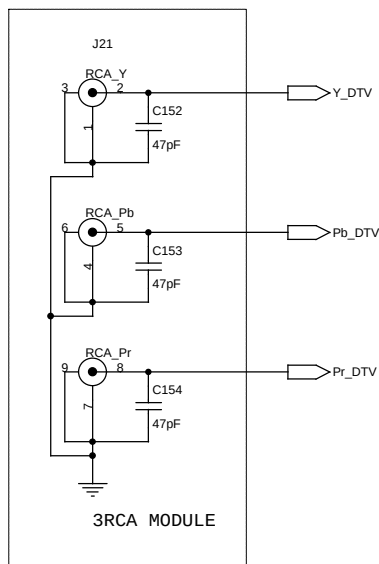
INVERTER CABLE PIN ASSIGN

20.1"용시 N/F

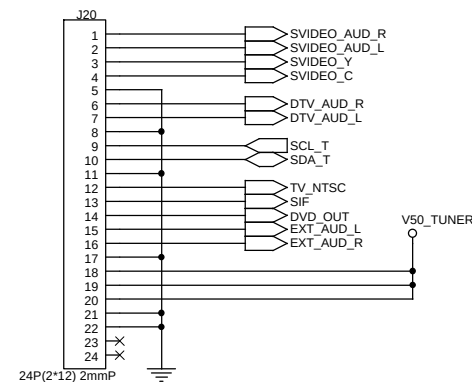
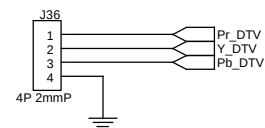


20P SMD SAMSUNG 17"WIDE
PANEL INTERFACE

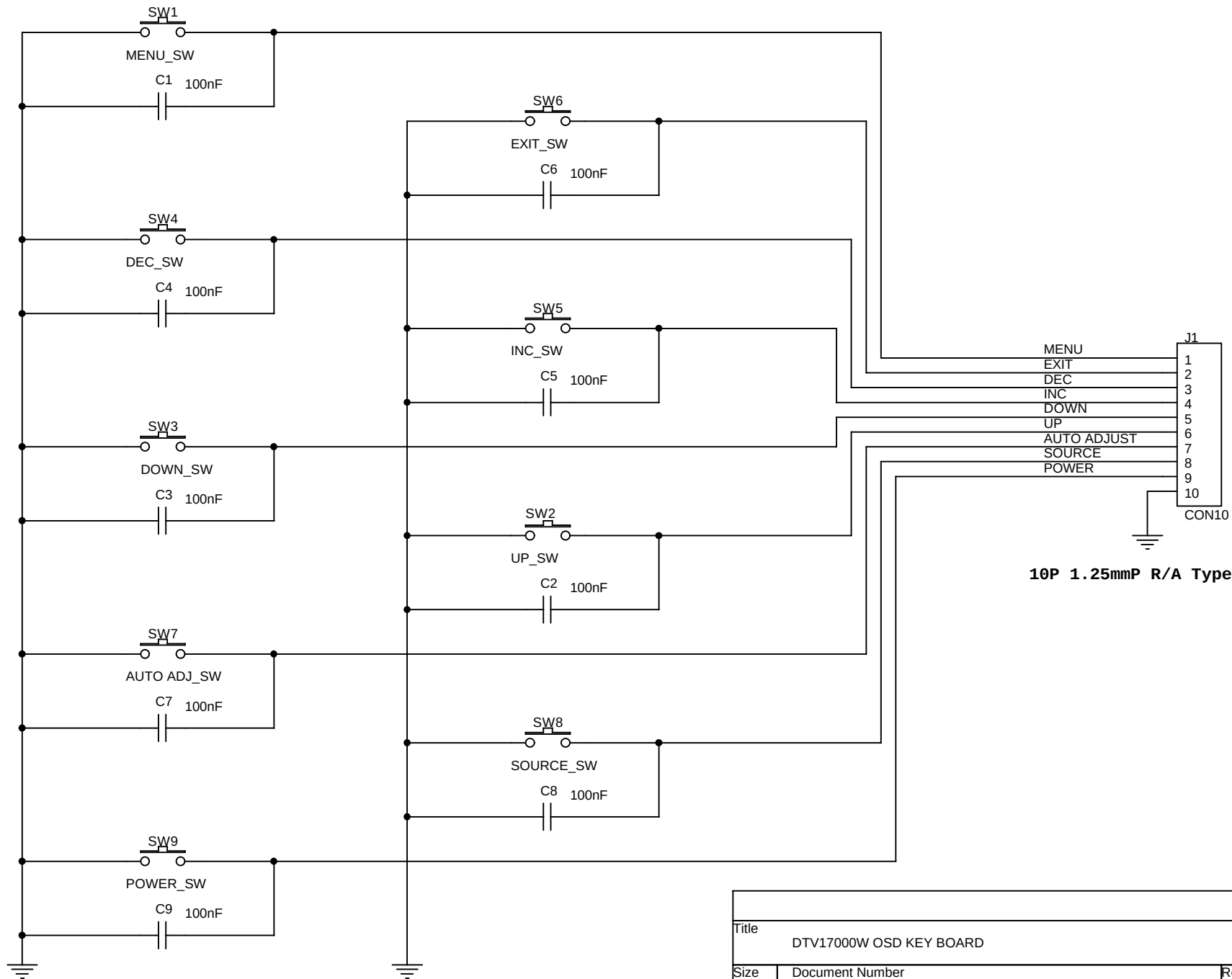
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Title	11. PANEL INTERFACE	
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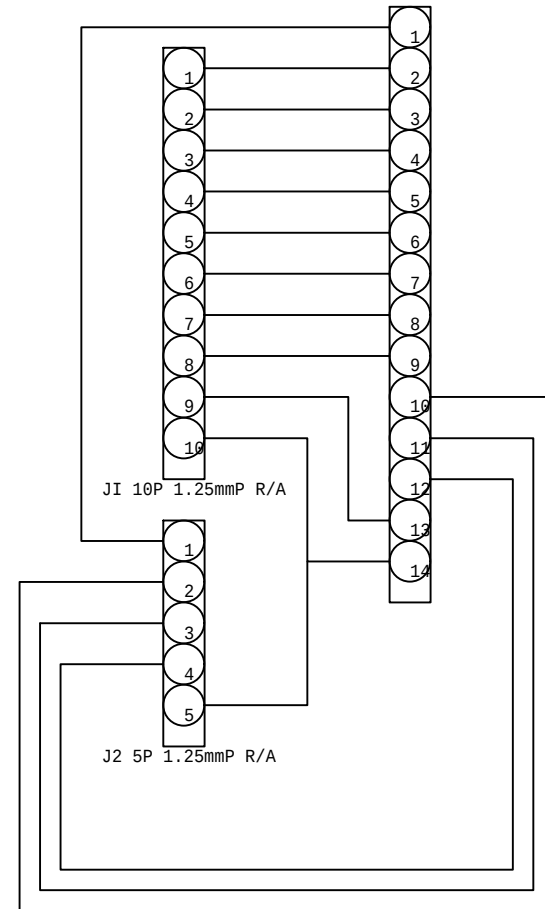
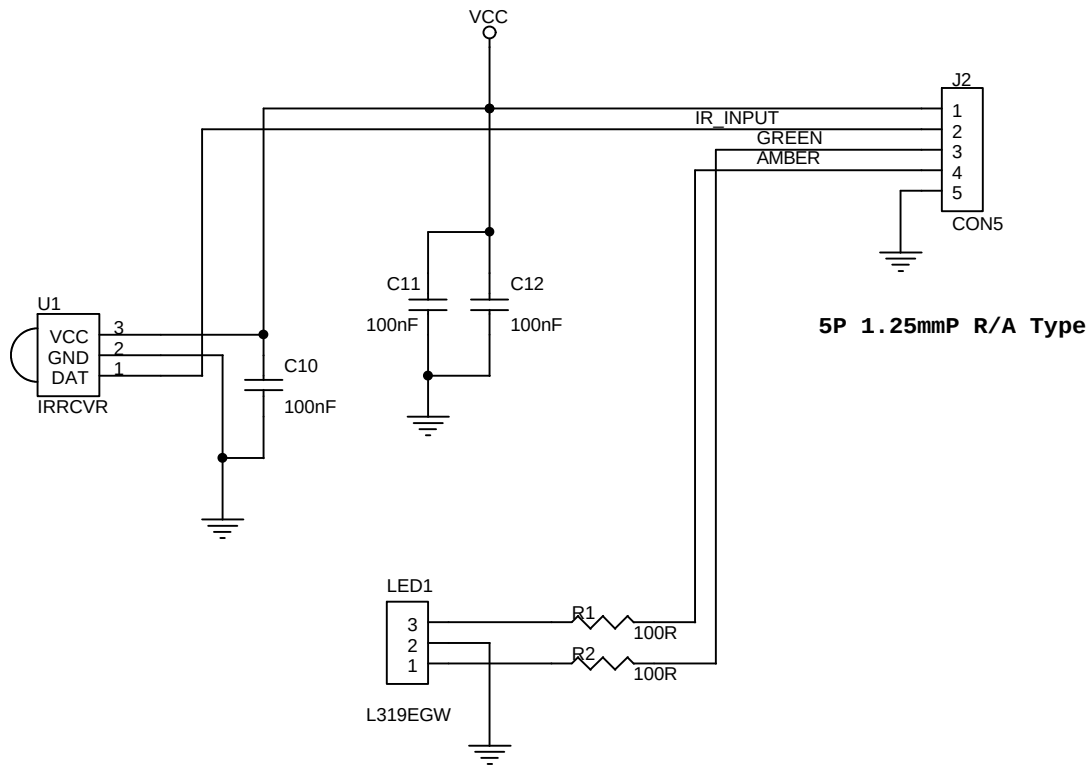
AS HIGH => I2C
:0xC6 (JAPAN/USA)



Title			
1.TUNER_I/O BD			
Size	Document Number	Rev	
B	TUNER bd	0.4	
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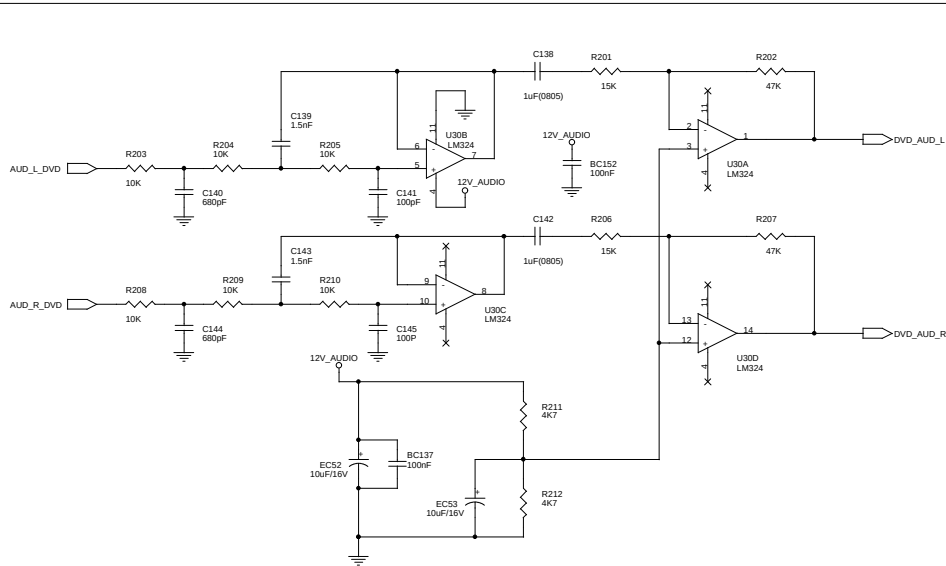
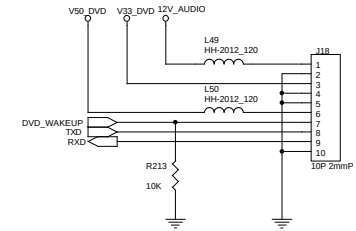
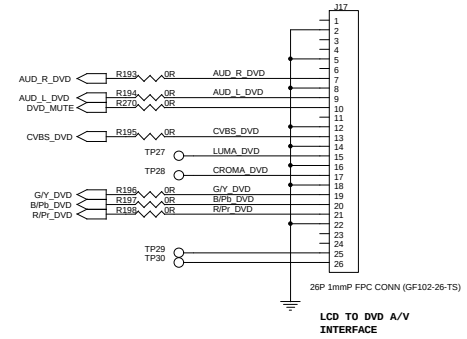
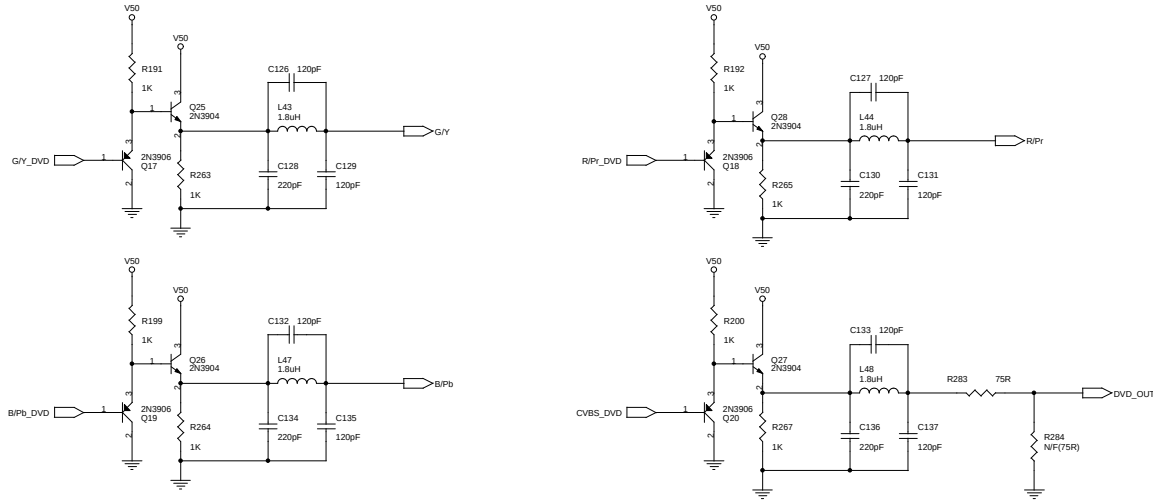
Title		
DTV17000W OSD KEY BOARD		
Size	Document Number	Rev
A	BRAVO17"WIDE_LCD OSD B/D	0.0
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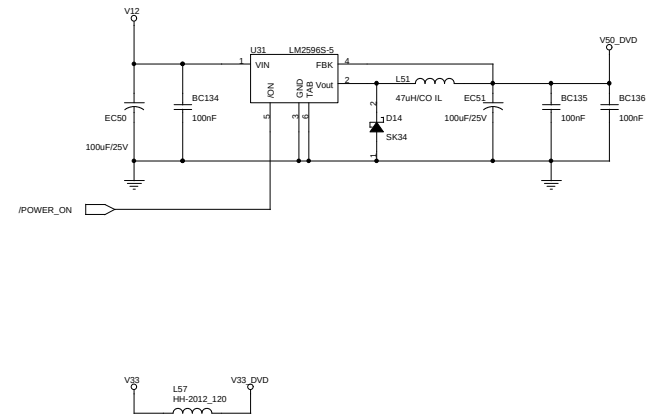
REMARKS : OSD_B/D TO MAIN_B/D INERFACE

Title		
DTV17000W OSD IR BOARD		
Size	Document Number	Rev
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DVD VIDEO FILTER



DVD AUDIO FILTER



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12.DVD INTERFACE		
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