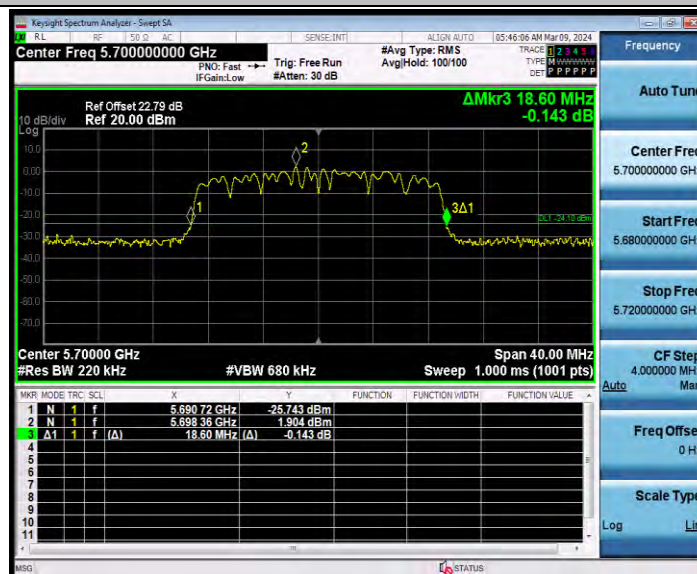




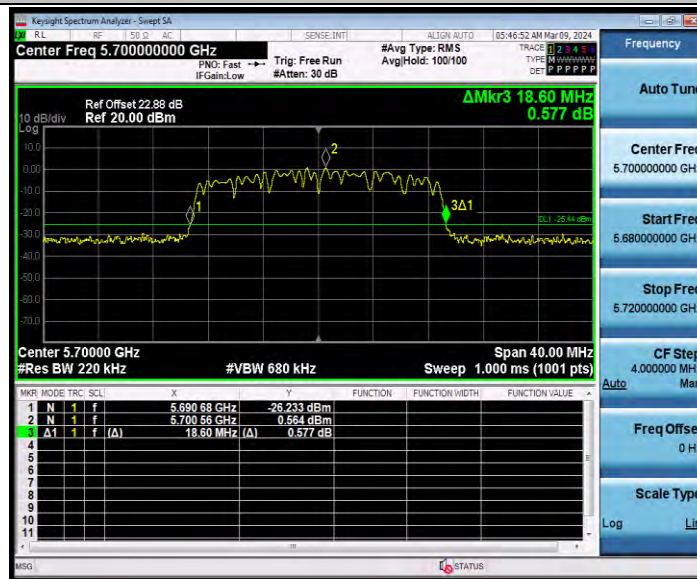
11N20MIMO\_Ant2\_5580



11N20MIMO\_Ant1\_5700



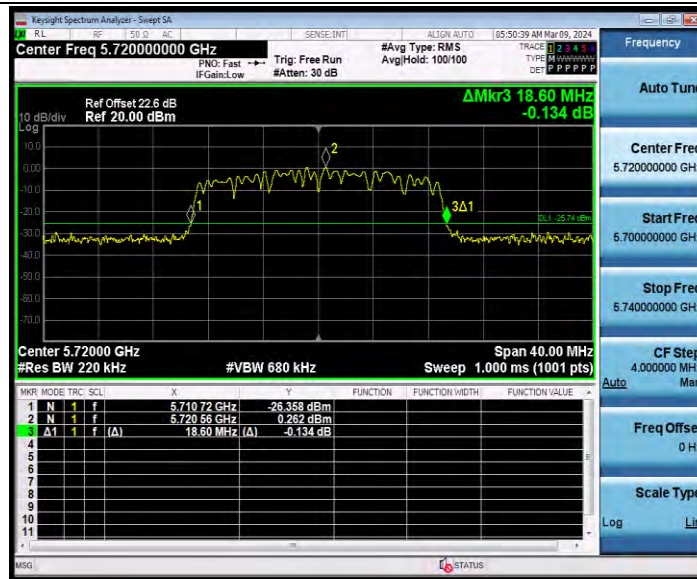
# 11N20MIMO\_Ant2\_5700



# 11N20MIMO\_Ant1\_5720



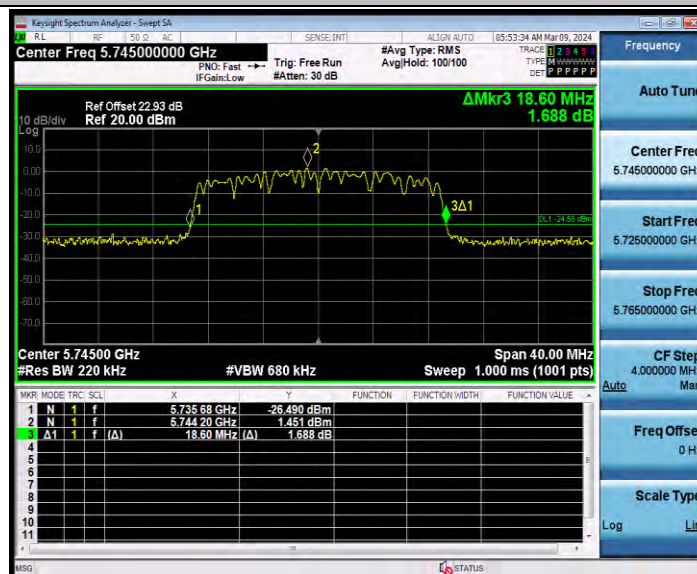
# 11N20MIMO\_Ant2\_5720



11N20MIMO\_Ant1\_5745

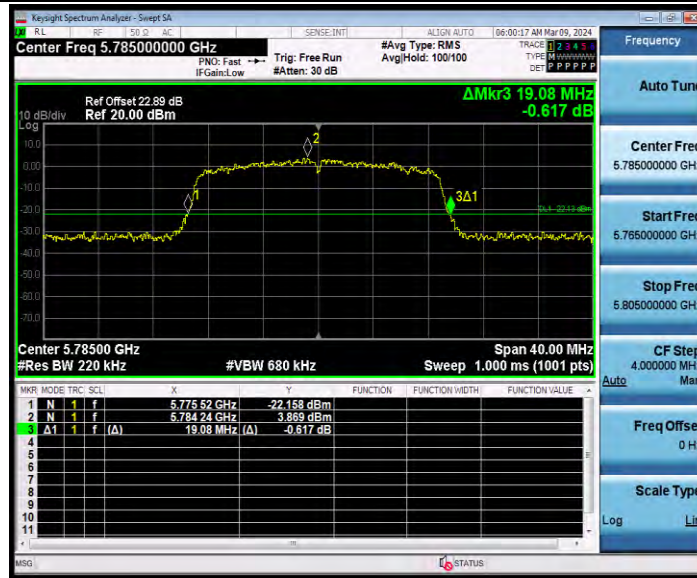


11N20MIMO\_Ant2\_5745

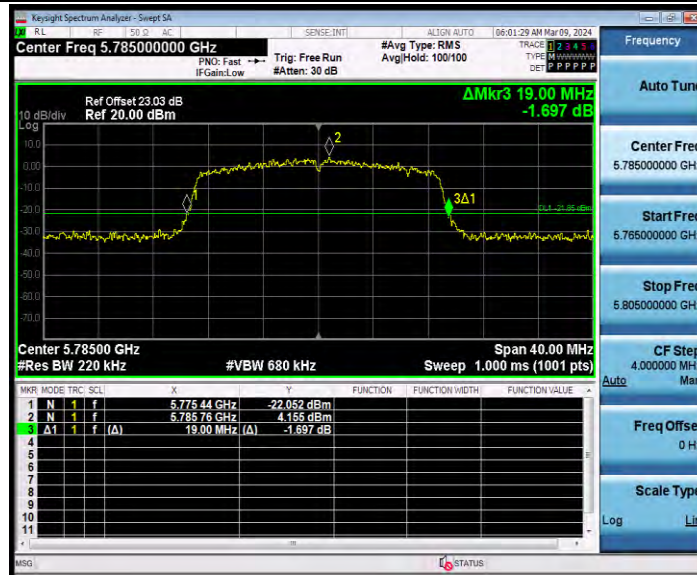




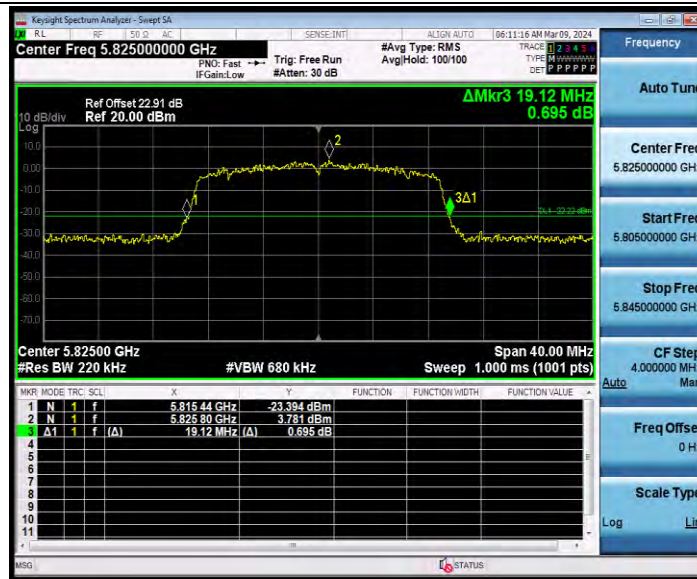
# 11N20MIMO\_Ant1\_5785



# 11N20MIMO\_Ant2\_5785



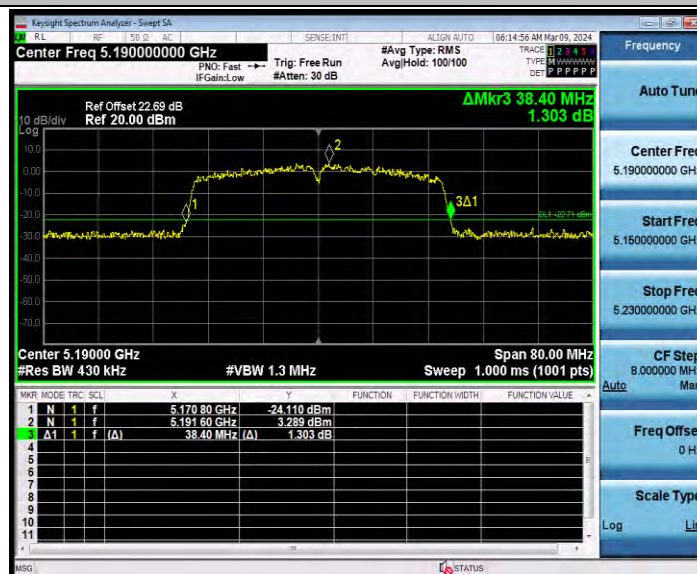
# 11N20MIMO\_Ant1\_5825



11N20MIMO\_Ant2\_5825



11N40MIMO\_Ant1\_5190



# 11N40MIMO\_Ant2\_5190

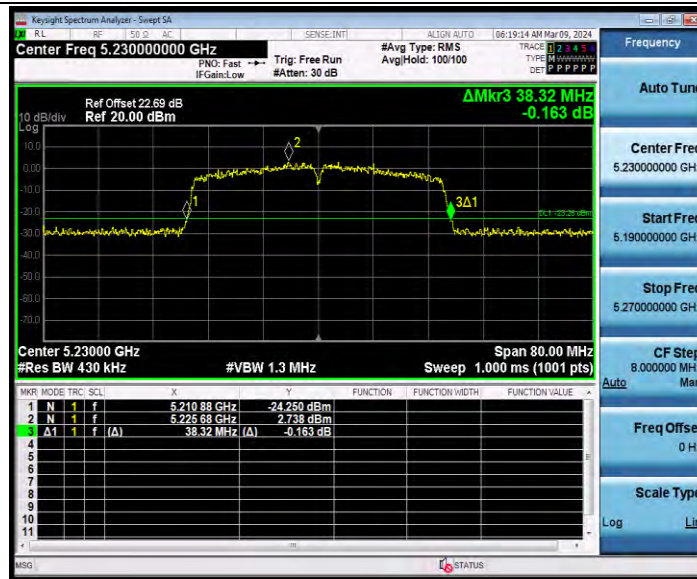


# 11N40MIMO\_Ant1\_5230



# 11N40MIMO\_Ant2\_5230





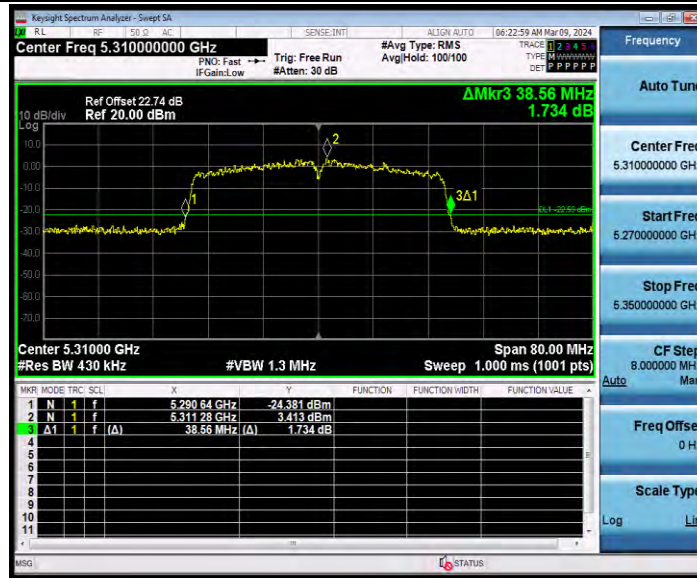
11N40MIMO\_Ant1\_5270



11N40MIMO\_Ant2\_5270



# 11N40MIMO\_Ant1\_5310



# 11N40MIMO\_Ant2\_5310



# 11N40MIMO\_Ant1\_5510

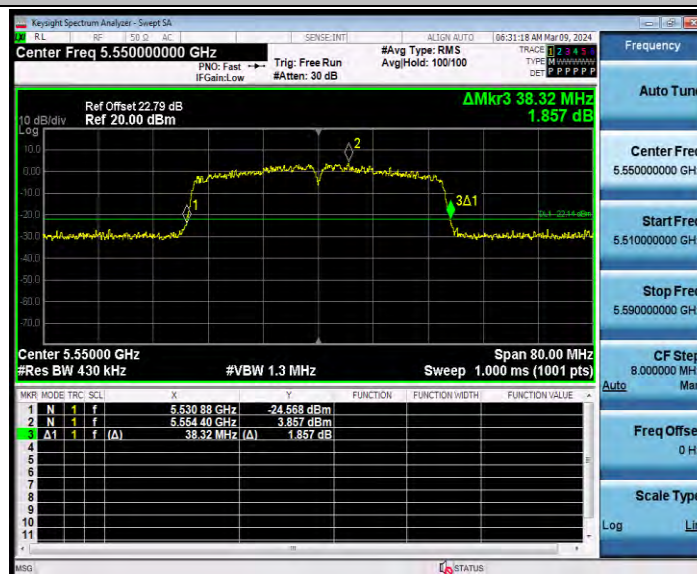




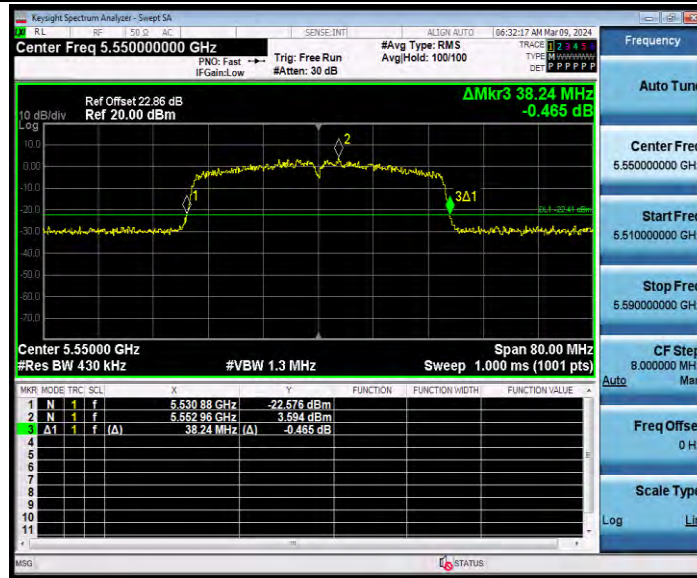
11N40MIMO\_Ant2\_5510



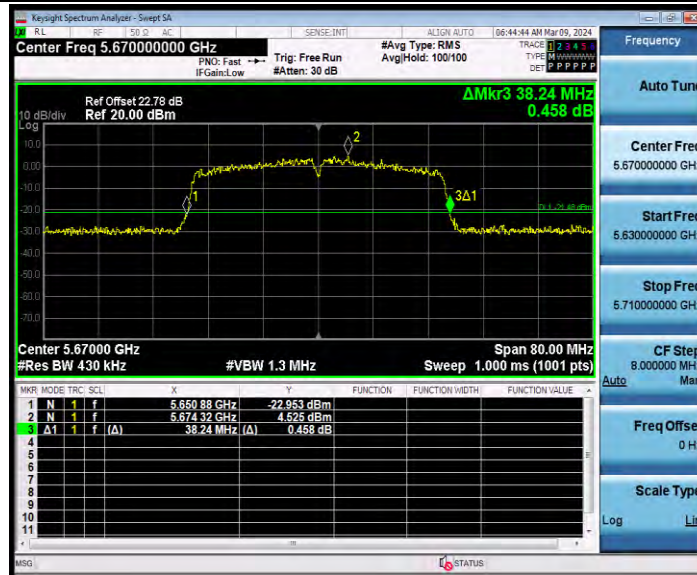
11N40MIMO\_Ant1\_5550



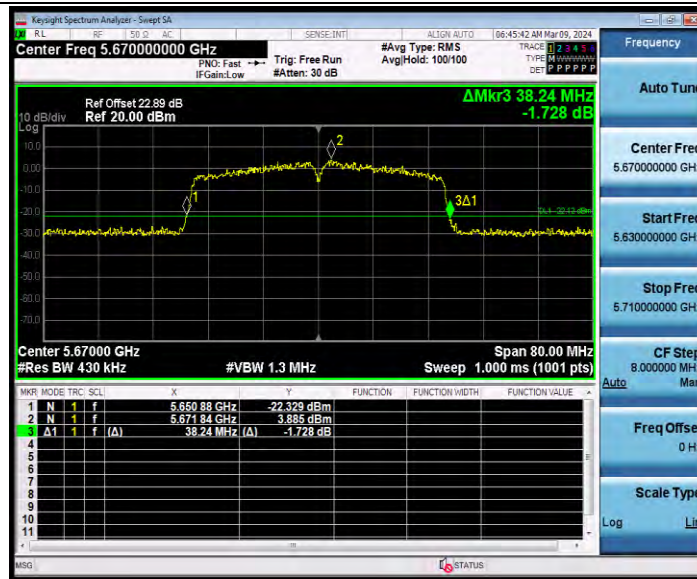
# 11N40MIMO\_Ant2\_5550



# 11N40MIMO\_Ant1\_5670



# 11N40MIMO\_Ant2\_5670



11N40MIMO\_Ant1\_5710



11N40MIMO\_Ant2\_5710

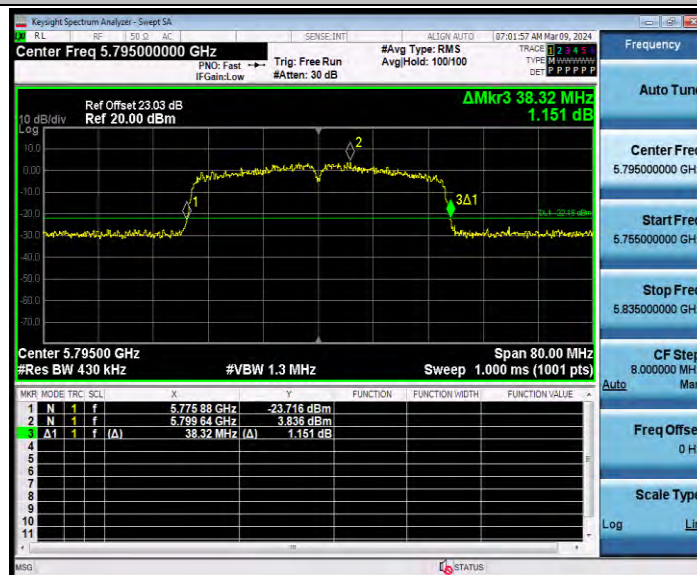




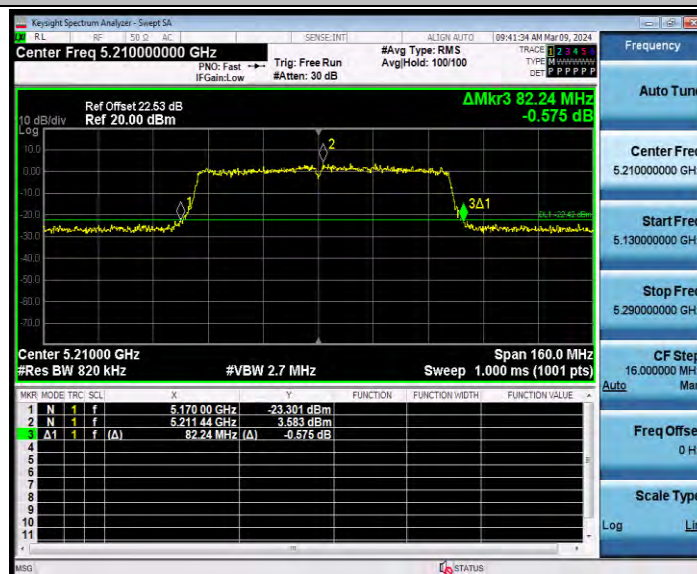




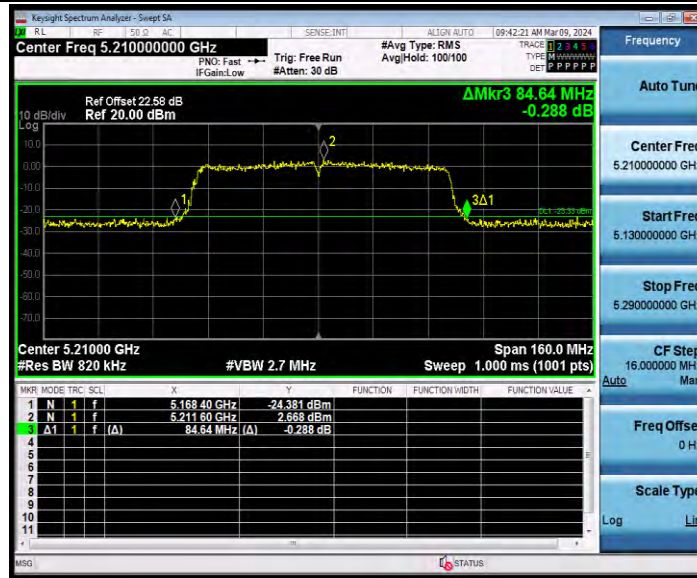
11N40MIMO\_Ant2\_5795



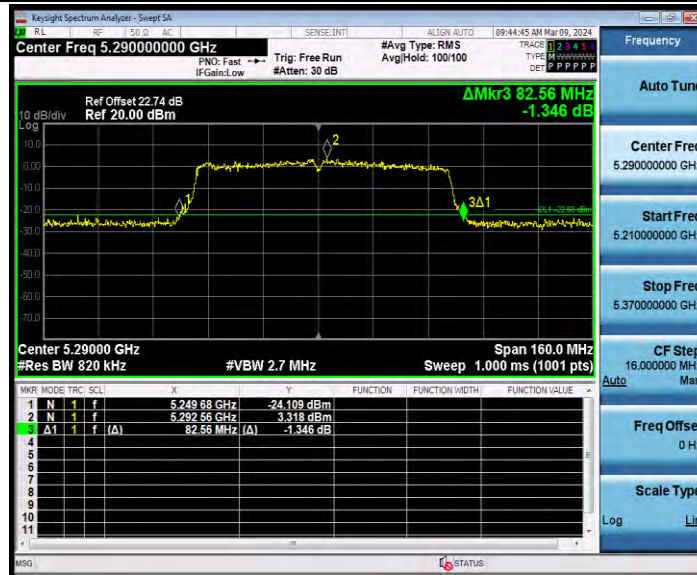
11AC80MIMO\_Ant1\_5210



# 11AC80MIMO\_Ant2\_5210

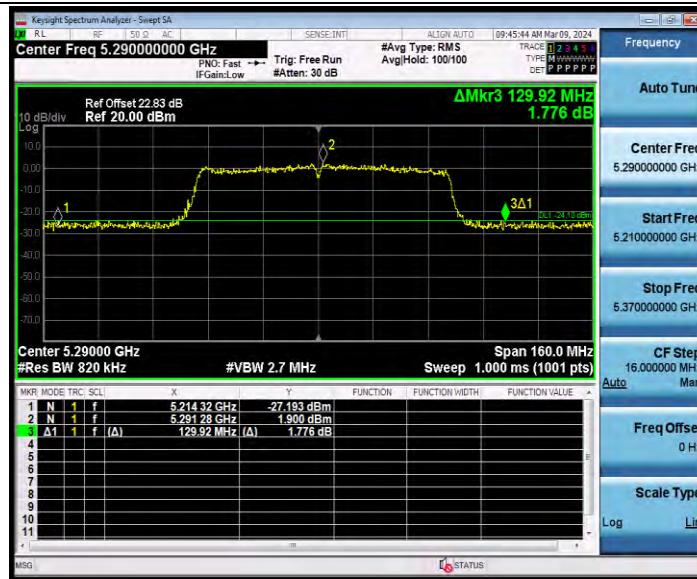


# 11AC80MIMO\_Ant1\_5290

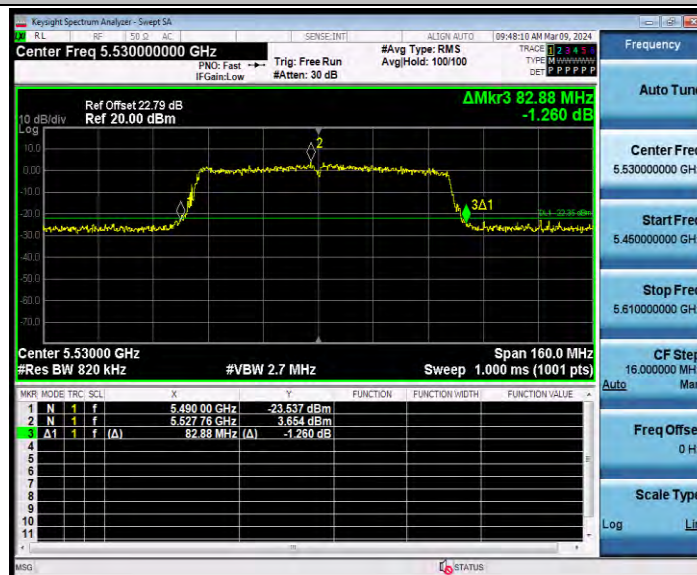


# 11AC80MIMO\_Ant2\_5290

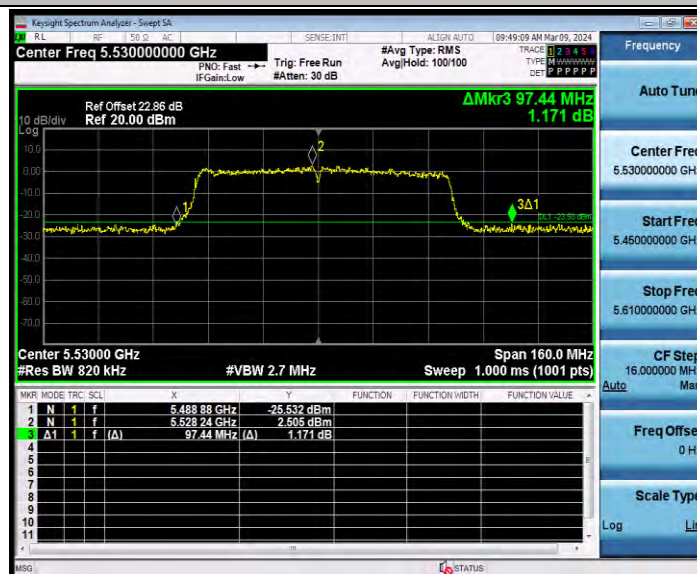




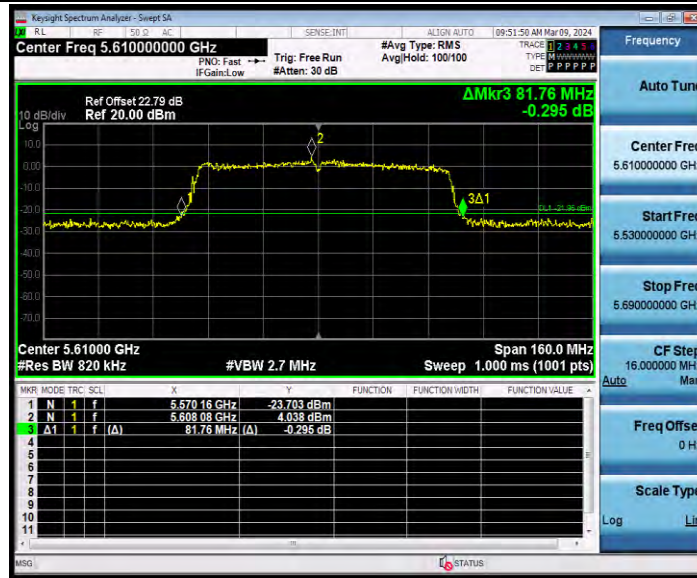
11AC80MIMO\_Ant1\_5530



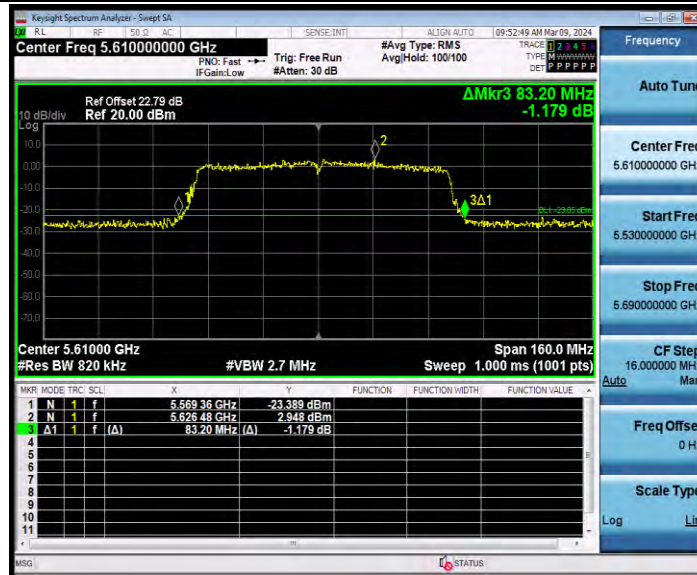
11AC80MIMO\_Ant2\_5530



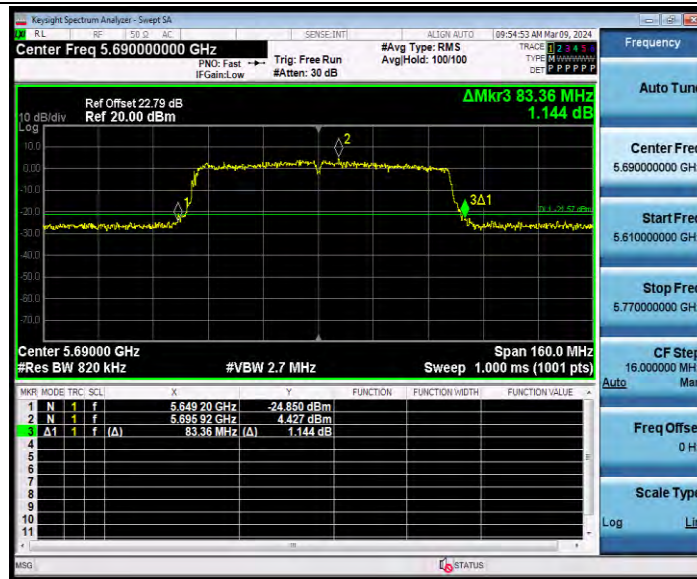
# 11AC80MIMO\_Ant1\_5610



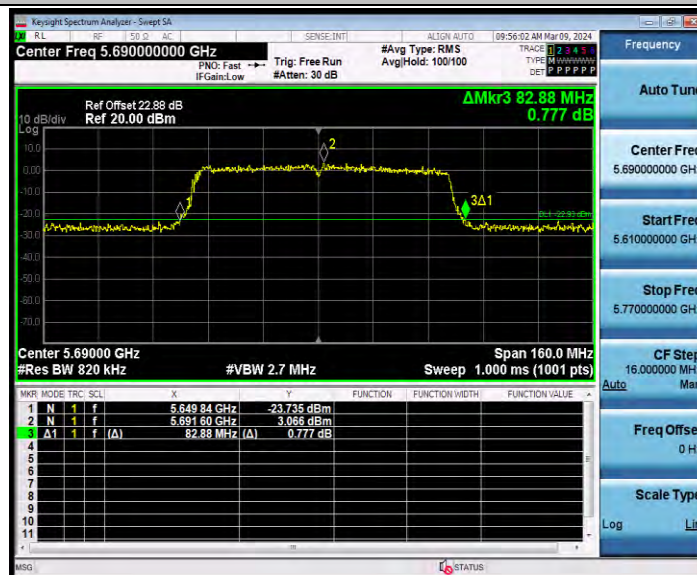
# 11AC80MIMO\_Ant2\_5610



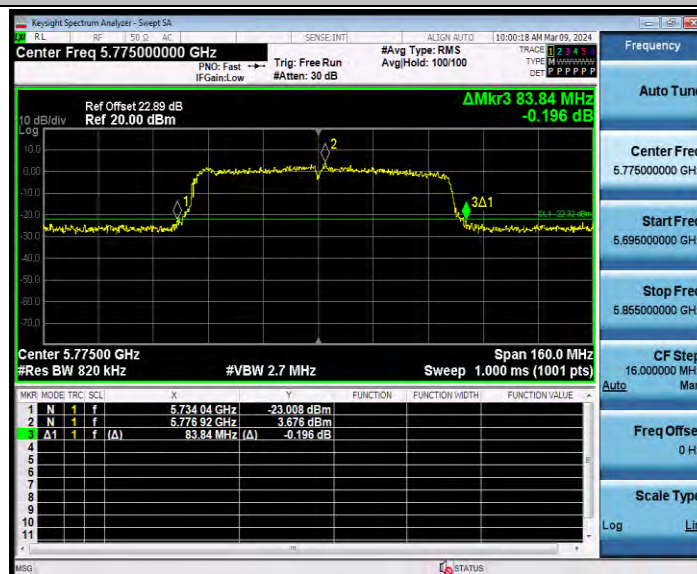
# 11AC80MIMO\_Ant1\_5690



11AC80MIMO\_Ant2\_5690



11AC80MIMO\_Ant1\_5775

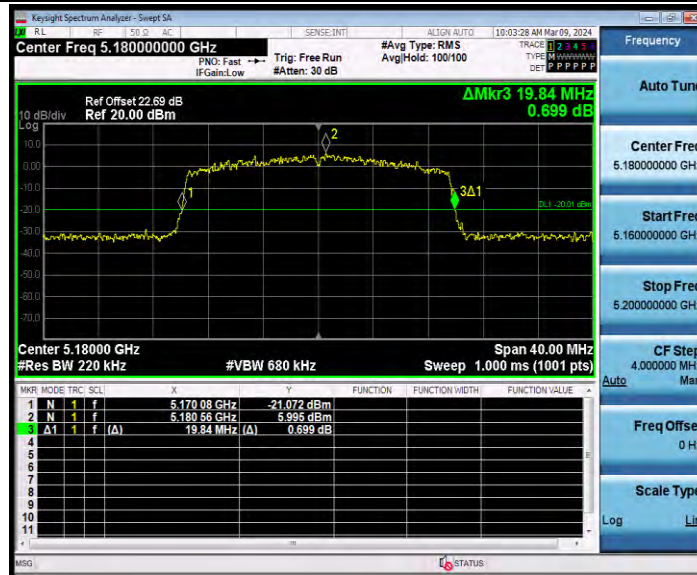




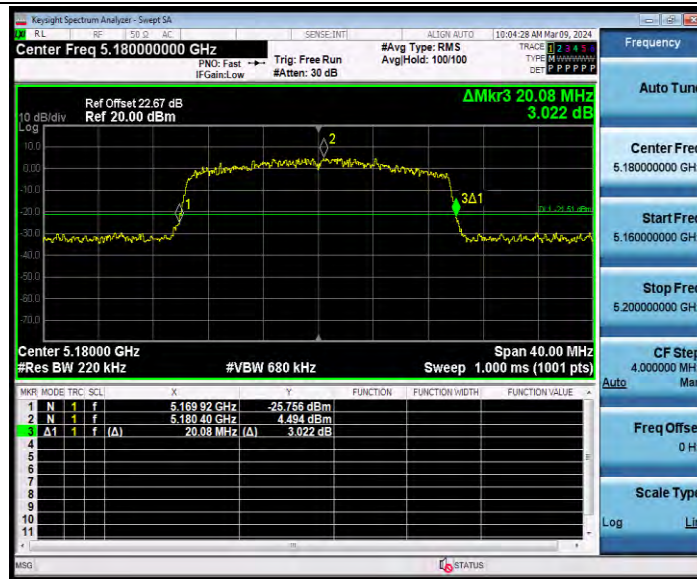
# 11AC80MIMO\_Ant2\_5775



# 11AX20MIMO\_Ant1\_5180



# 11AX20MIMO\_Ant2\_5180



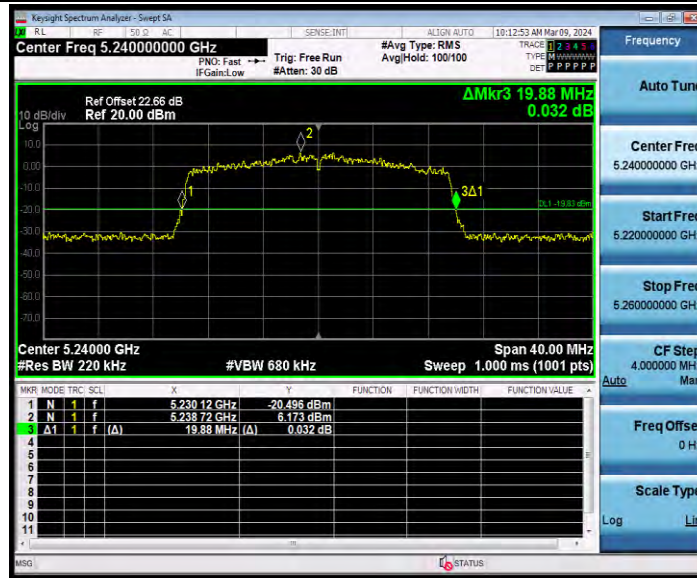
11AX20MIMO\_Ant1\_5200



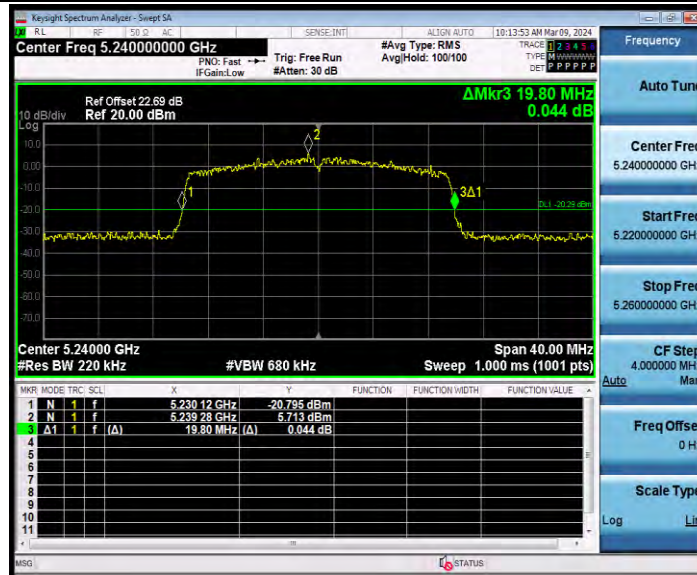
11AX20MIMO\_Ant2\_5200



# 11AX20MIMO\_Ant1\_5240

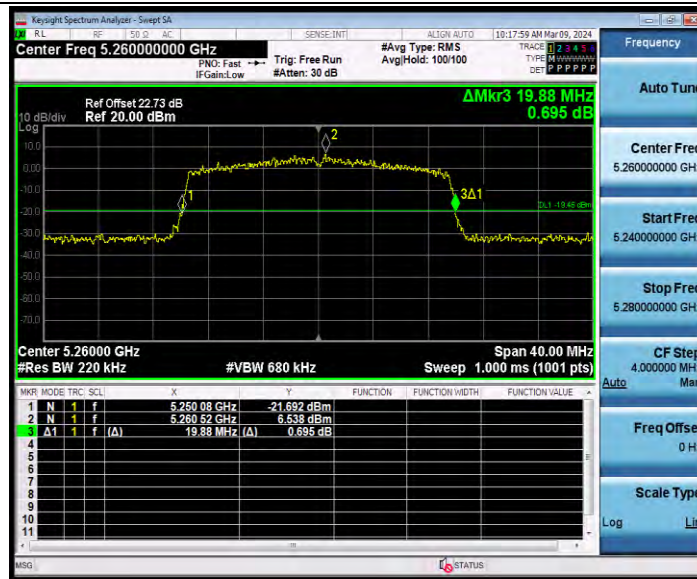


# 11AX20MIMO\_Ant2\_5240



# 11AX20MIMO\_Ant1\_5260

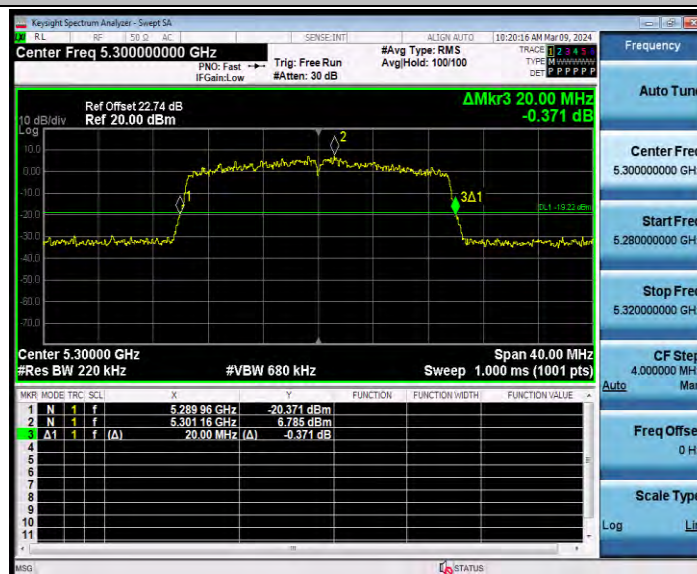




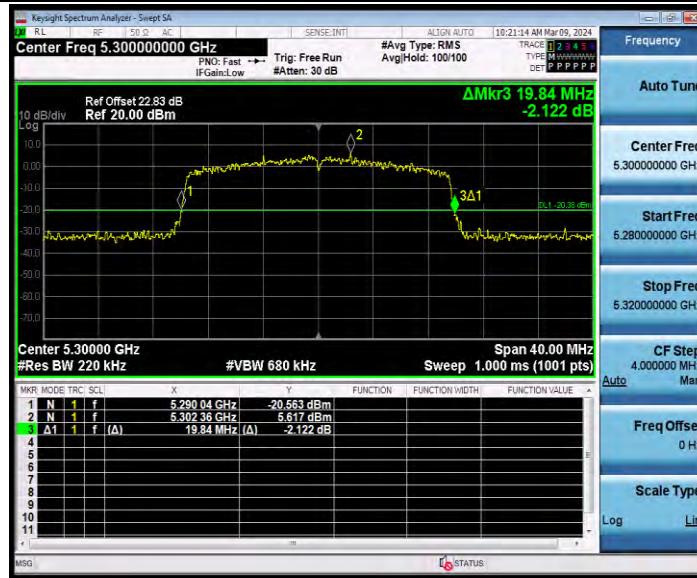
11AX20MIMO\_Ant2\_5260



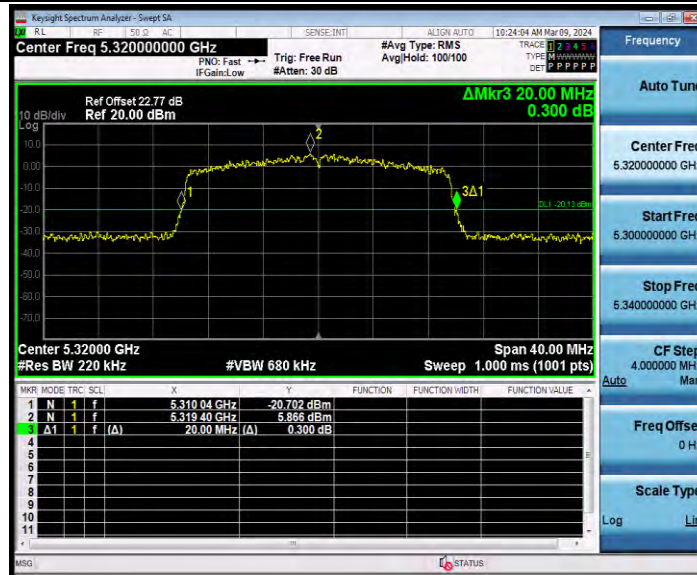
11AX20MIMO\_Ant1\_5300



# 11AX20MIMO\_Ant2\_5300



# 11AX20MIMO\_Ant1\_5320



# 11AX20MIMO\_Ant2\_5320



11AX20MIMO\_Ant1\_5500

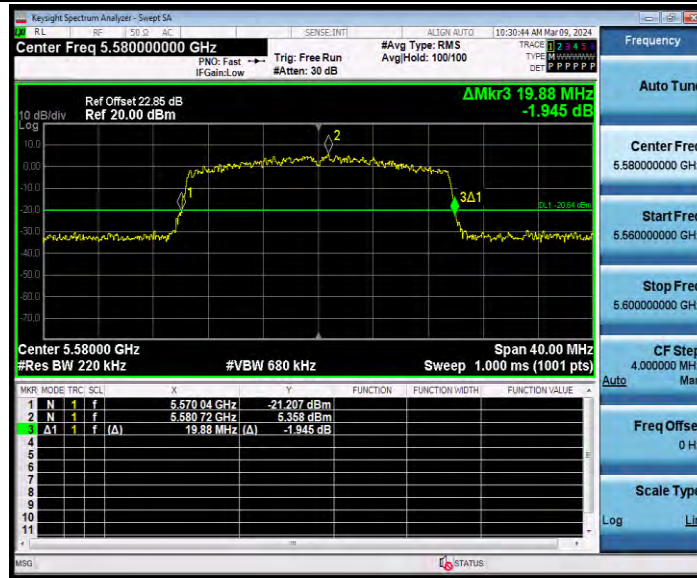


11AX20MIMO\_Ant2\_5500

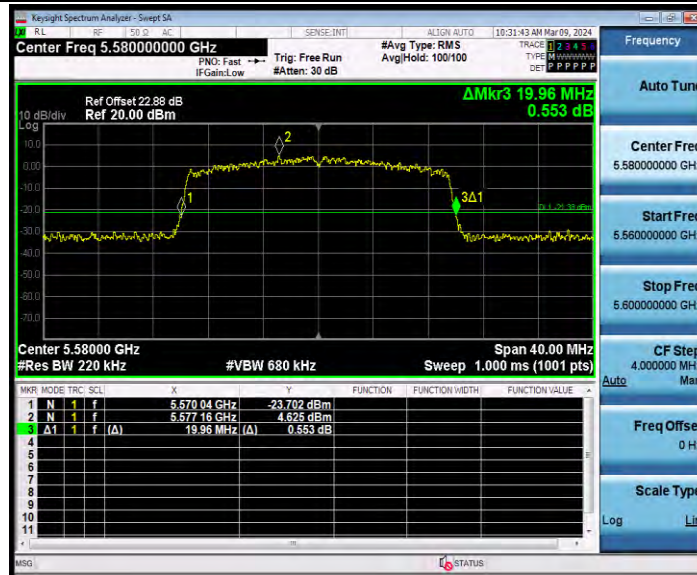




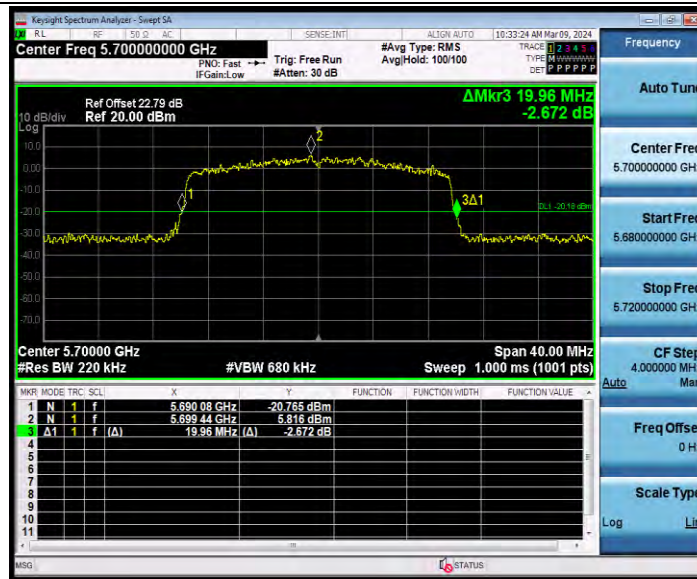
# 11AX20MIMO\_Ant1\_5580



# 11AX20MIMO\_Ant2\_5580



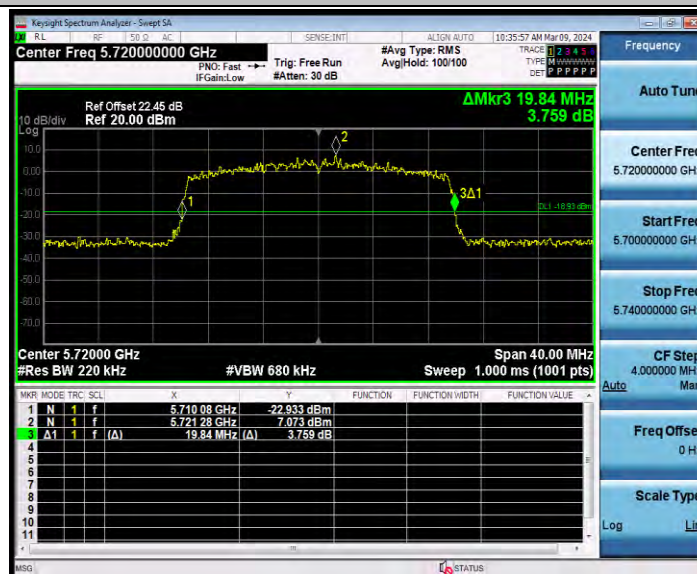
# 11AX20MIMO\_Ant1\_5700



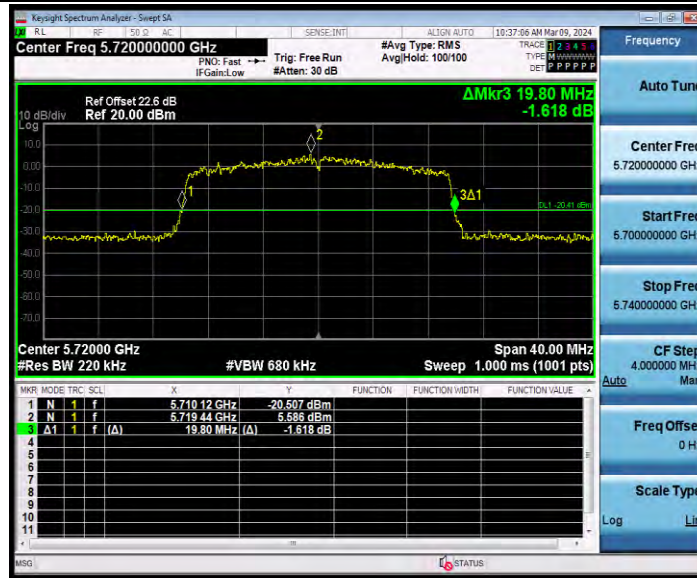
11AX20MIMO\_Ant2\_5700



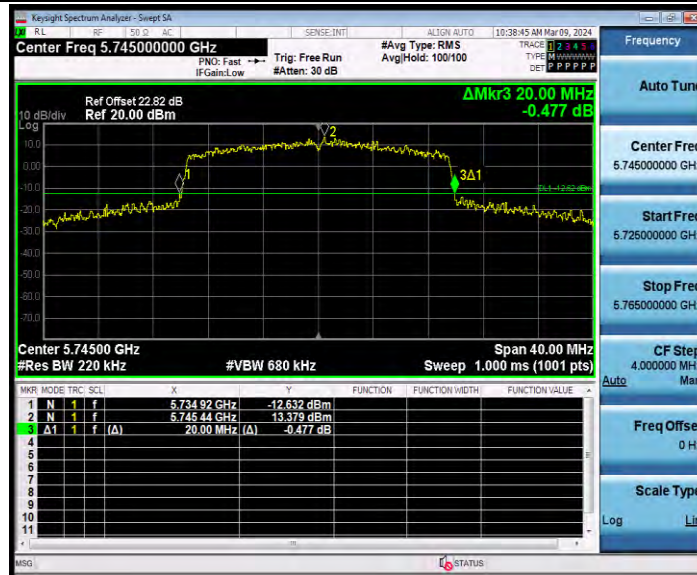
11AX20MIMO\_Ant1\_5720



# 11AX20MIMO\_Ant2\_5720

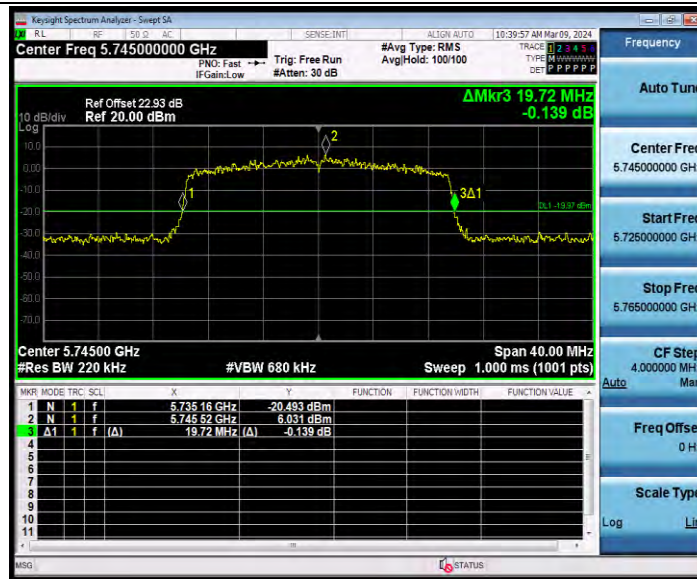


# 11AX20MIMO\_Ant1\_5745



# 11AX20MIMO\_Ant2\_5745

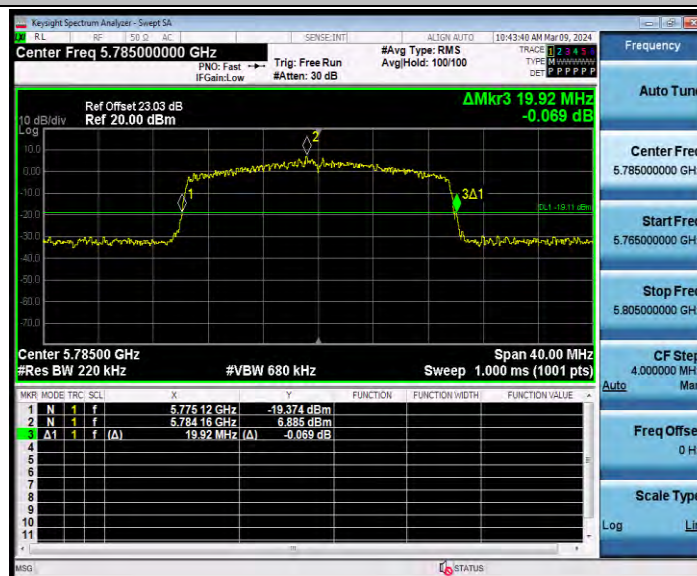




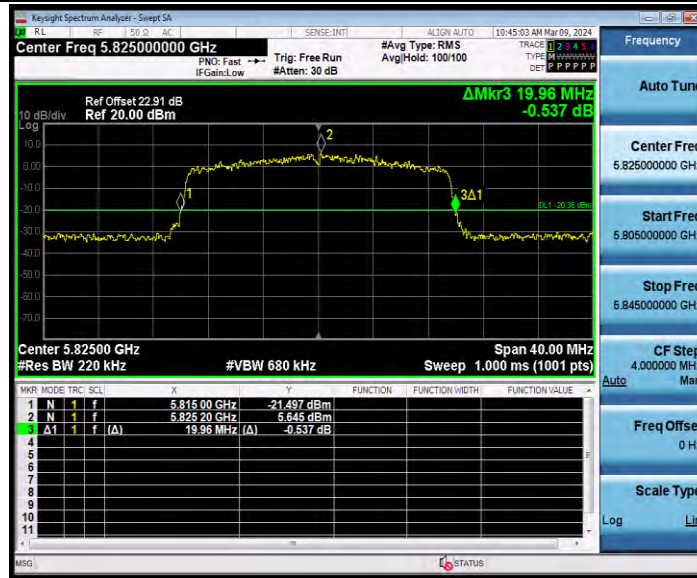
11AX20MIMO\_Ant1\_5785



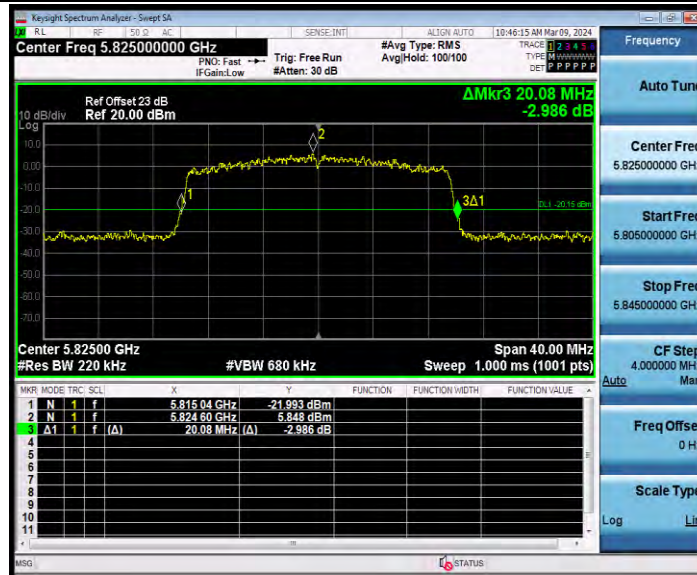
11AX20MIMO\_Ant2\_5785



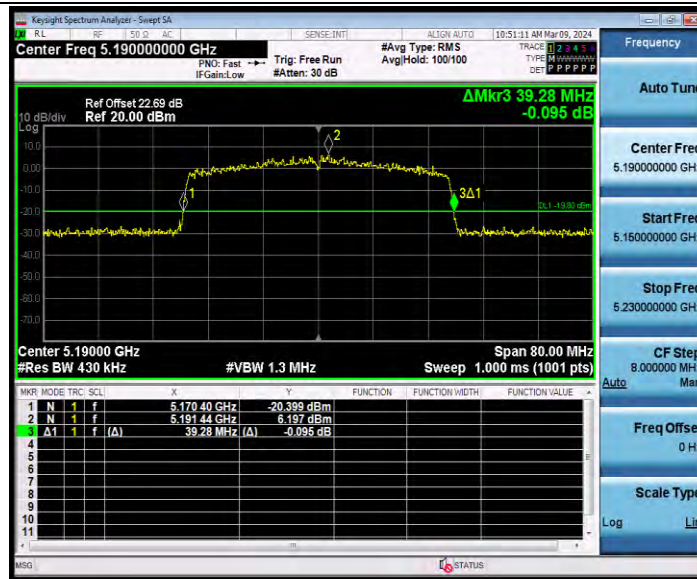
# 11AX20MIMO\_Ant1\_5825



# 11AX20MIMO\_Ant2\_5825



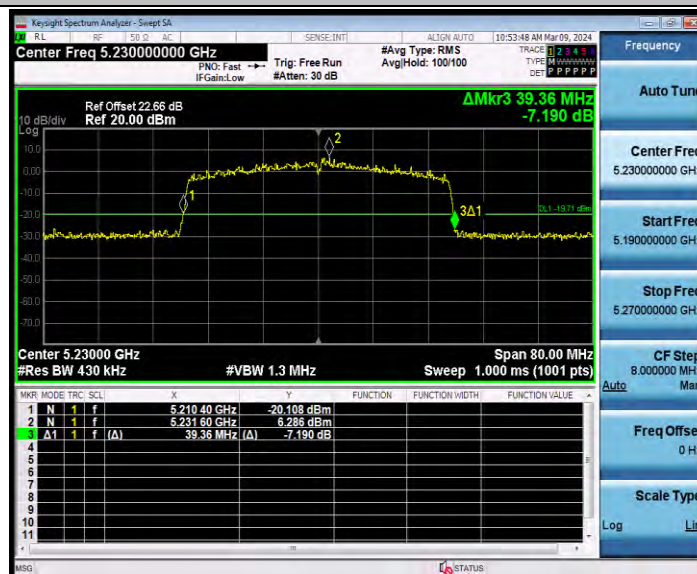
# 11AX40MIMO\_Ant1\_5190



11AX40MIMO\_Ant2\_5190

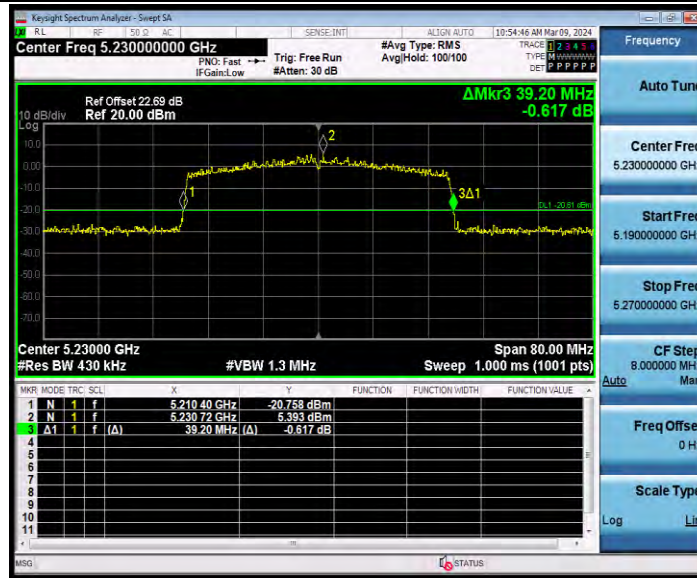


11AX40MIMO\_Ant1\_5230

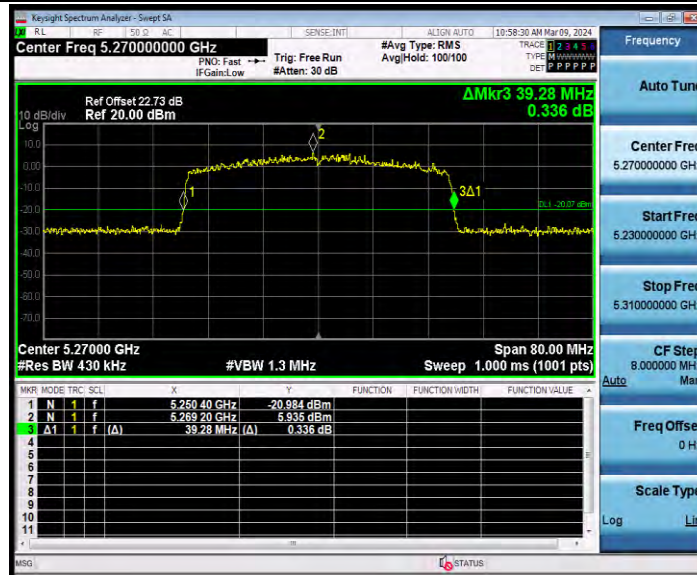




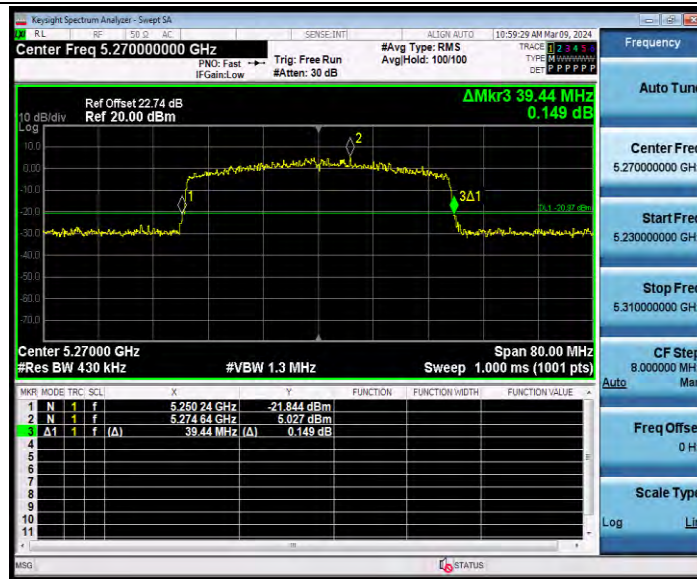
# 11AX40MIMO\_Ant2\_5230



# 11AX40MIMO\_Ant1\_5270



# 11AX40MIMO\_Ant2\_5270



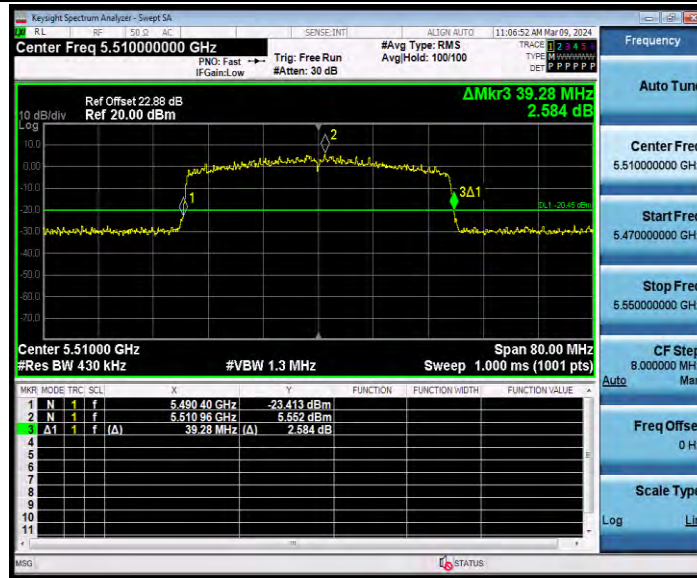
11AX40MIMO\_Ant1\_5310



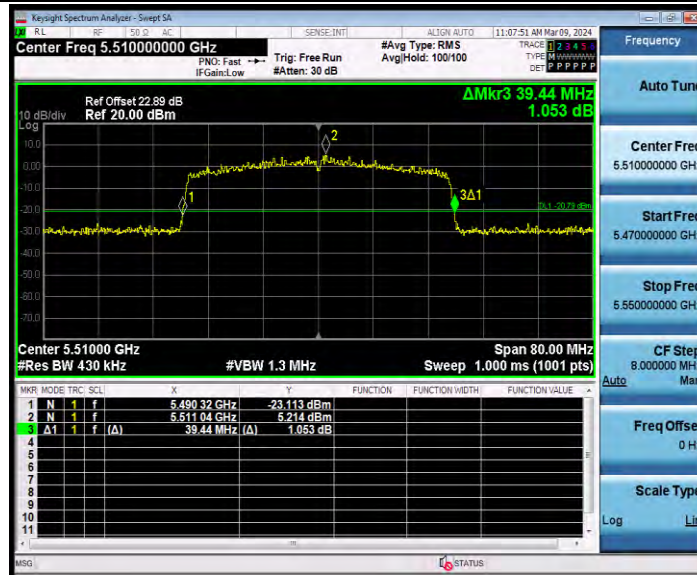
11AX40MIMO\_Ant2\_5310



# 11AX40MIMO\_Ant1\_5510

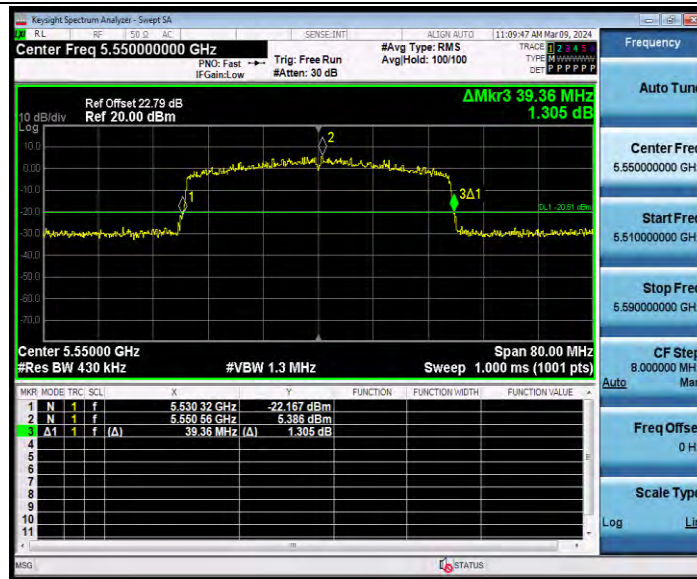


# 11AX40MIMO\_Ant2\_5510



# 11AX40MIMO\_Ant1\_5550

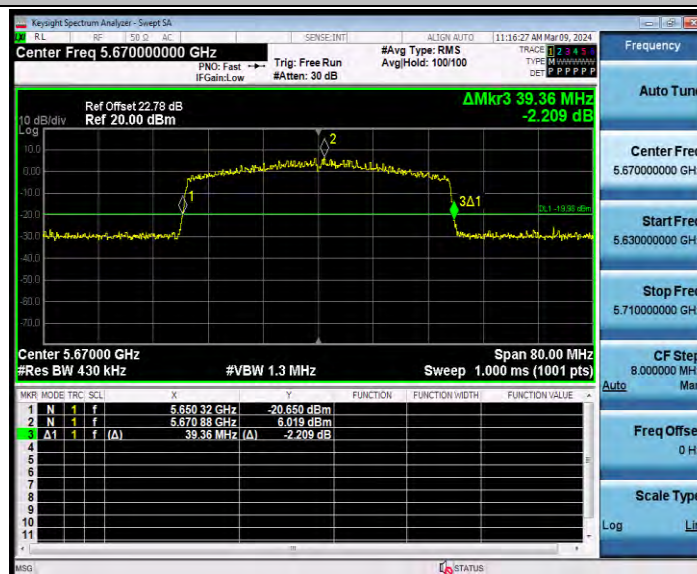




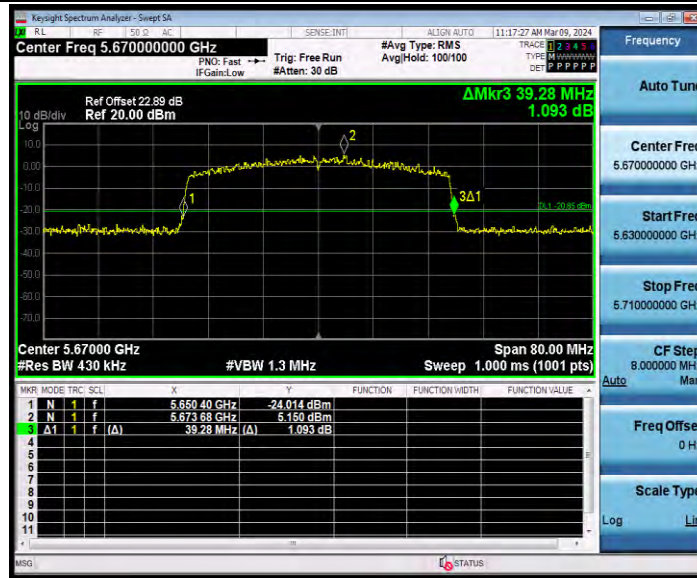
11AX40MIMO\_Ant2\_5550



11AX40MIMO\_Ant1\_5670



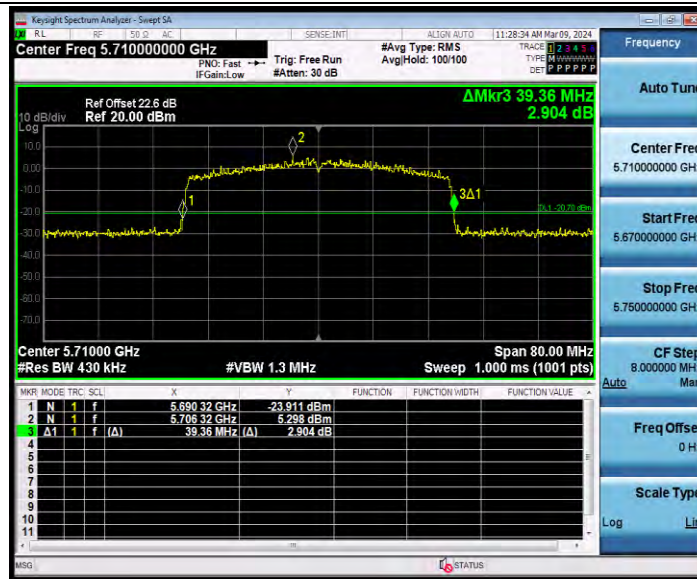
# 11AX40MIMO\_Ant2\_5670



# 11AX40MIMO\_Ant1\_5710



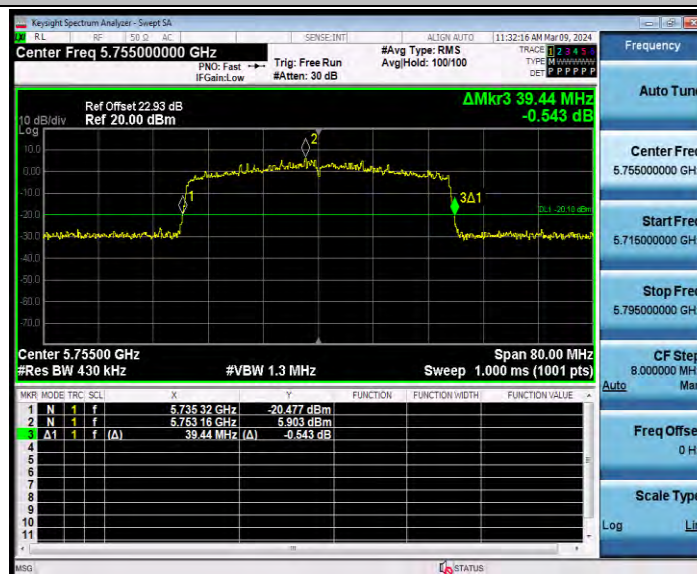
# 11AX40MIMO\_Ant2\_5710



11AX40MIMO\_Ant1\_5755

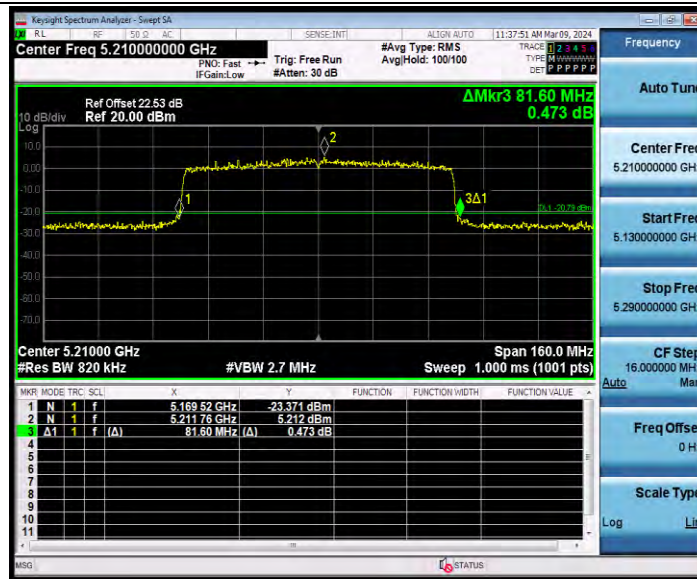


11AX40MIMO\_Ant2\_5755

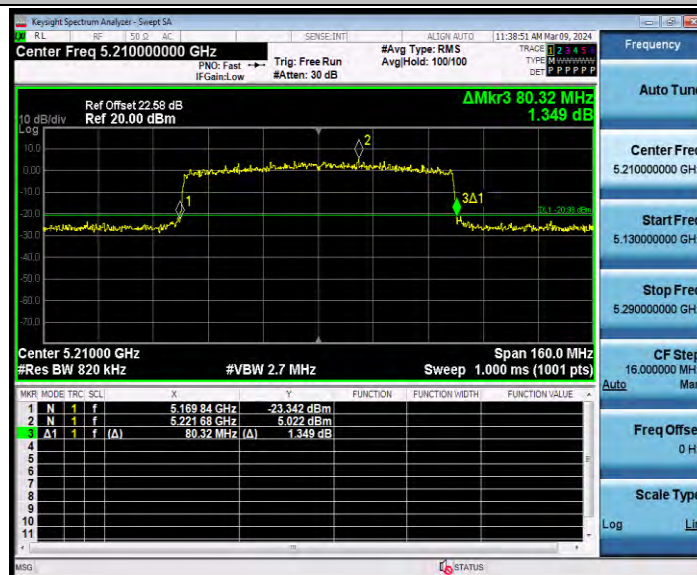








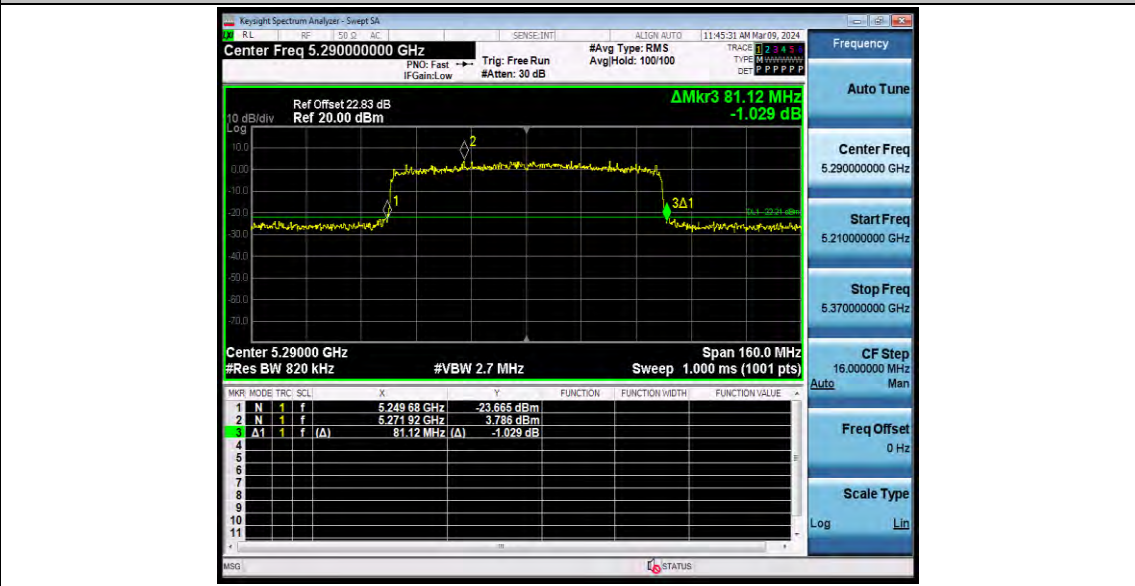
11AX80MIMO\_Ant2\_5210



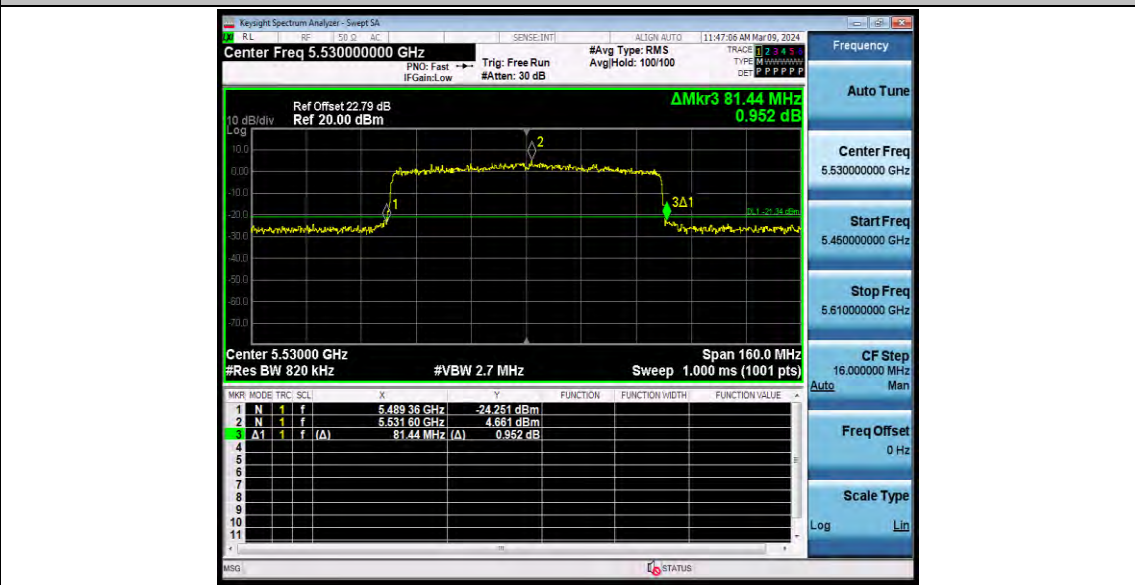
11AX80MIMO\_Ant1\_5290



11AX80MIMO\_Ant2\_5290

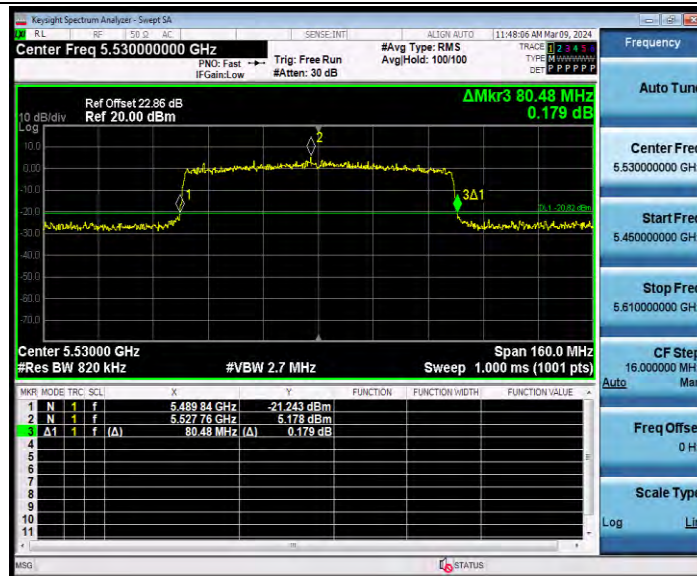


11AX80MIMO\_Ant1\_5530



11AX80MIMO\_Ant2\_5530





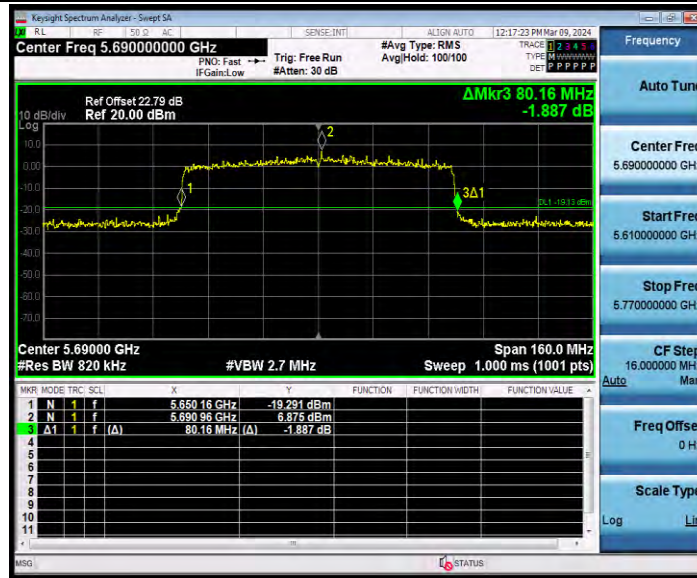
11AX80MIMO\_Ant1\_5610



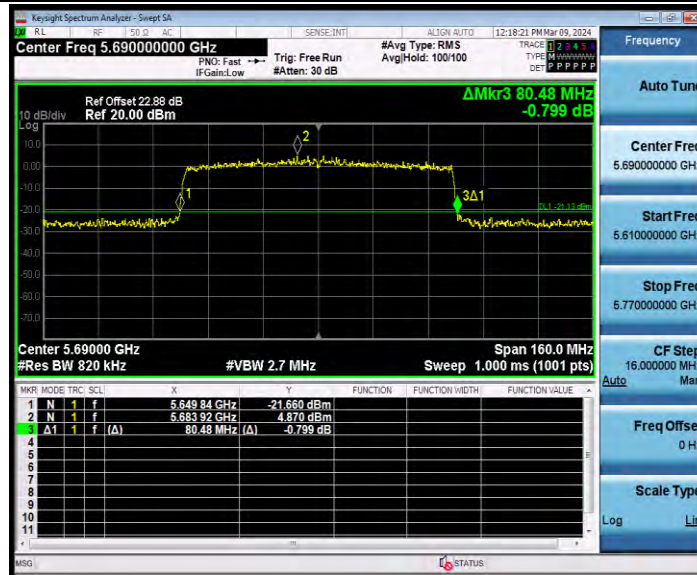
11AX80MIMO\_Ant2\_5610



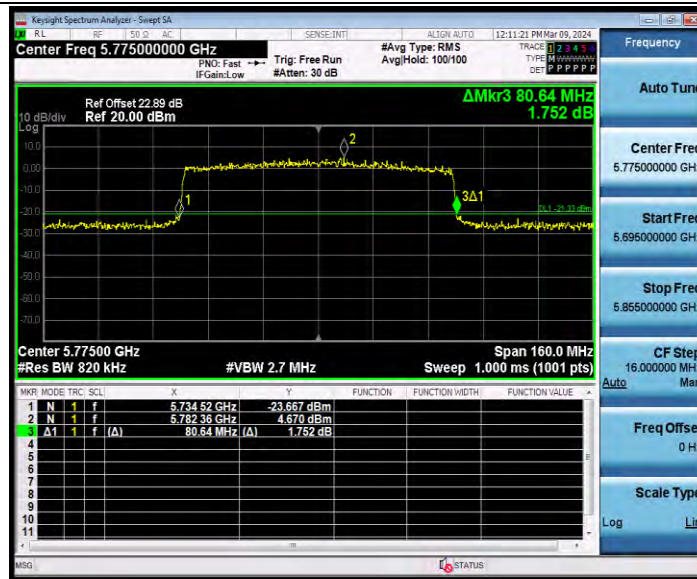
# 11AX80MIMO\_Ant1\_5690



# 11AX80MIMO\_Ant2\_5690



# 11AX80MIMO\_Ant1\_5775



11AX80MIMO\_Ant2\_5775

