FN920C04

Hardware Design Guide

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Contents

1 Applicability Table

Table 1: Applicability Table

Products
FN920C04-WW

2 Introduction

2.1 Scope

This document introduces Telit FN920C04 module and presents possible and recommended hardware solutions for the development of a product based on this module. All the features and solutions described in this document apply to FN920C04 variant listed in the applicability table.

This document cannot include every hardware solution or every product that can be designed. Where the suggested hardware configurations are not to be considered mandatory, the information provided should be used as a guide and starting point for the proper development of the product with the Telit FN920C04 module.

2.2 Audience

This document is intended for Telit customers, especially system integrators, about to implement their applications using the Telit FN920C04 module.

2.3 Contact Information, Support

For technical support and general questions, e-mail:

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Alternatively, use: https://www.telit.com/contact-us

Product information and technical documents are accessible 24/7 on our website: https://www.telit.com

2.4 Conventions

Note:	Provide advice and suggestions that may be useful when integrating the module.
Danger:	This information MUST be followed, or catastrophic equipment failure or personal injury may occur.
Warning:	Alerts the user on important steps about the module integration.

All dates are in ISO 8601 format, that is YYYY-MM-DD

2.5 Terms and Conditions

Refer to https://www.telit.com/hardware-terms-conditions/.

2.6 Disclaimer

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3 General Product Description

3.1 Overview

The aim of this document is to present the possible and recommended hardware solutions useful for developing a product with the Telit FN920C04 M.2 module.

FN920C04 is Telit's platform for the M.2 module for applications, such as M2M applications and industrial IoT device platforms, based on the following technologies:

- 5G sub-6 / 4G networks for data communication
- Designed for industrial grade quality

Front-end for mobile products, offering mobile communication features to an external host CPU through its rich interfaces.

FN920C04 is available in hardware variants as listed in the APPLICABILITY TABLE.

The designated RF band set for each variant is detailed in section Frequency Bands Combinations.

3.2 Frequency Bands Combinations

3.2.1 Frequency Bands

Operating frequencies in 5G, LTE modes conform to 3GPP specifications.

The table below lists the FN920C04 operating frequencies on 5G, LTE modes.

- Supported 5G NR Sub-6 Bands

Table 2: supported 5G NR Sub-6 Bands

NR BAND	Duplex Mode	Uplink Frequency (MHz)	Downlink Frequency (MHz)	Channels	SCS (kHz)
n1 - 2100	FDD	1920 - 1980	2110 - 2170	Tx: 384000 - 396000 Rx: 422000 - 434000	15
n2 - 1900 PCS	FDD	1850 - 1910	1930 - 1990	Tx: 370000 - 382000 Rx: 386000 - 398000	15
n3 - 1800	FDD	1710 - 1785	1805 - 1880	Tx: 342000 - 357000 Rx: 361000 - 376000	15
n5 - 850	FDD	824 - 849	869 - 894	Tx: 164800 - 169800 Rx: 173800 - 178800	15
n7 - 2600	FDD	2500 - 2570	2620 - 2690	Tx: 500000 - 514000 Rx: 524000 - 538000	15
n8 - 900 GSM	FDD	880 - 915	925 - 960	Tx: 176000 - 183000 Rx: 185000 - 192000	15

NR BAND	Duplex Mode	Uplink Frequency (MHz)	Downlink Frequency (MHz)	Channels	SCS (kHz)
n12 - 700 a	FDD	699 - 716	729 - 746	Tx: 139800 - 143200 Rx: 145800 - 149200	15
n13 - 700 c	FDD	777 - 787	746 - 756	Tx: 155400 - 157400 Rx: 149200 - 151200	15
n14 - 700 PS	FDD	788 - 798	758 - 768	Tx: 157600 - 159600 Rx: 151600 - 153600	15
n18 - 800 Lower	FDD	815 - 830	860 - 875	Tx: 163000 - 166000 Rx: 172000 - 175000	15
n20 - 800	FDD	832 - 862	791 - 821	Tx: 166400 - 172400 Rx: 158200 - 164200	15
n25 - 1900+	FDD	1850 - 1915	1930 - 1995	Tx: 370000 - 383000 Rx: 386000 - 399000	15
n26 - 850+	FDD	814 - 849	859 -894	Tx: 162800 - 169800 Rx: 171800 - 178800	15
n28 - 700 APT	FDD	703 - 748	758 - 803	Tx: 140600 - 149600 Rx: 151600 - 160600	15
n30 - 2300 WCS	FDD	2305 - 2315	2350 - 2360	Tx: 461000 - 463000 Rx: 470000 - 472000	15
n38 - TD 2600	TDD	2570 - 2620		T/Rx: 514000 - 524000	30
n40 - TD 2300	TDD	2300 - 2400		T/Rx: 460000 - 480000	30
n41 - TD 2600+	TDD	2496 - 2690		T/Rx: 499200 - 537996	30
n48 - TD 3600	TDD	3550 - 3700		T/Rx: 636668 - 646666	30
n53 - TD 2500	TDD	2483.5 - 2495		T/Rx: 496700 - 499000	30
n66 - AWS	FDD	1710 - 1780	2110 - 2200	Tx: 342000 - 356000 Rx: 422000 - 440000	15
n70 - AWS-4	FDD	1695 - 1710	1995 - 2020	Tx: 339000 - 342000 Rx: 399000 - 404000	15
n71 - 600	FDD	663 - 698	617 - 652	Tx: 132600 - 139600 Rx: 123400 - 130400	15
n77 - TD 3700	TDD	3300 - 4200		T/Rx: 620000 - 680000	30
n78 - TD 3500	TDD	3300 - 3800		T/Rx: 620000 - 653332	30

NR BAND	Duplex Mode	Uplink Frequency (MHz)	Downlink Frequency (MHz)	Channels	SCS (kHz)
n79 - TD 4700	TDD	4400 - 5000		T/Rx: 693334 - 733332	30

- Supported LTE Bands

Table 3: Supported LTE Bands

E-UTRA BAND	Duplex Mode	Uplink Frequency (MHz)	Downlink Frequency (MHz)	Channels
B1 - 2100	FDD	1920 - 1980	2110 - 2170	Tx: 18000 - 18599 Rx: 0 - 599
B2 - 1900 PCS	FDD	1850 - 1910	1930 - 1990	Tx: 18600 - 19199 Rx: 600 - 1199
B3 - 1800+	FDD	1710 - 1785	1805 - 1880	Tx: 19200 - 19949 Rx: 1200 - 1949
B4 - AWS-1	FDD	1710 - 1755	2110 - 2155	Tx: 19950 - 20399 Rx: 1950 - 2399
B5 - 850	FDD	824 - 849	869 - 894	Tx: 20400 - 20649 Rx: 2400 - 2649
B7 - 2600	FDD	2500 - 2570	2620 - 2690	Tx: 20750 - 21449 Rx: 2750 - 3449
B8 - 900 GSM	FDD	880 - 915	925 - 960	Tx: 21450 - 21799 Rx: 3450 - 3799
B12 - 700 a	FDD	699 - 716	729 - 746	Tx: 23010 - 23179 Rx: 5010 - 5179
B13 - 700 c	FDD	777 - 787	746 - 756	Tx: 23180 - 23279 Rx: 5180 - 5279
B14 - 700 PS	FDD	788 - 798	758 - 768	Tx: 23280 - 23379 Rx: 5280 - 5379
B17 - 700 b	FDD	704 - 716	734 - 746	Tx: 23730 - 23849 Rx: 5730 - 5849
B18 - 800 Lower	FDD	815 - 830	860 - 875	Tx: 23850 - 23999 Rx: 5850 - 5999
B19 - 800 Upper	FDD	830 - 845	875 - 890	Tx: 24000 - 24149 Rx: 6000 - 6149
B20 - 800 DD	FDD	832 - 862	791 - 821	Tx: 24150 - 24449 Rx: 6150 - 6449

E-UTRA BAND	Duplex Mode	Uplink Frequency (MHz)	Downlink Frequency (MHz)	Channels
B25 - 1900+	FDD	1850 - 1915	1930 - 1995	Tx: 26040 - 26689 Rx: 8040 - 8689
B26 - 850+	FDD	814 - 849	859 - 894	Tx: 26690 – 27039 Rx: 8690 - 9039
B28 - 700 APT	FDD	703 - 748	758 - 803	Tx: 27210 - 27659 Rx: 9210 - 9659
B30 - 2300 WCS	FDD	2305 - 2315	2350 - 2360	Tx: 27660 - 27759 Rx: 9770 - 9869
B34 - TD 2000	TDD	2010 – 2025		T/Rx: 36200 - 36349
B38 - TD 2600	TDD	2570 - 2620		T/Rx: 37750 - 38249
B39 - TD 1900+	TDD	1880 - 1920		T/Rx: 38250 - 38649
B40 - TD 2300	TDD	2300 - 2400		T/Rx: 38650 - 39649
B41 - TD 2600+	TDD	2496 - 2690		T/Rx: 39650 - 41589
B42 - TD 3500	TDD	3400 - 3600		T/Rx: 41590 - 43589
B43 - TD 3700	TDD	3600 – 3800		T/Rx: 43590 – 45589
B48 - TD 3600	TDD	3550 - 3700		T/Rx: 55240 - 56739
B66 - AWS	FDD	1710 - 1780	2110 - 2200	Tx: 131972 - 132671 Rx: 66436 - 67335
B70 - AWS-4	FDD	1695 - 1710	1995 - 2020	Tx: 132972- 133121 Rx: 68336 - 68585
B71 - 600	FDD	663 - 698	617 - 652	Tx: 133122 - 133471 Rx: 68586 - 68935

3.3 Target Market

The FN920C04 can be used for industrial applications where tamper-resistance, confidentiality, integrity, and authenticity of end-user information are required, for example:

- Industrial router and gateways
- Connected laptops and tablets
- Network equipment requiring internet connectivity through cellular networks

3.4 Main Features

The FN920C04 of industrial-grade cellular modules features 5G Sub-6, LTE, and multi-RAT modules together with an on-chip powerful application processor and a rich set of interfaces.

The main functions and features are listed below:

Table 4: Main Features

Function	Features
Physical	M.2 Type 3042-S3-B
Modem	5G NR RedCap, 3GPP Rel 17 SA Mode: up to: 200Mbps on DL, 100Mbps on UL 4G: CAT4 ,150Mbps on DL, 50Mbps on UL
Supported Bands	5G: n1, n2, n3, n5, n7, n8, n12, n13, n14, n18, n20, n25, n26, n28, n30, n38, n40, n41, n48, n53, n66, n70, n71, n77, n78, n79 4G: B1/B2(B25)/B3/B4(B66)/B26(B5,B18,B19)/B7/B8/B12(B17)/B13/B14/B20/B28/ B30/ B34/B38/B39/B40/B41/B42/B43/B48/B70/B71
GNSS	GPS/Glonass/Beidou/Galileo
SRS	1T2R (n38, n40, n41, n53, n77, n78, n79)
USIM port – Dual Voltage	Two SIM support (UIM2 can be assigned as optional eSIM) Class B and Class C support
Application processor	Arm Cortex A7 processor at up to 1.7 GHz
Main Interfaces	PCIe Gen2 x 1-lane USB HS 2.0 Peripheral Ports – GPIOs
Antenna connection	2 x MHF-4 type Cellular/GNSS antenna connectors 1 x MHF-4 type Dedicated GNSS antenna connector
Form factor	M.2 Form factor (30 * 42 * 2.3 mm), accommodating the multiple RF bands
Environment and quality requirements	The device is designed and qualified by Telit to satisfy environmental and quality requirements.
Single supply module	The module internally generates all its required internal supply voltages.
Operating temperature	Range -40 degC to +85 degC (conditions as defined in Section 3.8.1, Temperature Range)

Function	Features
Operating Humidity	≤95%, non-condensing

3.4.1 Configurations Pins [TBD]

FN920C04 module can be configured as USB HS 2.0 or PCIe devices. The primary host interface can be set using the four pins listed in the table below.

Table 5: Configurations Pins

Pin	Signal	State	Interface Type
21	CONFIG_0	GND	PCIe or USB HS 2.0 Port Configuration 2 (Applicable to WWAN only) [TBD]
69	CONFIG_1	GND	
75	CONFIG_2	NC	
1	CONFIG_3	NC	

Note: On the host side, each of the CONFIG_0 to CONFIG_3 signals must be equipped with a pull-up resistor. Based on the state of the configuration pins on the Add-in Card, being tied to GND or left No Connect (NC), the detected pins will create a 4-bit logic state that requires decoding.

Used by an Add-in Card where USB is present on the connector and PCIe is No Connect.

Only a single lane of PCIe is available in these configurations.

For more details, please refer to the PCI Express M.2 Specification document.

3.5 Block Diagram

The figure below shows an overview of the internal architecture of the FN920C04 module.

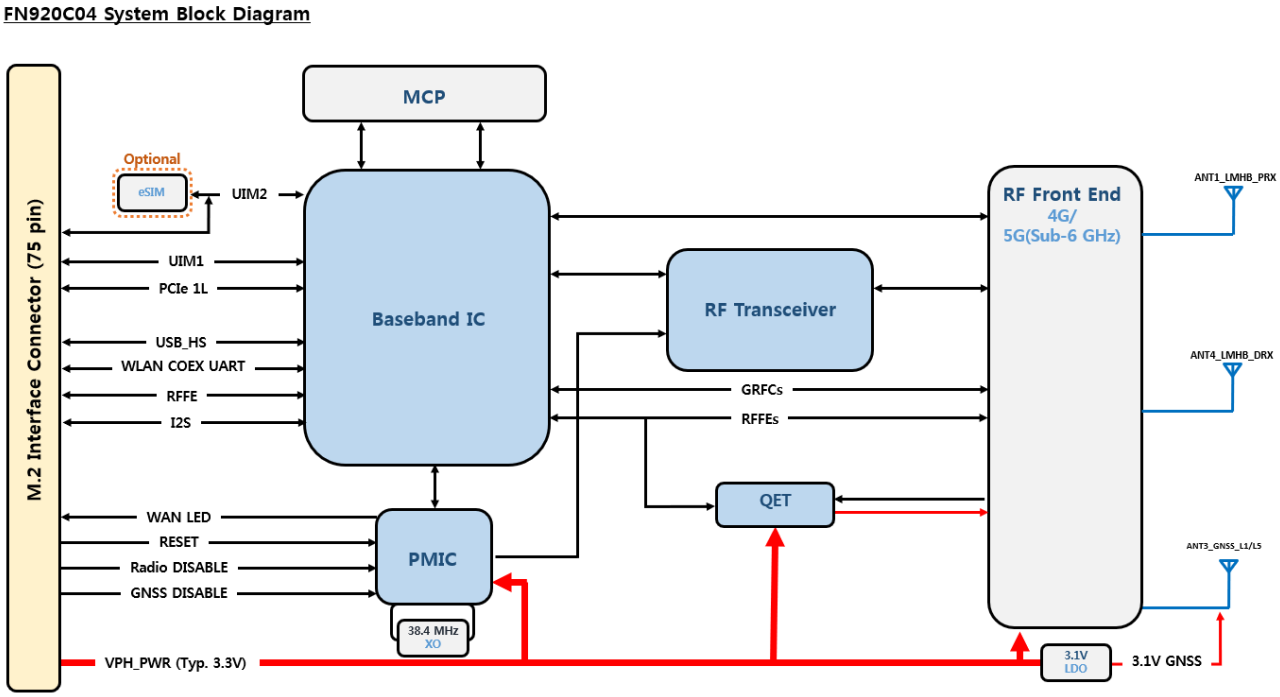


Figure 1: FN920C04 Block Diagram

3.6 RF Performance

Modem RF performance in 5G, LTE radio access technologies conforms to 3GPP specifications.

3.6.1 Conducted Transmit Output Power

TX power follows the measurement conditions and specifications defined in 3GPP.

Table 6: Conducted Transmit Output Power

Band	Power class	RF Power (dBm) Nominal[1)]
5G NR Sub-6 n1, n2, n3, n5, n7, n8, n12, n13, n14, n18, n20, n25, n26, n28, n30[3)], n38, n40, n41, n48[2)], n53, n66, n70, n71, n77, n78, n79	3 (0.2W)	23
LTE All Bands B1, B2(B25), B3, B4(B66), B26(B5,B18,B19), B7, B8, B12(B17), B13, B14, B20, B28, B30[3)], B34, B38, B39, B40, B41, B42[2)], B43[2)], B48[2)], B70, B71	3 (0.2W)	23
LTE B41, B42 Supports Power Class 2	2 (0.4W)	26

Note: [1] RF Power Nominal : Max output power tolerance according to 3GPP TS 38.521-1, 3GPP TS 36.521-1 and 3GPP TS 34.121-1 or better

Note: [2] LTE B42, B43, B48 / NR n48 : PC 3 Nominal RF Power 22dBm

These bands may show lower power due to Country-type approvals constraints

Note: [3] LTE B30 / NR n30 : PC 3 Nominal RF Power 22dBm

These bands may show lower power due to Country-type approvals constraints

3.6.2 Conducted Receiver Sensitivity [TBD]

The Sensitivity of the receiver follows the measurement conditions and specifications defined by 3GPP.

Table 7: 3GPP Compliance for Conducted Receiver Sensitivity

Technology	3GPP Compliance
5G NR Sub-6	Throughput >95%
4G LTE	Throughput >95%

Table 8: Typical Conducted Receiver Sensitivity - NR Bands

NR Band					
	SCS (kHz)	BW (MHz)	ANT1	ANT4	Combined
NR FDD n1	15	10	-98	-97	-101
NR FDD n2	15	10	-98	-98	-101
NR FDD n3	15	10	-97	-97	-100
NR FDD n5	15	10	-98	-100	-102
NR FDD n7	15	10	-97	-97	-100
NR FDD n8	15	10	-98	-100	-102
NR FDD n12	15	10	-98	-99	-102
NR FDD n13	15	10	-98	-99	-102
NR FDD n14	15	10	-98	-99	-102
NR FDD n18	15	10	-98	-99	-102
NR FDD n20	15	10	-99	-99	-102
NR FDD n25	15	10	-98	-98	-101

NR Band					
NR FDD n26	15	10	-98	-99	-102
NR FDD n28	15	10	-98	-99	-102
NR FDD n30	15	10	-96	-97	-99
NR TDD n38	30	10	-97	-97	-100
NR TDD n40	30	10	-97	-97	-100
NR TDD n41	30	10	-97	-97	-100
NR TDD n48	30	10	-99	-97	-101
NR TDD n53	30	10	-95	-97	-100
NR FDD n66	15	10	-98	-97	-100
NR FDD n70	15	10	-96	-98	-100
NR FDD n71	15	10	-99	-99	-102
NR TDD n77	30	10	-98	-97	-100
NR TDD n78	30	10	-98	-97	-100
NR TDD n79	30	10	-98	-96	-99

***3.3 Voltage / Room temperature**

Table 9: Typical Conducted Receiver Sensitivity - LTE Bands

E-UTRA Band	BW (MHz)	ANT1	ANT4	Combined
LTE FDD B1	10	-98	-98	-101
LTE FDD B2	10	-98	-98	-101
LTE FDD B3	10	-97	-98	-101
LTE FDD B4	10	-98	-97	-101
LTE FDD B5	10	-99	-99	-102
LTE FDD B7	10	-97	-97	-100
LTE FDD B8	10	-98	-100	-102
LTE FDD B12	10	-99	-99	-102
LTE FDD B13	10	-99	-100	-102
LTE FDD B14	10	-98	-99	-102
LTE FDD B17	10	-99	-99	-102
LTE FDD B18	10	-99	-99	-102
LTE FDD B19	10	-98	-99	-102
LTE FDD B20	10	-99	-99	-102
LTE FDD B25	10	-98	-98	-101

E-UTRA Band				
LTE FDD B26	10	-98	-99	-102
LTE FDD B28	10	-99	-99	-102
LTE FDD B30	10	-97	-97	-100
LTE TDD B34	10	-98	-98	-101
LTE TDD B38	10	-97	-98	-100
LTE TDD B39	10	-98	-98	-101
LTE TDD B40	10	-96	-97	-99
LTE TDD B41	10	-97	-97	-100
LTE TDD B42	10	-98	-97	-101
LTE TDD B43	10	-98	-98	-101
LTE TDD B48	10	-98	-97	-101
LTE FDD B66	10	-98	-97	-101
LTE FDD B70	10	-97	-98	-100
LTE FDD B71	10	-99	-99	-102

***3.3 Voltage / Room temperature**

Note: The sensitivity level can show a deviation of approximately +/- <2dB, device, and channel because the level shows a typical value.

LTE, NR5G sensitivity level is measured by combining the Primary and Diversity paths, and the combined sensitivity performance meets the 3GPP requirements.

NR5G, LTE level is measured at BW 10 MHz.

3.7 Mechanical Specifications

3.7.1 Dimensions

Overall FN920C04 dimensions are:

- Length: 42 mm
- Width: 30 mm
- Thickness: 2.3 mm

3.7.2 Weight

- The nominal weight of the FN920C04 is 6.6 grams.

3.8 Environmental Requirements

3.8.1 Temperature Range

Table 10: Temperature Range

Mode	Temperature	Note
Operating Temperature Range	–20°C ~ +55°C	The module is fully functional(*) in all the temperature range and it fully meets the 3GPP specifications.
	–40°C ~ +85°C	The module is fully functional (*) in all the temperature range, might slightly deviate from the 3GPP specifications.
Storage and non-operating Temperature Range	–40°C ~ +85°C	The module is not powered and not connected to power supply

Note: (*) Functional: if applicable, the module is able to make and receive voice calls, data calls, send and receive SMS and data traffic.

Warning: The application processor temperature which is in the FN920C04 must be kept below 95°C for the best performance. Depending on the various application, a heat sink, thermal pad, or, other cooling systems may be required to properly dissipate the heat.

A large solder-resist opening area is located on the bottom side of the module. Adding a TIM on that area with a heatsink is one of the best ways to dissipate the heat well. The temperature can be read via AT commands. For more details, please refer to the SW user guide or thermal design guideline.

3.8.2 RoHS Compliance

As a part of the Telit Cinterion corporate policy of environmental protection, the FN920C04 products comply with the RoHS (Restriction of Hazardous Substances) directive of the European Union (EU directive 2011/65/EU).

4 Pins Allocation

4.1 Pin-out

Table 11: FN920C04 Pin-out Information

Pin	Signal	I/O	Function	Type	Comment
USB Communication Port					
7	USB_HS_DP	I/O	USB 2.0 Data Plus	Analog	
9	USB_HS_DM	I/O	USB 2.0 Data Minus	Analog	
29	NC	-			
31	NC	-			
35	NC	-			
37	NC	-			
PCIe Communication Port					
41	PCIE_TX0_M	O	PCIe transmit 0 – Minus	Analog	
43	PCIE_TX0_P	O	PCIe transmit 0 – Plus	Analog	
47	PCIE_RX0_M	I	PCIe receive 0 – Minus	Analog	
49	PCIE_RX0_P	I	PCIe receive 0 – Plus	Analog	
53	PCIE_REFCLK_M	I/O	PCIe differential reference clock – Minus	DI/DO	
55	PCIE_REFCLK_P	I/O	PCIe differential reference clock – Plus	DI/DO	
50	PCIE_RESET_N		Functional reset to PCIe bus		Internal 10k PU Input: Slave Output: Master
52	PCIE_CLKREQ_N		PCIe reference clock request signal		Internal 10k PU Input: Master Output: Slave
54	PCIE_WAKE_N		PCIe wake-up		Internal 10k PU Input: Master Output: Slave
SIM Card Interface 1					
36	UIM1_VCC	O	Supply output for an external UIM1 card	1.8V / 2.95V	Power

Pin	Signal	I/O	Function	Type	Comment
34	UIM1_DATA	I/O	Data connection with an external UIM1 card	1.8V / 2.95V	Internal 20k PU
32	UIM1_CLK	O	Clock output to an external UIM1 card	1.8V / 2.95V	
30	UIM1_RESET_N	O	Reset output to an external UIM1 card	1.8V / 2.95V	
66	UIM1_PRESENT	I	UIM1 Card Present Detect	1.8V	Internal 100k PU Active HIGH
SIM Card Interface 2					
48	UIM2_VCC	O	Supply output for an external UIM2 card	1.8V / 2.95V	Power
42	UIM2_DATA	I/O	Data connection with an external UIM2 card	1.8V / 2.95V	Internal PU
44	UIM2_CLK	O	Clock output to an external UIM2 card	1.8V / 2.95V	
46	UIM2_RESET_N	O	Reset output to an external UIM2 card	1.8V / 2.95V	
40	UIM2_PRESENT	I	UIM2 Card Present Detect	1.8V	Internal 100k PU Active HIGH
Miscellaneous Functions					
6	FULL_CARD_POWER_OFF_N	I	Module On/Off	1.8V / 3.3V	Internal 100k PD
8	W_DISABLE1_N	I	RF disable	1.8V / 3.3V	Internal 100k PU
10	LED_N	O	LED control		Open Drain
23	WAKE_ON_WAN_N	O	Wake Host	1.8V	
65	VREG_L6B_1P8	O	Reference Voltage	1.8V	Power
67	SYS_RESIN_N	I	Reset Input	1.8V	Internal 100k PU
68	TGPIO_01	I/O	General Purpose I/O Can be I2S_CLK	1.8V	
25	TGPIO_02	I/O	General Purpose I/O Can be DPR	1.8V	
62	TGPIO_03	I/O	General Purpose I/O	1.8V	
64	TGPIO_04	I/O	General Purpose I/O	1.8V	
20	USB_PCIE_SWITCH	I	Switch Host Interface	1.8V	Internal 10k PU
22	TGPIO_06	I/O	General Purpose I/O Can be I2S_DIN	1.8V	

Pin	Signal	I/O	Function	Type	Comment
24	TGPIO_07	I/O	General Purpose I/O Can be I2S_DOUT	1.8V	
28	TGPIO_08	I/O	General Purpose I/O Can be I2S_WS	1.8V	
56	I2C_SDA	I/O	I2C Data Can be TGPIO_09	1.8V	Internal 2.2k PU
58	I2C_SCL	I/O	I2C Clock Can be TGPIO_10	1.8V	Internal 2.2k PU
38	1PPS	O	1PPS/TSN	1.8V	
26	W_DISABLE2_N	I	GNSS disable	1.8V / 3.3V	Internal 100k PU
MIPI Control					
61	RFFE0_DATA	I/O	Data	1.8V	
63	RFFE0_CLK	O	Clock	1.8V	
Power Supply					
2	VPH_PWR	I	Power supply	Power	
4	VPH_PWR	I	Power supply	Power	
70	VPH_PWR	I	Power supply	Power	
72	VPH_PWR	I	Power supply	Power	
74	VPH_PWR	I	Power supply	Power	
GROUND					
3	GND	-	Ground	Ground	
5	GND	-	Ground	Ground	
11	GND	-	Ground	Ground	
27	GND	-	Ground	Ground	
33	GND	-	Ground	Ground	
39	GND	-	Ground	Ground	
45	GND	-	Ground	Ground	

Pin	Signal	I/O	Function	Type	Comment
51	GND	-	Ground	Ground	
57	GND	-	Ground	Ground	
71	GND	-	Ground	Ground	
73	GND	-	Ground	Ground	
Config					
21	CONFIG_0	-		Ground	
69	CONFIG_1	-		Ground	
75	CONFIG_2	-		Floating	
1	CONFIG_3	-		Floating	
Reserved for Future Use					
59	RFU				
60	RFU				

Warning: Reserved pins must not be connected.

4.2 FN920C04 Signals That Must be Connected

The below table specifies FN920C04 signals that must be connected for debugging purposes, even if not used by the end application:

Table 12: Mandatory Signals

Pin	Signal	Notes
2, 4, 70, 72, 74	VPH_PWR	
3, 5, 11, 27, 33, 39, 45, 51, 57, 71, 73	GND	
6	FULL_CARD_POWER_OFF_N	
7	USB_D+	If not used, connect to a test point or a USB connector
9	USB_D-	If not used, connect to a test point or a USB connector

4.3 Pin Layout

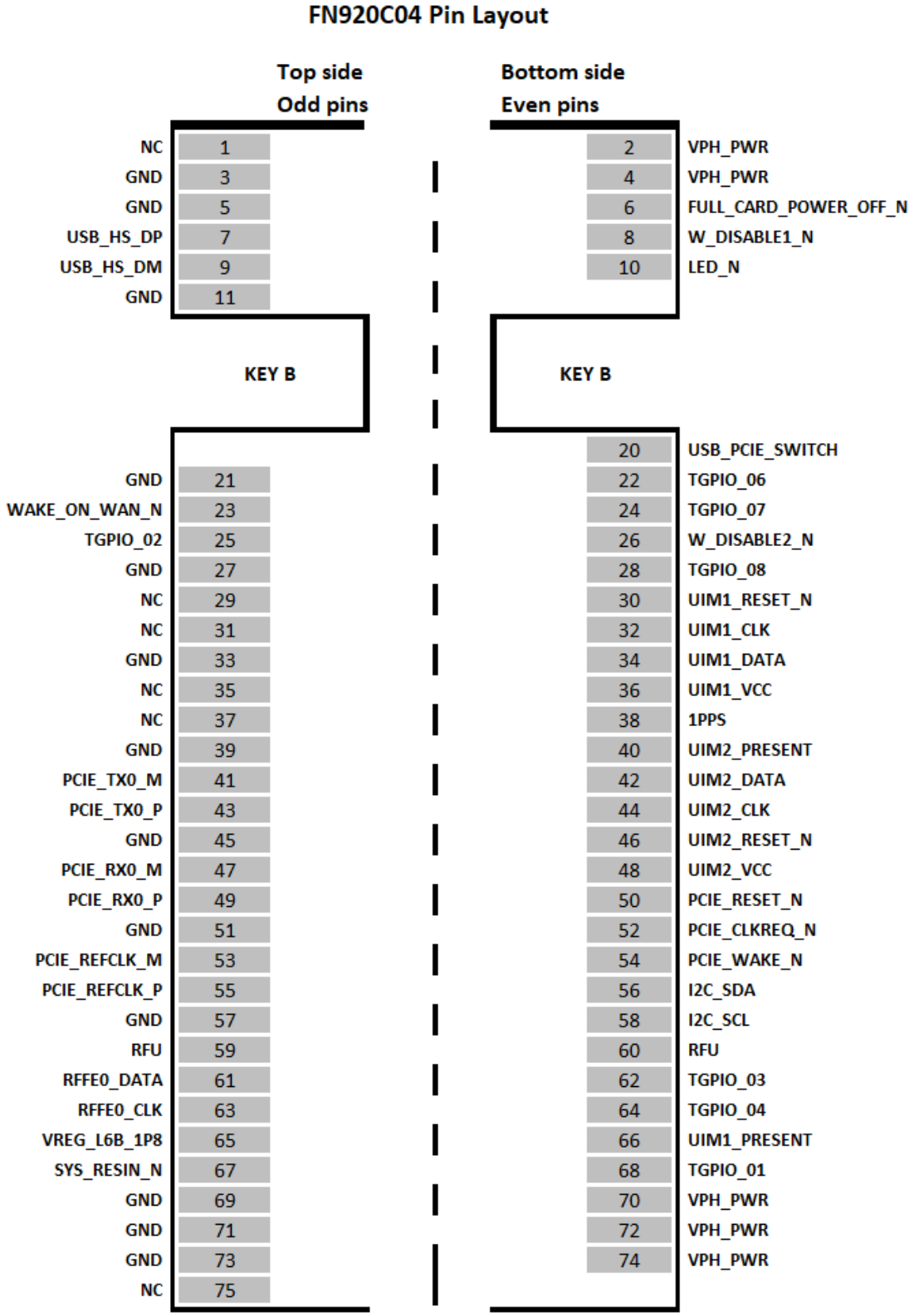


Figure 2: FN920C04 Pinout

5 Power Supply

The power supply circuitry and board layout are very important areas of the whole product design, with a critical impact on the overall product performance. Please follow carefully the following requirements and guidelines to ensure a reliable and stable design.

5.1 Power Supply Requirements

The FN920C04 power requirements are as follows:

Table 13: Power Supply Requirements

Power Supply	Value
Nominal Supply Voltage	3.3V
Supply Voltage Range	3.135 V – 4.4 V
Maximum ripple on the module input supply	30 mV
Peak current consumption*	3.3 V @ 2.5 A

* The peak current must be higher than the average current due to TDD bands and power class 2.

5.2 Power Consumption

Table 14: FN920C04 Current Consumption

Mode	Average [Typ.]	Mode Description
IDLE Mode		
CFUN=1	105 mA	No call connection USB is connected to a host
Airplane Mode (PSMWDISACFG=1, W_DISABLE1_N: Low)		
CFUN=4	3.78 mA	Tx and Rx are disabled; the module is not registered on the network (Airplane mode) USB is disconnected
Standby Mode (PSMWDISACFG=1, W_DISABLE1_N: Low)		
CFUN=1	4.095 mA	Module cycles between wake and sleep USB is disconnected
Operative Mode (LTE)		
Max power	1400 mA	BW 10 MHz, RB=12, QPSK, Tx=Max power, GPS OFF
0 dBm	180 mA	BW 10 MHz, RB=12, QPSK, Tx=0dBm, GPS OFF
Operative SA Mode (NR-FR1)		
Max power	1250 mA	BW 10 MHz, RB=12, SCS 15 (DFT-s-OFDM/QPSK)
0 dBm	168 mA	BW 10 MHz, RB=12, SCS 15 (FDD)/DFT-s-OFDM/QPSK
GNSS Mode (LTE idle)		
Acquisition	126 mA	L1+L5 All Constellations (USB Active)
Tracking	141.75 mA	L1+L5 All Constellations (USB Active)

Note: Worst/best-case current consumption values depend on mobile network configuration – not under module control.

5.3 General Design Rule

The principal guidelines for the Power Supply Design include three different design steps:

- Electrical design
- Thermal design
- PCB layout

5.3.1 Electrical Design Guidelines

The electrical design of the power supply is highly dependent on the power source from which the power is drained.

5.3.1.1 +5v Source Power Supply Design Guidelines

- The desired power supply voltage output is 3.3V. Being the difference between the input source and the desired output moderate, a linear regulator can be used. A switching power supply is preferred to reduce power dissipation.
- When using a linear regulator, a proper heat sink must be provided to dissipate the power generated.
- A low ESR bypass capacitor of adequate capacity must be provided to cut the current absorption peaks close to the FN920C04 module. A 100 µF tantalum capacitor is usually suitable on VPH_PWR.
- Make sure that the low ESR capacitor on the power supply output (usually a tantalum one) is rated at least 10V.
- A protection diode must be inserted close to the modem power input to protect the FN920C04 module from power polarity inversion.

5.3.2 Thermal Design Guidelines

This section provides thermal design guidelines useful for developing a product with a Telit FN920C04 modem.

Proper thermal protection design protects against human or component damage under worst-case conditions.

Furthermore, it reduces the probability of failure and does not adversely affect the use of the module, and greatly extends the operation time with maximum performance.

For more details, please refer to the thermal design guidelines.

Note: FN920C04 supports different RATs: 4G and 5G Sub-6.

Note: Based on the Radio Access Technology, the FN920C04 modem might exhibit high current consumption, thus proper thermal designs are essential to dissipate heat well.

Note: There is a large solder-resist opening area on the bottom side of the module. Adding a TIM on that area with the solder-resist opening area is highly recommended to ensure proper heat dissipation.

The modem temperature value can be read via AT command.

Note: For optimal RF performance, thermal dissipation, and mechanical stability, the FN920C04 must be connected to the ground and metal chassis of the host board.

It is recommended that between the shield cover of the module and the heatsink or the metal chassis of the host device use a TIM for better heat dissipation.

5.3.3 Power Supply PCB Layout Guidelines

As described in the electrical design guidelines, a low ESR capacitor should be connected to the power supply output to reduce current peaks. A protection diode on the modem power supply input should be connected to protect the FN920C04 from spikes and polarity inversion.

Placement of these components is crucial for correct operation: a misplaced component can badly affect power supply performance:

- The bypass low ESR capacitor must be placed close to the FN920C04 power input pins or - if the power supply is of a switching type - it can be placed close to the inductor to reduce ripple, as long as the PCB trace from the capacitor to FN920C04 is wide enough to avoid significant voltage drop even during the 2.5A current peaks.
- The protection diode must be placed close to the modem connector.
- The PCB traces from the input connector to the power regulator IC must be wide enough to ensure that no voltage drops occur during the 2.5A current peaks.
- The PCB traces connecting the FN920C04 and bypass capacitor must be wide enough to avoid voltage drops even when 2.5A current absorption peaks occur. These traces should be kept as short as possible.

- The PCB traces connecting the switching output to the inductor and the switching diode must be kept as short as possible by placing the inductor and the diode as close as possible to the power switching IC (only for the switching power supplies). This is done to reduce the radiated field (noise) at the switching frequency (usually 100-500 kHz).
- Use a good common ground plane.
- Place the power supply on the board to ensure that the high current return paths in the ground plane do not overlap any noise-sensitive circuitry, such as microphone amplifier/buffer or earphone amplifier.
- The power supply input cables must be kept separate from noise-sensitive lines, such as microphone/earphone cables.

5.4 RTC

The RTC within the FN920C04 module does not have a dedicated RTC supply pin. The RTC block is supplied by the VPH_PWR supply.

If the VPH_PWR power is removed, RTC is not maintained so if it is necessary to maintain an internal RTC, VPH_PWR must be supplied continuously.

5.5 Reference Voltage

The 1.8V regulated power supply output is supplied as the reference voltage to a host board. This output is active when the module is turned ON and turns OFF when the module is shut down.

The table below lists the reference voltage of the FN920C04 modules.

Table 15: FN920C04 Reference Voltage

Pin	Signal	I/O	Function	Type	Comment
65	VREG_L6B_1P8	O	Reference Voltage	Power	1.8V

5.6 Internal LDO for GNSS Bias

The LDO for GNSS bias is applied inside the FN920C04 model.

The voltage supply is generated internally by the FN920C04 (LDO) and is fed to GNSS active antenna.

The table below lists the LDO for GNSS bias of FN920C04 modules.

Table 16: LDO for GNSS bias of FN920C04

Symbol	Parameter	Min	Typ	Max	Unit
$V_{GNSS\ DC\ bias}$	Voltage of internal LDO for GNSS bias	2.9	3.1	3.15	[V]
$I_{GNSS\ DC\ bias}$	Current of internal LDO for GNSS bias	-	-	100	[mA]

6 Electrical Specifications

6.1 Absolute Maximum Ratings – Not Optional

No functionality is guaranteed outside the following operating specifications.

Warning: A deviation from the value ranges listed below could damage the FN920C04 module.

Table 17: Absolute Maximum Ratings - Not Optional

Symbol	Parameter	Min	Max	Unit
VPH_PWR	Battery supply voltage on pin VPH_PWR	-0.3	+4.7	[V]

6.2 Recommended Operating Conditions

Table 18: Recommended Operating Conditions

Symbol	Parameter	Min	Typ	Max	Unit
T_{amb}	Ambient temperature	-40	+25	+85	[degC]
VPH_PWR	Battery supply voltage on pin VPH_PWR	3.135	3.3	4.4	[V]
I_{VPH_PWR}	Peak current on pin VPH_PWR	-	-	2.5	[A]

7 Digital Section

7.1 Logic Levels

Unless otherwise specified, all FN920C04 interface circuits are 1.8V CMOS logic.

Only USIM interfaces are capable of dual voltage I/O.

The following tables show the logic level specifications used in the FN920C04 interface circuits. The data specified in the tables below are valid throughout all drive strengths and the whole temperature range.

Warning: Do not connect FN920C04 digital logic signal directly to the application digital logic signals with a voltage higher than 2.134V for 1.8V CMOS signals.

7.1.1 1.8V Standard GPIOs

Table 19: Operating Range - Interface Levels (1.8V CMOS)

Parameter		Min	Max	Unit	Comment
V_{IH}	Input high level	1.26	2.1	[V]	
V_{IL}	Input low level	-0.3	0.36	[V]	
V_{OH}	Output high level	1.44	1.8	[V]	
V_{OL}	Output low level	0	0.4	[V]	

7.1.2 1.8V UIM1/UIM2 Pins

Table 20: Operating Range - UIM Pins Working at 1.8V

Parameter		Min	Max	Unit	Comment
V_{IH}	Input high level	1.17	2.1	[V]	
V_{IL}	Input low level	-0.3	0.36	[V]	
V_{OH}	Output high level	1.44	1.8	[V]	
V_{OL}	Output low level	0	0.45	[V]	

7.1.3 2.95V Pins – Absolute Maximum Ratings

Table 21: Absolute Maximum Ratings - Not Functional

Parameter	Min	Max
Input level on any digital pin when on	-	+3.344 V
Input voltage on analog pins when on	-	+3.344 V

7.1.4 2.95V SIM Card Pins

7.1.5 2.95V SIM Card Pins

Table 22: Operating Range - UIM Pins Working at 2.95V

Parameter		Min	Max	Unit	Comment
V_{IH}	Input high level	2.065	3.25	[V]	
V_{IL}	Input low level	-0.3	0.59	[V]	
V_{OH}	Output high level	2.36	2.95	[V]	
V_{OL}	Output low level	0	0.4	[V]	

7.1.6 VPH_PWR Level I/O Pins

Table 23: Operating Range - I/O Pins Working at VPH_PWR

Parameter		Min	Max	Unit
V_{IH}	Input high level	0.65 x VPH_PWR	VPH_PWR + 0.3	[V]
V_{IL}	Input low level	-0.3	0.35 x VPH_PWR	[V]
V_{OH}	Output high level	VPH_PWR – 0.45	VPH_PWR	[V]
V_{OL}	Output low level	0	0.45	[V]

7.2 Power ON/OFF/RESET

The following tables show the description of power control pins.

Table 24: Power Interface Signals

Pin	Signal	I/O	Function	Type	Comment
6	FULL_CARD_POWER_OFF_N	I	Module On/Off	1.8V / VPH_PWR	Internal 100k PD
67	SYS_RESIN_N	I	Reset Input	1.8V	Internal 100k PU
65	VREG_L6B_1P8	O	Reference Voltage	1.8V	Power
*	BOOT_OK / Shutdown Indicator	O	Power ON/OFF Status Check	1.8V	* Can be assigned to GPIO

7.2.1 Power On

To turn on the FN920C04 data card, the FULL_CARD_POWER_OFF_N pin must be asserted high.

Note: To turn on the FN920C04 module, the SYS_RESIN_N pin must not be asserted low. If asserted low for more than one second, the FN920C04 modem will be reset.

Even so, please control the FN920C04 ON/OFF status using the FULL_CARD_POWER_OFF_N pin.

7.2.1.1 Initialization and Activation State

After turning on the FN920C04 module, the device is not yet fully functional because the software boot and initialization process takes some time to complete. For this reason, it is not recommended to start communicating with the FN920C04 module during the initialization phase.

The AT command interface is accessible via USB or PCIe port. In general, as shown in the figure below, the FN920C04 modems become fully operational (in the Activation state) at least 50 seconds after the FULL_CARD_POWER_OFF_N is asserted.

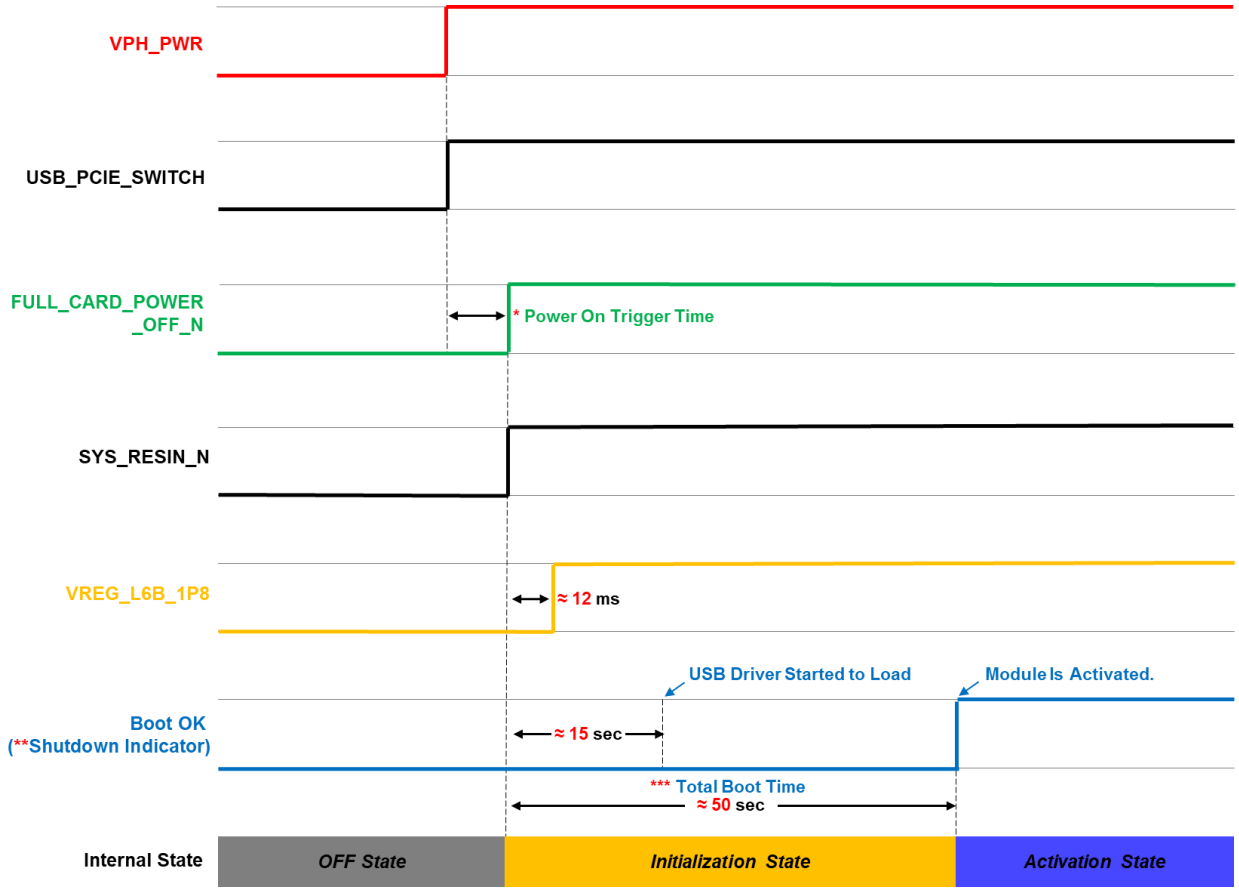


Figure 3: FN920C04 Power ON Sequence - USB mode (USB_PCIE_SWITCH: High, Default) – [TBD]

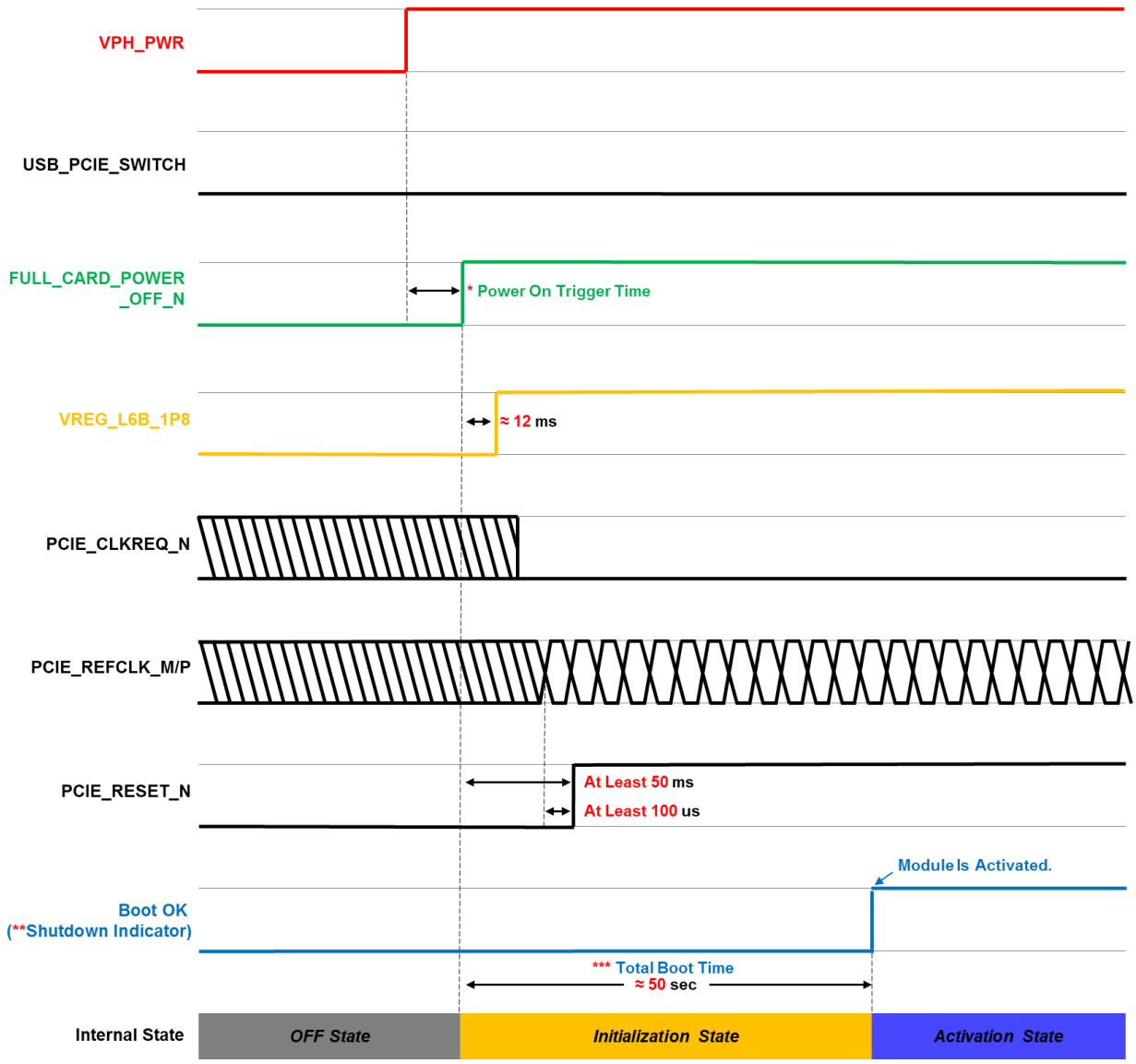


Figure 4: FN920C04 Power ON Sequence – PCIe EP mode (USB_PCIE_SWITCH: Low) - [TBD]

Note: To verify if the FN920C04 has powered up properly, please follow the indications below:

* Power on trigger time is the interval between VPH_PWR to FULL_CARD_POWER_OFF_N: this could be null (0 ms) if the customer application requires turning on the module automatically.

** Monitoring BOOT_OK (Shutdown indicator) pin. When the status translates to high, the module boot-up process is complete. To use BOOT_OK (Shutdown indicator), the shutdown indication function must be enabled through the AT#SHDNIND command. (please refer to the AT Reference Guide document)

*** The stated total boot time is an approximate measure of the latest SW and HW configuration. The boot time may be lengthened or shortened depending on the module configuration, firmware, or hardware version.

Note: Active low signals are labeled with a name ending with "_N".

Note: To avoid a back-powering effect, it is recommended to prevent any HIGH logic level signals from being applied to the digital pins of the module when it is powered OFF or during an ON/OFF transition.

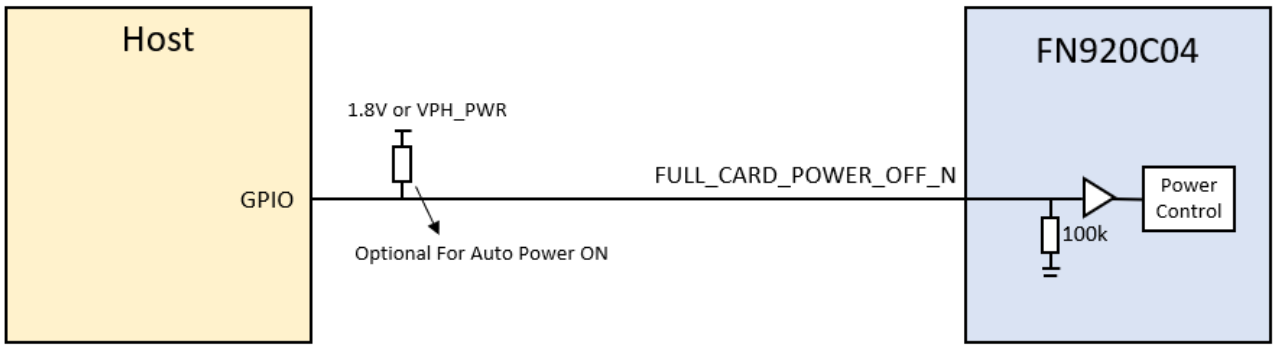


Figure 5: Example Circuit for ON/OFF by FULL_CARD_POWER_N

7.2.2 Power Off

Power off of the device can be done in two different ways:

- Fast shutdown by GPIO triggered

7.2.2.1 Unconditional Shutdown

To shutdown the FN920C04 module safely, the host can use the unconditional shutdown function.

The unconditional shutdown can be triggered by:

- FULL_CARD_POWER_OFF_N

7.2.2.2 Unconditional Shutdown by FULL_CARD_OFF_N

To unconditional shutdown the FN920C04, FULL_CARD_POWER_OFF_N should be asserted to Low.

Once FULL_CARD_POWER_N is asserted to Low, the FN920C04 enters the finalization state, terminates active processes, and prepares to turn off safely.

As shown in the diagram below, VREG_L6B_1P8 will indicate when the module is ready to be turned off.

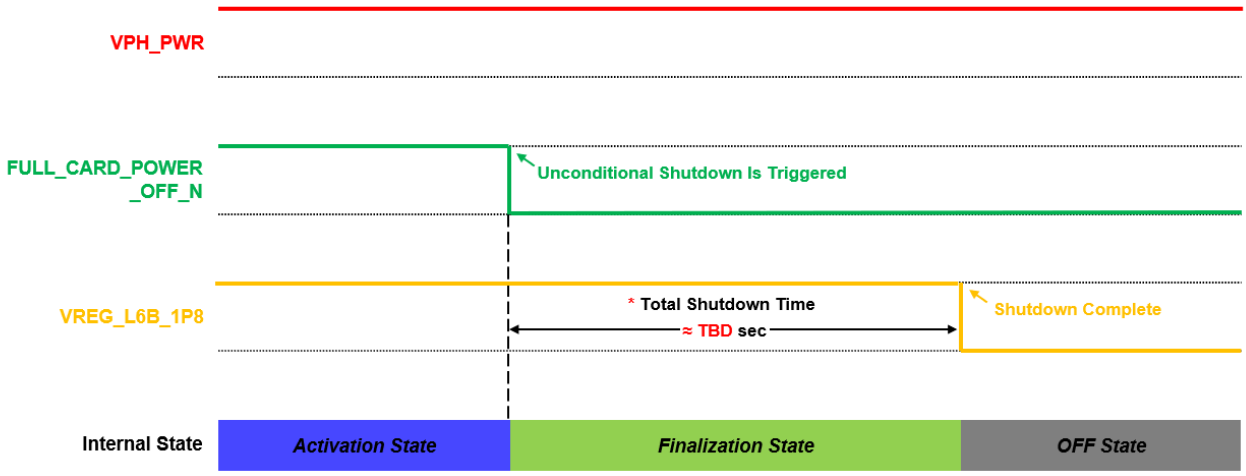


Figure 6: Unconditional Shutdown by FULL_CARD_POWER_OFF_N - [TBD]

Note: Unconditional Shutdown triggered by FULL_CARD_POWER_OFF_N is only effective after the module boots up completely.

* The stated total shutdown time is an approximate measure of the latest SW and HW combination. The shutdown time may be lengthened or shortened depending on the SW configuration, SW, or HW version.

7.2.2.3 Fast Shutdown

For quicker FN920C04 module shutdown, the host application can use the fast shutdown function, which can be triggered by:

- GPIO (+ optional shutdown indicator)

7.2.2.4 Fast Shutdown by GPIO

To leverage the fast shutdown feature, one of the GPIO lines should be assigned as Fast Shutdown Trigger using the AT commands.

Once the fast shutdown trigger senses a High to Low transition, the fast shutdown is started: the FN920C04 enters a finalization state, terminates active processes, and prepares to turn off safely. As shown in the figure below, when the module is ready to be turned off, it will be indicated via VREG_L6B_1P8.

Please refer to the AT User Guide for more details on how to enable the shutdown indicator and fast shutdown trigger.

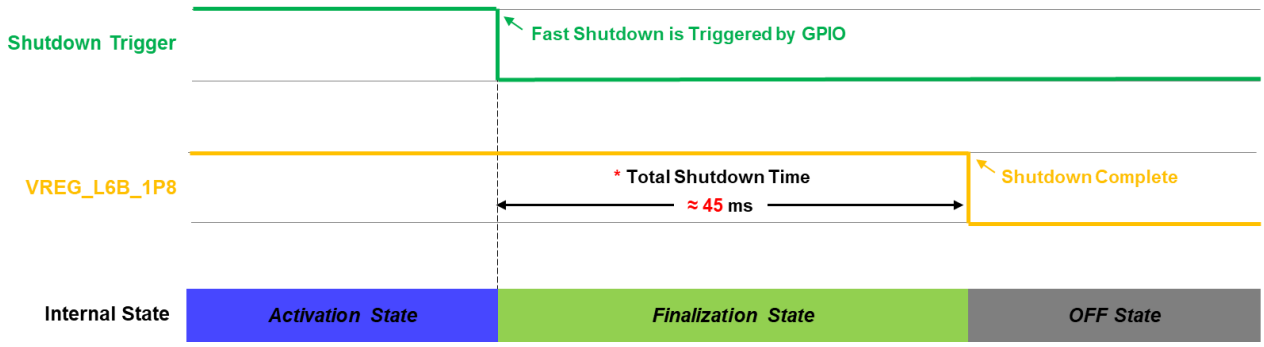


Figure 7: Fast Shutdown by GPIO - [TBD]

Note: Using a fast shutdown without a shutdown indicator function, the FULL_CARD_POWER_N pin should be controlled to prevent the FN920C04 from rebooting.

For more information, please refer to the AT commands reference guide and SW user guide document.

* The stated total shutdown time is an approximate measure of the latest SW and HW combination. The shutdown time may be lengthened or shortened depending on the SW configuration, SW, or HW version.

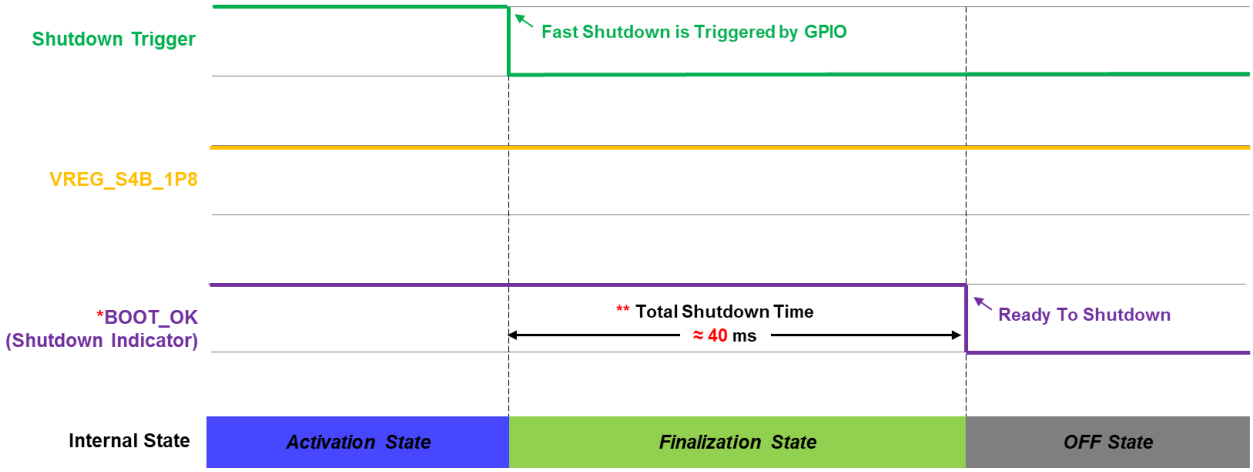


Figure 8: Fast Shutdown by GPIO (*SHDNIND Enable, Optional) - [TBD]

Warning: If the VPH_PWR is to be kept at a high status, the module will reboot. (Not applicable to the Shutdown Indicator function).

Warning: Failure to follow recommended shut-down procedures might damage the device and consequently void the warranty.

Note: *Shutdown Indicator is an optional function and is disabled by default. The host can verify the module entered the OFF state by monitoring the shutdown indicator pin status. To turn on the

module after using a fast shutdown with a shutdown indicator function, it should be re-powered or rebooted.

For more information, please refer to AT Commands Reference Guide and SW User Guide document.

Note: Fast shutdown function is disabled by default. To use the fast shutdown function, please refer to the AT Commands Reference Guide and SW User Guide document.

7.2.3 Reset

Device reset can be achieved as follows:

- Unconditional warm reset using the SYS_RESIN_N

7.2.3.1 Unconditional Warm Reset

To unconditionally warm restart the FN920C04 module, the SYS_RESIN_N pin must be asserted low for more than 1 second and then released.

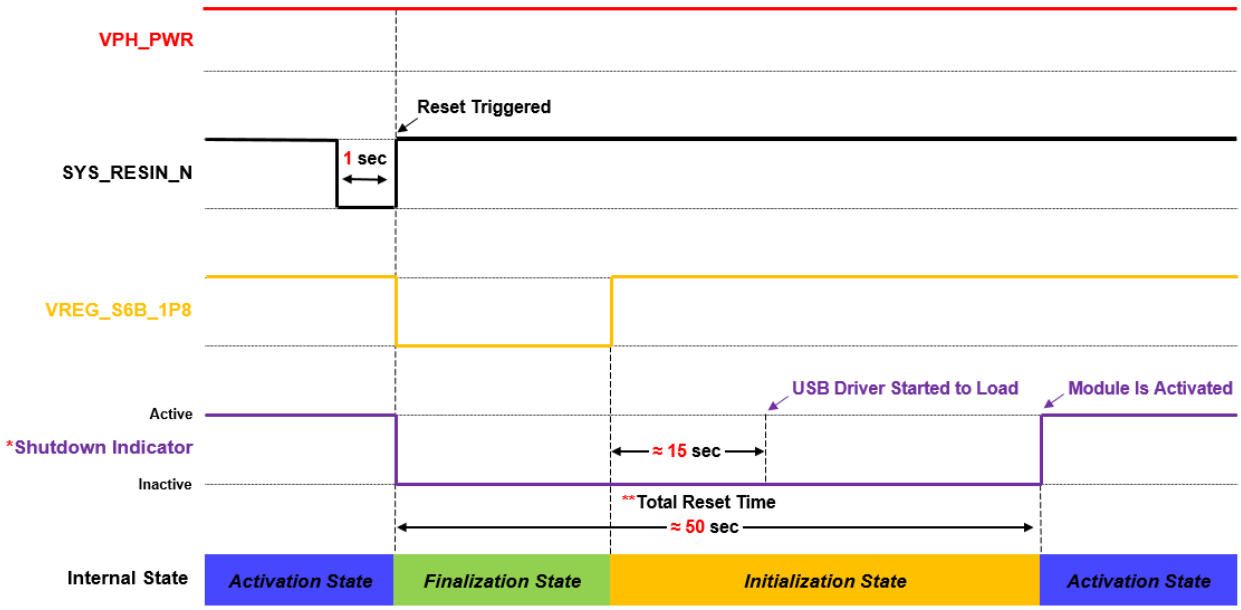


Figure 9: Unconditional Warm Reset by SYS_RESIN_N - [TBD]

Note: *Shutdown Indicator is an optional function. If SHDIND is enabled, it can verify the status via the SHDIND function.

Please refer to the AT commands user guide document.

** The stated total reset time is an approximate measure of the latest SW and HW combination. The shutdown time may be lengthened or shortened depending on the SW configuration, SW, or HW version.

Warning: If an unconditional warm reset is to be performed on the FN920C04, the crash dump extraction mode must not be set.

Otherwise, the device will enter crash dump mode.

Note: Unconditional warm reset must be used only as an emergency procedure, and not as a normal power-off operation.

Warning: Be aware that when the RESET# signal is triggered the modem is disconnected form the network, the system drivers are removed, and all data in the module is lost.

@ Low (0 V); The module enters in reset mode and only boots when the signal is high again.

Note: Do not use any pull-up resistor on the RESET_N line or any totem pole digital output. Using a pull-up resistor may cause latch-up problems on the FN920C04 power regulator and improper functioning on the module.

The RESET_N line must be connected only in an open-collector configuration.

The below figure shows a simple circuit for this action.

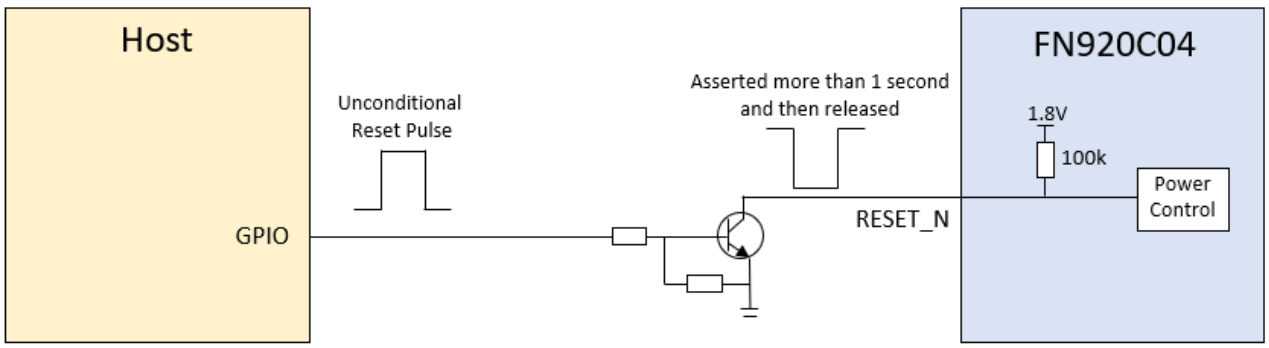


Figure 10: Example Circuit for RESET by SYSTEM_RESET_N

7.3 Communication Ports

The below table summarizes all the hardware interfaces of the FN920C04 modules.

Table 25: FN920C04 Hardware Interfaces

Interface	Description
PCIe	Peripheral Component Interconnect Express Gen 2.0
USB	USB 2.0 interface
USIM	x2 dual voltage each (1.8V / 2.95V)
eSIM	Embedded SIM (optional)
I2C	Inter-Integrated Circuit
I2S	Inter-IC Sound
Control Interfaces	W_DISABLE_N, WAKE_ON_WAN_N, LED, DPR
Antenna ports	x2 Cellular, 1 for GNSS

7.3.1 Host Interface

Note: FN920C04 M.2 data card supports USB HS 2.0 and PCIe Gen 2 respectively.

7.3.1.1 Host Interface Switch Function

This section describes the host interface switch functions.

Note: The meaning of the name USB/PCIe switch implies which interface provides the main function.

Please refer to the 1VV0301882, FN920C04 Software User Guide, regarding each function.

Table 26:Host Interface Switch Pin

Pin	Signal	I/O	Function	Type	Comment
20	USB_PCIE_SWITCH	I	Switch Host Interface	1.8V	Internal 10k PU

Table 27: USB/PCIe Switch Pin

USB/PCIe Switch	Mode	USB	PCIe EP	PCIe RC
Open (Internal Pull-Up)	USB	Available	Not support	Available
Low	PCIe	Available	Available	Not support

FN920C04 M.2 Card determines the host interface by checking the status of the USB_PCIE_SWITCH pin at the beginning of the power-on sequences.

Note: When using PCIe EP, PCIe RC cannot be used, and basic functions such as mobile data and AT commands are provided through PCIe EP, while USB is provided only for debugging and specific purpose.

For more details, please refer to the 1VV0301882, FN920C04 Software User Guide.

Note: FN920C04 supports the following devices as EP for data interface:

- Qualcomm WCN6856 WLAN / BT
- Qualcomm QCA6174A-1 WLAN

Please consult Telit if a different EP device is used as a data interface since the EP device kernel driver needs to be modified to use the FN920C04 network hardware accelerator.

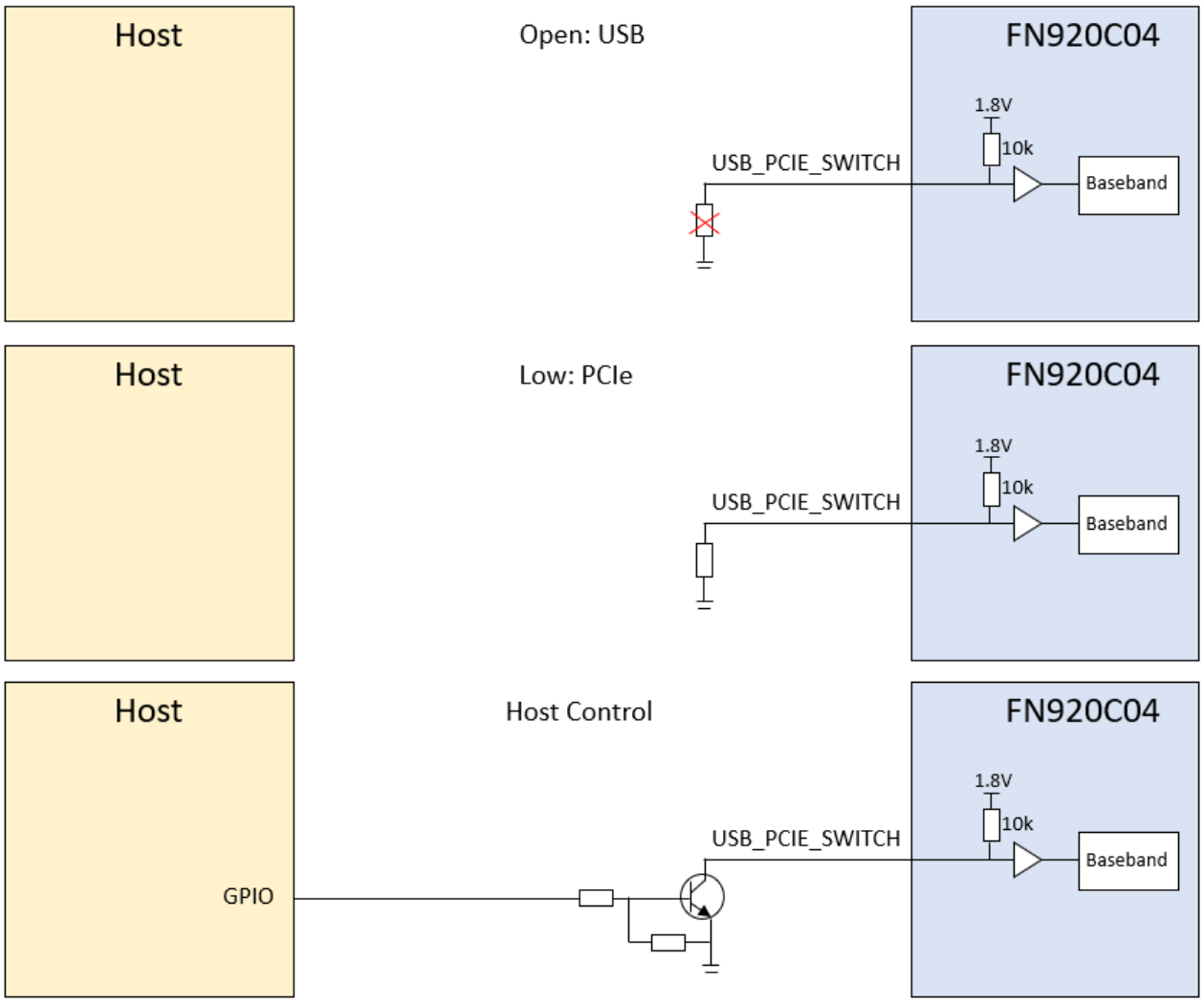


Figure 11: Example Circuit for HOST Interface Switch Function

7.3.1.2 PCIe Interface

The FN920C04 module includes a PCIe interface. PCIe needs AC coupling series capacitors on the TX lines in both directions. To interface PCIe with the application board that controls the modem, 0.22 uF capacitors should be installed on PCIE_RX_P/M lines of the FN920C04. The series capacitors are already placed on PCIE_TX_P/M lines inside the FN920C04.

Internally, the VPH_PWR level 10k pull-up resistor is already mounted on PCIE_WAKE_N, PCIE_CLKREQ_N, and PCIE_RESET_N.

The suggested PCIe interface connection is shown in the diagram below:

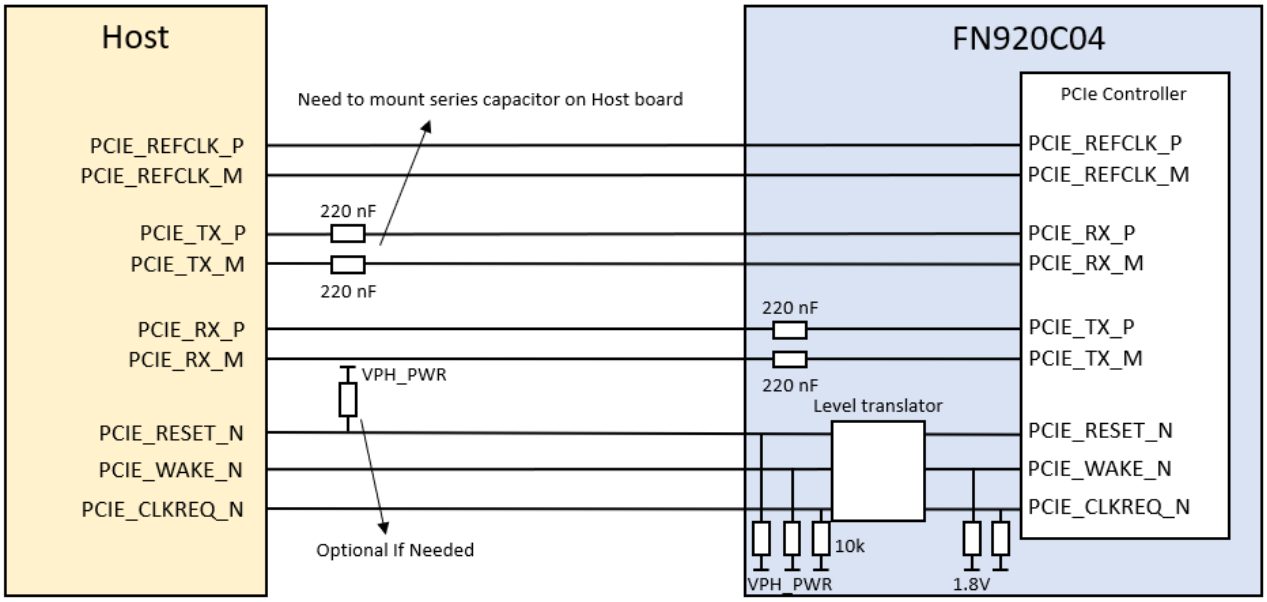


Figure 12: Connection for PCIe Interface

Note: FN920C04 supports PCIe root complex (RC) mode.

Depending on the EP device such as NIC or something else, may require a suitable value of pull-up resistor on the PCIE_RESET_N line.

Customers interested in using PCIe RC mode can contact Telit Technical Support at:

- TS-EMEA@telit.com
- TS-AMERICAS@telit.com
- TS-APAC@telit.com

Note: The PCIe signals traces must be routed carefully: minimize trace lengths, number of vias, and capacitive loading. The impedance value should be as close as possible to the 85 Ohm differential.

Table 28: PCIe Interface Signals

Pin	Signal	I/O	Function	Type	Comment
41	PCIE_TX0_M	O	PCIe transmit 0 – Minus	Analog	
43	PCIE_TX0_P	O	PCIe transmit 0 – Plus	Analog	
47	PCIE_RX0_M	I	PCIe receive 0 – Minus	Analog	
49	PCIE_RX0_P	I	PCIe receive 0 – Plus	Analog	
53	PCIE_REFCLK_M	I	PCIe differential reference clock – Minus	Analog	
55	PCIE_REFCLK_P	I	PCIe differential reference clock – Plus	Analog	
50	PCIE_RESET_N	I	Functional reset to PCIe bus	VPH_PWR	Internal 10k PU
52	PCIE_CLKREQ_N	O	PCIe reference clock request signal	VPH_PWR	Internal 10k PU
54	PCIE_WAKE_N	O	PCIe wake-up	VPH_PWR	Internal 10k PU

Warning: In the case of EP mode, FN920C04 data cards are not designed or intended to support Hot-Swap or Hot-Plug connection. Performing How-Swap or Hot-Plug may pose a danger to the FN920C04 module, to the host device, and to the person handling the device.

Note: PCIE_RESET_N operates as digital input in PCIe end point mode and open drain in PCIe root complex mode. PCIE_CLKREQ_N and PCIE_WAKE_N operate as an open drain in PCIe end point mode, and digital input in PCIe root complex mode.

The default of the FN920C04 is end point mode.

Note: Consider placing a low-capacitance ESD protection component to protect the FN920C04 against ESD spikes.

7.3.1.3 PCIe Layout Guidelines

The below guidelines will provide general guidelines for the PCIe interface to improve signal integrity.

- All other sensitive/high-speed signals and circuits must be protected from PCIe corruption.
- PCIe signals must be protected from noisy signals (clocks, SMPS, and so forth).
- Pay extra attention to crosstalk, ISI, and intra-lane skew and impedance discontinuities.

- PCIe Tx AC coupling capacitors are better placed close to the source or receiver side to keep good SI of the route on the PCB.
- To maintain impedance balance, maintain positive and negative traces as balanced as possible in terms of the signal and its return path.
- Trace length matching between the reference clock, Tx, and Rx pairs is not required.
- External capacitors also should keep differential traces. Ensure not to stagger the capacitors. This can affect the differential integrity of the design and can create EMI.

Table 29: PCIe Routing Constraints

Type of guidance	Guideline	Requirement
General	Data rate	5 Gbps*
	Insertion loss at 2.5 GHz (dB)	-6 dB
	Impedance	85 ohms differential
	Bus length	TBD
Length matching	Intra pair match	< 0.7mm
Spacing	To all other signals	> 4 x line width
	Tx lane to Rx lane	> 3 x line width
Component	AC capacitance	220 nF

***Actual throughput at the system level could be lower due to overheads.**

****PCIe trace length in FN920C04: Unknown**

7.3.1.4 USB HS 2.0 Interface

The FN920C04 modules include a Universal Serial Bus (USB) transceiver, which operates at USB high-speed (480Mbits/sec). It can also operate with USB full-speed hosts (12Mbits/sec).

It is comliant with the USB 2.0 specification and can be used for control and data transfer as well as for diagnostic monitoring and firmware update.

Note: The USB D+ and USB D- signal have a clock rate of 480 MHz.

The signal traces must be carefully. Minimize trace lengths, number of vias, and capacitive loading. The impedance value should be as close as possible to the 90 Ohm differential.

Note: Consider placing a low-capacitance ESD protection component to protect FN920C04 against ESD strikes.

7.3.1.5 USB OTG Feature

FN920C04 supports USB On-The-Go (OTG) function.

Note: Since the FN920C04 does not support a USB_ID pin, a TPGIO pin should be set as the purpose of USB_ID using the #OTGCFG command. The module will be in host mode if the TGPIO pin is connected to the ground. External 5 V power is required on an application board to supply 5 V power to the OTG device because the FN920C04 does not supply power for the OTG device.

7.3.1.6 USB Layout Guidelines

- If third-party components are required for signal improvement, place them closer to the USB connector.
- There are relatively fast edge rates, so must be routed away from sensitive circuits and signals (RF, audio, and XO).
- Maintain good isolation between the USB connector and RF antennas (especially 2.4 GHz).
- Route differential pairs in the inner layers with a solid GND reference to have good impedance control and to minimize discontinuities.
- Keep isolation between the DP/DM to avoid crosstalk.
- For USB 2.0 signal, the maximum trace length should be less than 210 mm.
-

Table 30:USB Routing Constraints

Type of guidance	Guideline	Requirement
		USB 2.0
General	Data rate	480Mbps
	Impedance	90 ohms differential
	Bus length	TBD mm
Length matching	Intra pair match	< 2mm
Spacing	To all other signals	> 3 x line width

7.3.2 SIM Interface

The FN920C04 modem family supports an external SIM interface. (1.8 V or 2.95 V)

Note: UIM2 can be assigned as an optional eSIM. In that case, UIM2 can't be used as an external SIM interface.

Table 31: SIM Interface Signals

Pin	Signal	I/O	Function	Type	Comment
SIM Card Interface 1					
36	UIM1_VCC	O	Supply output for an external UIM1 card	1.8V / 2.95V	Power
34	UIM1_DATA	I/O	Data connection with an external UIM1 card	1.8V / 2.95V	Internal 20k PU
32	UIM1_CLK	O	Clock output to an external UIM1 card	1.8V / 2.95V	
30	UIM1_RESET_N	O	Reset output to an external UIM1 card	1.8V / 2.95V	
66	UIM1_PRESENT	I	UIM1 Card Present Detect	1.8V	Internal 100k PU Active High*
SIM Card Interface 2					
48	UIM2_VCC	O	Supply output for an external UIM2 card	1.8V / 2.95V	Power
42	UIM2_DATA	I/O	Data connection with an external UIM2 card	1.8V / 2.95V	Internal PU
44	UIM2_CLK	O	Clock output to an external UIM2 card	1.8V / 2.95V	
46	UIM2_RESET_N	O	Reset output to an external UIM2 card	1.8V / 2.95V	
40	UIM2_PRESENT	I	UIM2 Card Present Detect	1.8V	Internal 100k PU Active High*

Note: *Default UIM_PRESENT pin setting is high to comply with the M.2 specification standard.

Pin settings can be changed through AT#SIMINCFG, please refer to the AT commands reference guide for details.

If you have any special requirements, please contact Technical Support or Sales.

7.3.2.1 SIM Schematic Example

The diagram below shows in particular how the SIM part of the application interface should be designed.

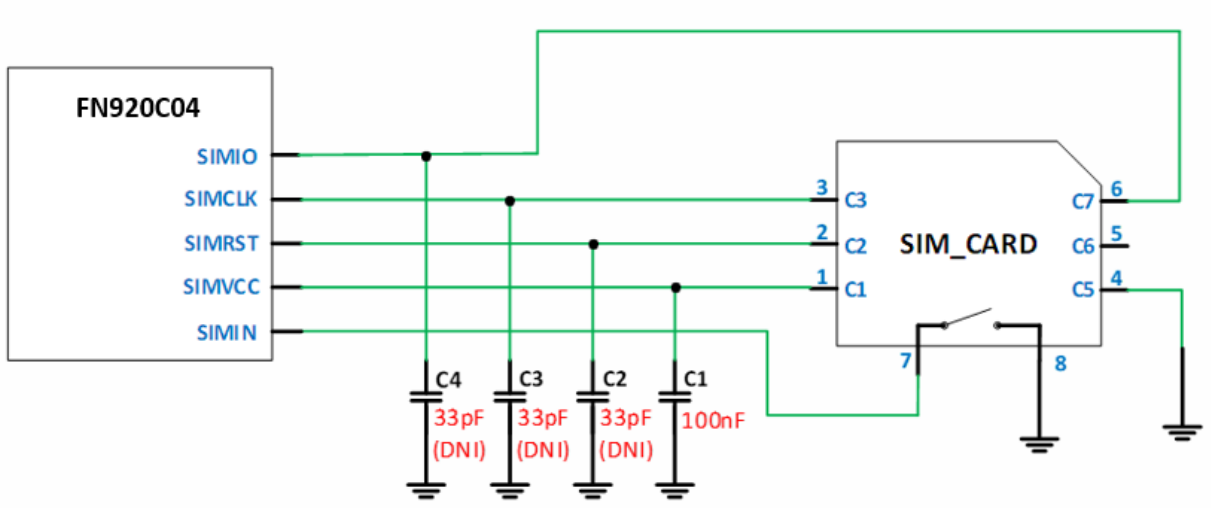


Figure 13: SIM Schematic Example

Note: FN920C04 modems contain an internal pull-up resistor on SIMIO. It is not necessary to install an external pull-up resistor.

7.3.3 eSIM Interface

FN920C04 modems include pads for an optional embedded SIM.

Customers interested in using an embedded SIM mounted on the FN920C04 can contact Telit Technical Support at:

- TS-EMEA@telit.com
- TS-AMERICAS@telit.com
- TS-APAC@telit.com

7.3.4 I2C – Inter-integrated Circuit

The FN920C04 supports an I2C interface: the table below lists the I2C signals of the modem.

Table 32: I2C Signal

Pin	Signal	I/O	Function	Type	Comment
56	I2C_SDA	I/O	I2C Data Can be TGPIO_09	1.8V	Internal 2.2k PU
58	I2C_SCL	I/O	I2C Clock Can be TGPIO_10	1.8V	Internal 2.2k PU

7.3.5 Control Interface

Table 33: Control Interface Pins

Pin	Signal	I/O	Function	Type	Comment
8	W_DISABLE_N	I	WLAN disable	1.8 / 3.3 V	Internal 100k PU
26	W_DISABLE2_N	I	GNSS disable	1.8 / 3.3 V	Internal 100k PU
10	LED_N	O	LED control		Open Drain
23	WAKE_ON_WAN_N	O	Wake Host	1.8V	
25	TGPIO_02	I/O	General Purpose I/O Can be DPR	1.8V	

7.3.5.1 WLAN/GNSS Disable

The W_DISABLE_N signal is provided to disable the WLAN/GNSS function:

- W_DISABLE_N

Low: Airplane mode

High or Floating: Normal operation

- W_DISABLE2_N

Low: GNSS Disable

High or Floating: Normal operation

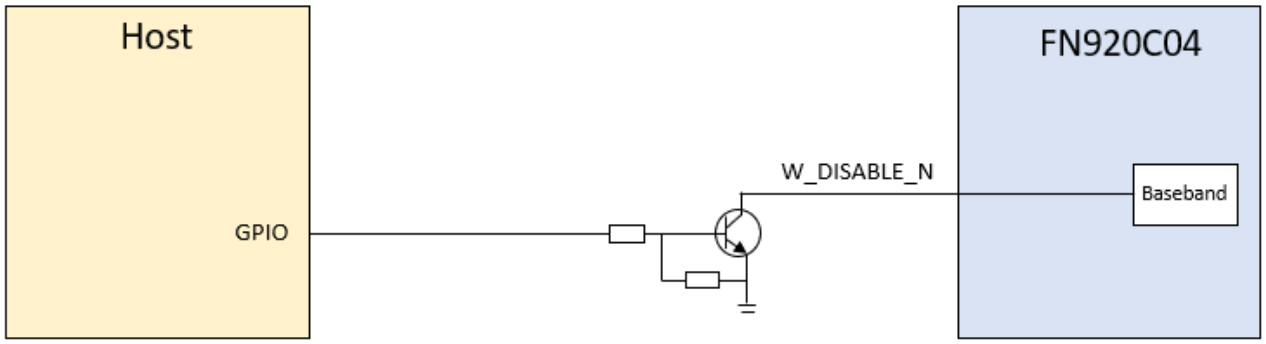


Figure 14: Example Circuit for WLAN/GNSS Disable Function

Please refer to the AT commands guide for setting the WLAN/GNSS disable function.

7.3.5.2 LED

The LED signal drives the LED output. The recommended LED connection is the following:

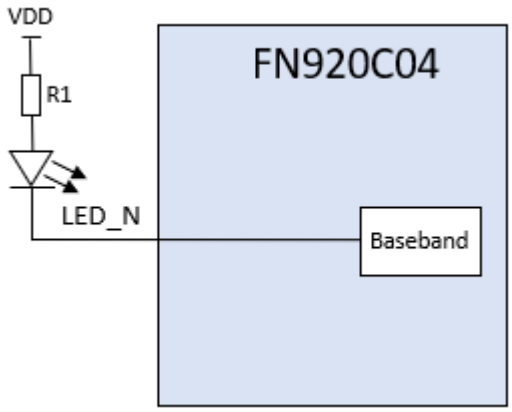


Figure 15: Recommended LED Connection

R1 and VDD determine the LED brightness and forward current.

When VDD is 3.3V and LED's forward voltage is 2.0V, the recommended R1 value ranges from 66 to 250 Ohm.

However, the resistor value must be calculated considering the LED specifications. It is recommended to use VDD below the VPH_PWR level.

Note: If the LED function is enabled and a LED is connected to the LED_N pin, current consumption may be slightly increased. And current sinking mode (up to 10mA) can be supported.

7.3.5.3 Wake Host

WAKE_ON_WAN_N is an active low signal and is used to wake the Host when specific events occur.

- SMS
- Network de-registration
- Voice Call (if supported by the modem)

Please refer to the AT commands guide for setting the Wake function.

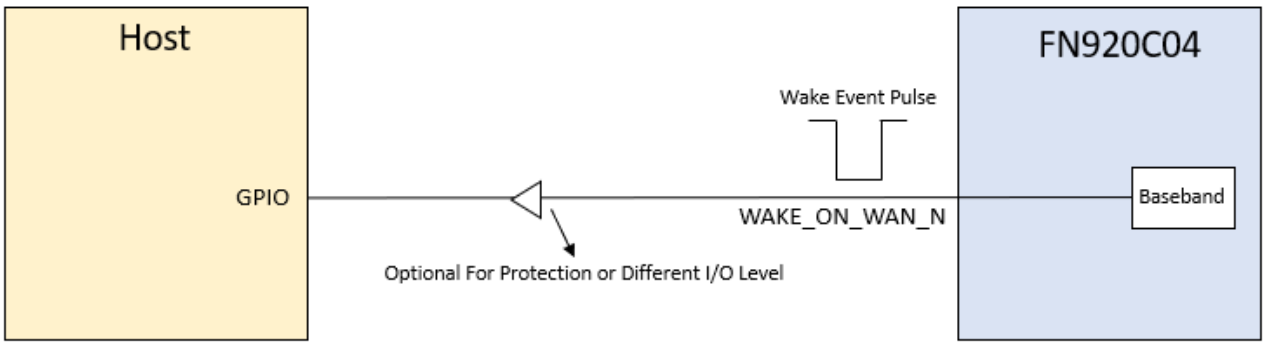


Figure 16: Recommended LED Connection

7.3.5.4 DPR

This signal is input directly to the FN920C04 module from a suitable SAR sensor. Then FN920C04 module will reduce output tx power.

DPR function is not available yet: specific implementation will be determined on customer request.

For further information on the DPR function on the FN920C04 modem family, please contact Telit Technical Support at:

- TS-EMEA@telit.com
- TS-AMERICAS@telit.com
- TS-APAC@telit.com

7.4 General Purpose I/O

The general-purpose I/O pins can be configured to act in four different ways:

- Input
- Output
- Fast shutdown
- Dedicated function (Customer requirement)

Input pins can only report digital values (high or low) present on the pin at the read time.

Output pins can only be set or the pin level can be queried.

Table 34: General Purpose I/O

Pin	Signal	I/O	Function	Type	Comment
General Purpose I/O					
68	TGPIO_01	I/O	General Purpose I/O Can be I2S_CLK	1.8V	
25	TGPIO_02	I/O	General Purpose I/O Can be DPR	1.8V	
62	TGPIO_03	I/O	General Purpose I/O	1.8V	
64	TGPIO_04	I/O	General Purpose I/O	1.8V	
22	TGPIO_06	I/O	General Purpose I/O Can be I2S_DIN	1.8V	

Pin	Signal	I/O	Function	Type	Comment
24	TGPIO_07	I/O	General Purpose I/O Can be I2S_DOUT	1.8V	
28	TGPIO_08	I/O	General Purpose I/O Can be I2S_WS	1.8V	
56	I2C_SDA	I/O	I2C Data Can be TGPIO_09	1.8V	Internal 2.2k PU
58	I2C_SCL	I/O	I2C Clock Can be TGPIO_10	1.8V	Internal 2.2k PU

7.4.1 Using a GPIO as INPUT

GPIO pins, when used as inputs, can be tied to a digital output of another device and report its status, provided the device interface levels are compatible with the GPIO 1.8V CMOS levels.

If a digital output of a device is tied to GPIO input, the pin has interface levels different than 1.8V CMOS. It can be buffered with an open collector transistor with a 47K ohm pull-up resistor to 1.8V.

7.4.2 Using a GPIO as OUTPUT

GPIO pins, when used as output, can drive 1.8V CMOS digital devices or compatible hardware. When set as outputs, the pins have a push-pull output, and therefore the pull-up resistor can be omitted.

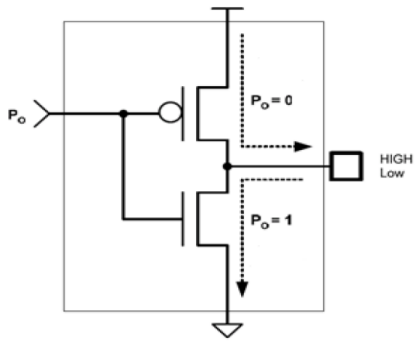


Figure 17: GPIO Output Pin Equivalent Circuit

8 RF Section

8.1 Antenna Interface

The antenna connection is one of the most important aspects of the whole application design as it strongly affects the overall radio performance. Hence, please read and follow the requirements and the guidelines as carefully as possible.

FN920C04 modules provide two MHF-4 type RF connectors covering 5G FR1/LTE/WCDMA bands including GNSS and one MHF-4 type RF connector dedicated to GNSS.

Warning: When connecting cellular and GNSS antennas to the module, pay special attention not to damage RF connectors.

8.1.1 Antenna Configuration

Please refer to the picture below for the connector position.

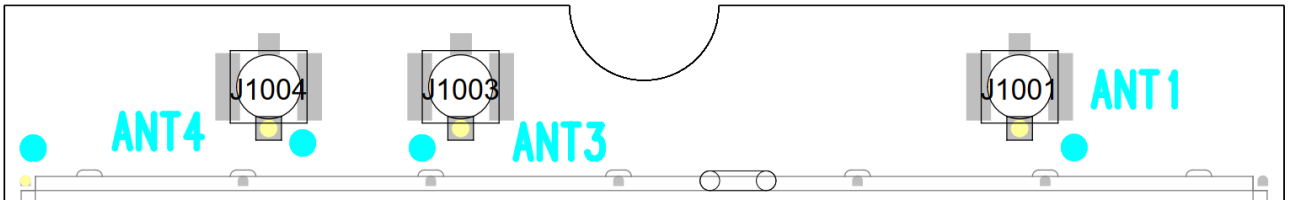


Figure 18: Antenna Configuration

Refer to the following antenna configuration assigned.

Table 35: Antenna Configuration

Antenna port	Technology	Tx	Rx	GNSS
ANT 1	LTE	B1, B2, B3, B4, B5, B7, B8, B12, B13, B14, B17, B18, B19, B20, B25, B26, B28, B30, B34, B38, B39, B40, B41, B42, B43, B48, B66, B70, B71	B1, B2, B3, B4, B5, B7, B8, B12, B13, B14, B17, B18, B19, B20, B25, B26, B28, B30, B34, B38, B39, B40, B41, B42, B43, B48, B66, B70, B71	-
	5G NR FR1	n1, n2, n3, n5, n7, n8, n12, n13, n14, n18, n20, n25, n26, n28, n30, n38, n40, n41, n48, n53, n66, n70, n71, n77, n78, n79	n1, n2, n3, n5, n7, n8, n12, n13, n14, n18, n20, n25, n26, n28, n30, n38, n40, n41, n48, n53, n66, n70, n71, n77, n78, n79	
ANT 4	LTE	-	B1, B2, B3, B4, B5, B7, B8, B12, B13, B14, B17, B18, B19, B20, B25, B26, B28, B30, B34, B38, B39, B40, B41, B42, B43, B48, B66, B70, B71	GPS L1/L5
	5G NR FR1	-	n1, n2, n3, n5, n7, n8, n12, n13, n14, n18, n20, n25, n26, n28,	

Antenna port	Technology	Tx	Rx	GNSS
			n30, n38, n40, n41, n48, n53, n66, n70, n71, n77, n78, n79	
ANT 3	Active antenna (Dedicate)	-	-	GPS L1/L5

8.2 Antenna Connector

The FN920C04 is equipped with a set of 50 Ω RF MHF-4 Receptacles from I-PEX 20449-001E.

For more information about mating connectors, please consult https://www.i-pex.com

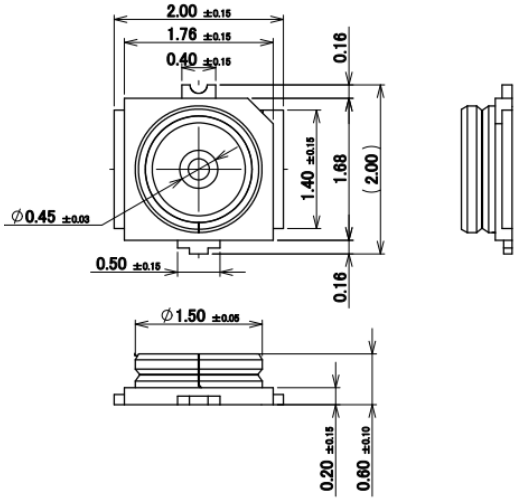


Figure 19: MHF-4 RF connector

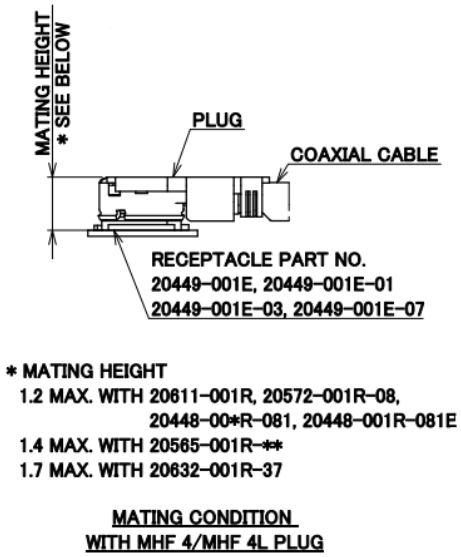


Figure 20: MHF-4 Receptacle

8.3 Antenna Requirements

Antennas for FN920C04 modules must meet the requirements listed in the table below.

8.3.1 LTE/5G Sub-6 Antenna Requirements

Table 36: LTE / 5G Sub-6 Antenna Requirements

Item	Value
Frequency range	Depending on the frequency band(s) provided by the network operator, the customer must use the most suitable antenna for that/those band(s). The bands supported by the FN920C04 are provided in Section 3.2.1 Frequency Bands.
Impedance	50 Ohm
Input power	> 24 dBm average power in LTE & 5G Sub-6
VSWR absolute max	<= 10:1
VSWR recommended	<= 2:1

8.4 Antenna Cable

Connecting cables between the module and LTE/Sub-6 antenna must have a 50 Ohm impedance.

If the impedance of the module does not match, RF performance is significantly reduced.

Table 37: Minimize Antenna Cable Recommendations

Item	Value
Impedance	50 Ohm
Max cable loss	Less than 0.5 dB
Avoid coupling with other signals.	

Warning: Impedance of antenna connector and RF cable must be matched to 50 Ohm: mismatch will affect RF performance; especially high insertion loss of RF cable will cause Tx power and Rx sensitivity degradation.

Warning: The FN920C04 should be located away from noise sources: RF cables and antennas should be installed away from noise sources such as SMPS, USB/PCIe interfaces, etc.

8.4.1 Antenna Installation Guidelines

- Each antenna must be installed with 20dB isolation.
- Install the antenna in a location with access to the network radio signal.
- The Antenna must not be installed inside metal cases.
- The Antenna must be installed according to the antenna manufacturer instructions.
- Antenna integration should optimize Radiation Efficiency. Efficiency values > 50% are recommended on all frequency bands.
- Antenna integration should not perturb the radiation pattern described in the Antenna manufacturer documentation.
- It is preferable to get an omnidirectional radiation pattern.
- To meet the related EIRP limitations, antenna gain must not exceed the values indicated in regulatory requirements, where applicable. The Typical antenna Gain in most M2M applications does not exceed 2dBi.
- If the device antenna is located farther than 20 cm from the human body and there are no co-located transmitters, then the Telit FCC/IC approvals can be re-used by the end product.
- If the device antenna is located closer than 20 cm from the human body or there are co-located transmitters, then additional FCC/IC testing may be required for the end product (Telit FCC/IC approvals cannot be reused).

Note: GNSS receive path uses either the dedicated GNSS connector or the shared Secondary AUX antenna connector.

8.5 GNSS Receiver

The FN920C04 integrates a GNSS receiver that can be used either in Standalone or in A-GPS (assisted GPS) mode.

FN920C04 modems support active GNSS antennas.

Table 38: GNSS Receiver

Item	Value
Frequency range	• GPS L1: 1574.4 ~ 1576.4 MHz (centered on 1575.42 MHz) • GLONASS (GLO): 1597.5 ~ 1605.9 MHz (centered on 1601.7 MHz) • BeiDou (BDS): 1559.1 ~ 1563.1 MHz (centered on 1561 MHz) • Galileo (GAL): 1573.4 ~ 1577.5 MHz (centered on 1575.42 MHz) • GPS L5: 1166.22 ~ 1186.68 MHz (centered on 1176.45 MHz) • BDS B2a: 1166.22 ~ 1186.68 MHz (centered on 1176.45 MHz) • Galileo E5a: 1166.22 ~ 1186.68 MHz (centered on 1176.45 MHz)
Passive Antenna Gain	1.5 dBi < Gain < 3dBi[1]
Impedance	50 Ohm

Item	Value
External Amplification Gain	7.5 dB < Gain < 26 dB for nominal performance[2,3]
Supply Voltage	3.1 V

Note: [1] Configured as AT$GPSANTPORT= 1 (Internal LNA Active in either configuration)

[2] Configured as AT$GPSANTPORT= 2 (Internal LNA bypassed)

[3] Must not exceed 26 dB (Active antenna)

Total gain applied at FN920C04 RF input connector (Passive Antenna gain + External LNA gain-losses)

8.5.1 GNSS RF Front-End Design

The FN920C04 contains an integrated LNA and front-end SAW filter.

This allows the module to operate properly with a passive GNSS antenna. If the antenna cannot be located near the FN920C04, then an active antenna (that is, an antenna with a built-in low noise amplifier) can be used with an external dedicated power supply circuit.

GNSS receive path uses either the dedicated GNSS connector #3 or the shared antenna connector #4.

Note: Please refer to the FN920C04 AT Commands Reference Guide, 80691ST11097A for detailed information about GNSS operating modes and GNSS antenna selection.

8.6 GNSS Characteristics [TBD]

The below table specifies the GNSS characteristics and expected performance:

Table 39: GNSS Characteristics

Parameters		Typical Measurement	Notes
Sensitivity	Tracking Sensitivity	-162 dBm	Standalone or MS-based
	Acquisition	-158 dBm	
	Cold Start	-147 dBm	
TTFF	Hot	1 sec	Open Sky, meanTTFF
	Warm	21 sec	Open Sky, mean TTFF
	Cold	26 sec	Open Sky, mean TTFF
Supported update rate		10 Hz	
CEP-50		<2m	Open sky conditions. Standalone

9 Mechanical Design

9.1 General

The FN920C04 module was designed to be compliant with a standard lead-free SMT process.

9.2 Finishing & Dimensions

The FN920C04 module's overall dimensions are:

- Length: 42.00 mm
- Width: 30.00 mm
- Thickness: 2.3 mm

9.3 Drawing

This figure shows the mechanical dimensions of the FN920C04 module.

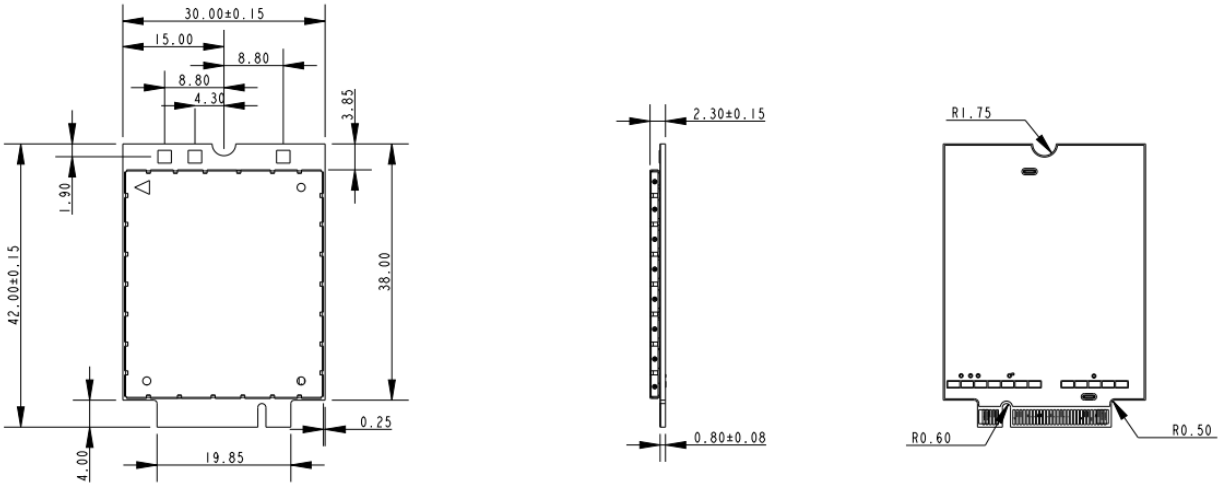


Figure 21: FN920C04 Module Mechanical Dimensions

Warning: The keep-out area (30*2.27 mm) on the bottom side is only for debugging purposes. Please do not use this area for hardware design.

10 Application Guide

10.1 Debug the FN920C04 Module in Production

To test and debug the FN920C04 module integration, it is strongly recommended to add test pins on the host PCB for the following purposes:

- Checking the connection between the FN920C04 itself and the application
- Testing the performance of the module by connecting it to an external computer

Depending on the customer application these test pins include, but are not limited to, the following signals:

- FULL_CARD_POWER_N, SYS_RESET_N, W_DISABLE_N, PCIE_WAKE_N
- VPH_PWR, GND
- VREG_L6B_1P8
- USB_D +/-
- PCIE_TX/RX_M/P

10.2 Bypass Capacitor on Power Supplies

When a sudden voltage step is asserted to or a cut from the power supplies, the step transition causes effects such as overshoot and undershoot. This abrupt voltage transition can affect the device causing it to fail or to malfunction.

Bypass capacitors are needed to alleviate this behavior, which can appear differently depending on the various applications. Integrators must pay special attention to this issue when they design their application board.

The power lines length and width must be considered carefully, and the capacitors value must be selected accordingly.

The capacitor will also prevent power supply ripple and the switching noise caused in TDMA systems, such as GSM.

Most important, a suitable bypass capacitor must be mounted on the following lines on the application board:

- VPH_PWR

Recommended value:

- 100 µF for VPH_PWR

It must be considered that the capacitance mainly depends on the application board.

Generally, additional capacitance is required when the power line is longer.

Furthermore, if the fast power-down function is used, an additional bypass capacitor should be mounted on the application board.

10.2.1 EMC Recommendations

EMC protection on all FN920C04 pins should be designed on the application side according to the customer's requirement.

10.2.2 ESD rating on all pins of FN920C04

Human Body Model (HBM): +/- 1000 V

Charged Device Model (CDM): +/- 250 V

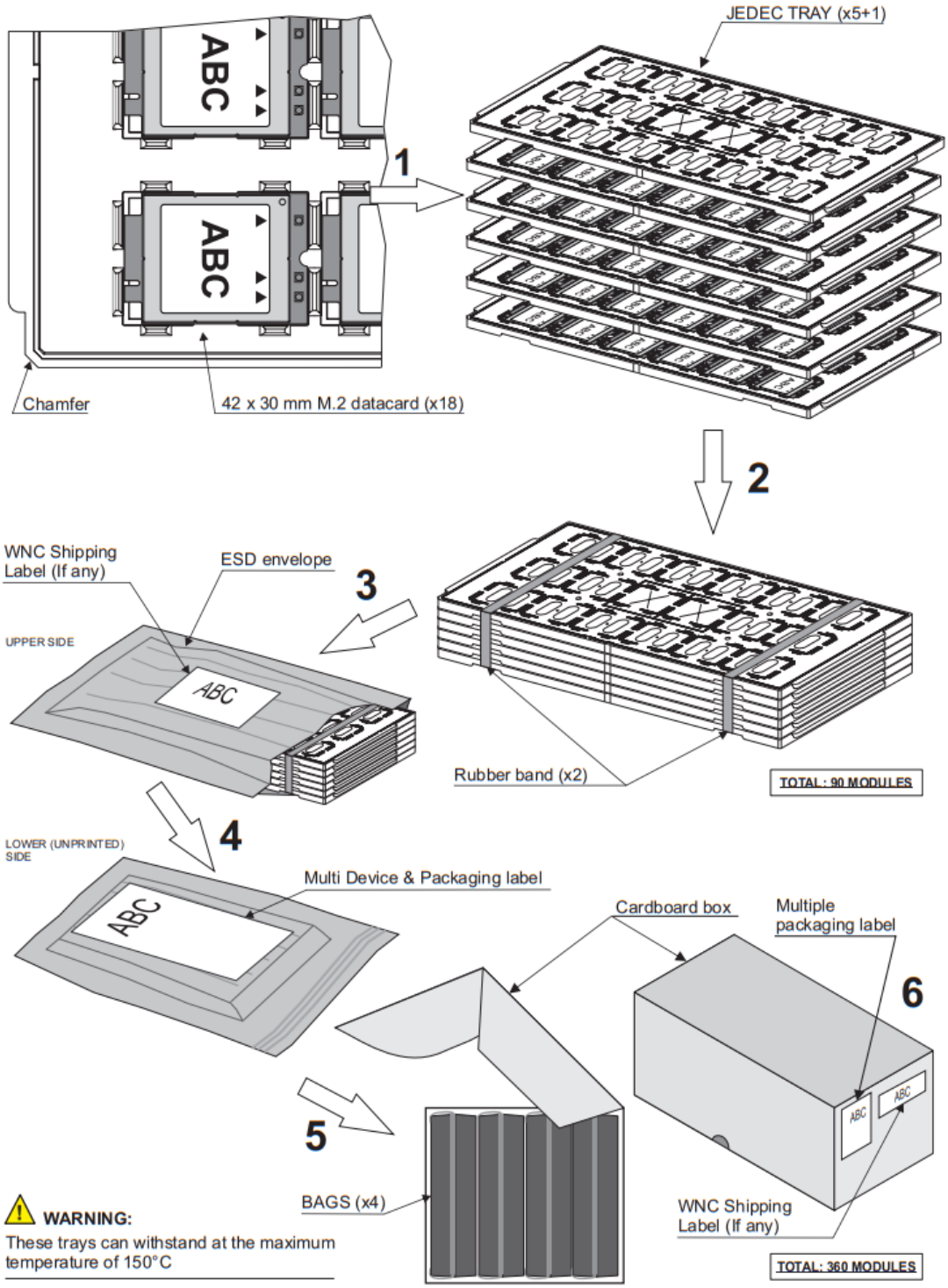
All antenna pins up to +/- 4 kV

Warning: Do not touch without proper electrostatic protective equipment. The product must be handled with care, avoiding any contact with the pins because electrostatic discharge may damage the product itself

11 Packaging

11.1 Tray

The FN920C04 modules are packaged on trays of 15 pieces each. These trays can be used in SMT processes for pick & place handling.



12 Conformity Assessment Issues [TBD]

12.1 Approvals Compliance Summary [TBD]

Table 40: Legend Description

Legend	Description
●	The equipment is compliant
●	Type approval is in progress
	The equipment is not compliant

Table 41: Americas Approvals Compliance Summary

Region	Americas									
Country & Type Approval	AR ENACOM	BR ANATEL	CA ISED	CO CRC	MX IFETEL	PE MTC	US FCC	BOL ATT	ECU ARCOTEL	PY CONATEL
FN920C04-WW										

Table 42: APAC Approvals Compliance Summary

Region	APAC				
Country & Type Approval	AU RCM	CH CCC	JP JRL / JTBL	SG IMDA	TW NCC
FN920C04-WW					

Table 43: EMEA Approvals Compliance Summary

Region	EMEA	
Country & Type Approval	EU RED	UK UKCA
FN920C04-WW		

Note: For approvals not included in the above, contact Telit support.

12.2 Americas Approvals

12.2.1 USA FCC

12.2.1.1 FCC Certificates

The FCC Grants can be found here: https://www.fcc.gov/oet/ea/fccid

12.2.1.2 Applicable FCC Rules

Table 44: Applicable FCC Rules

Model	Applicable FCC rules
FN920C04-WW	Title 47 CFR Part 2, Part 22, Part 24, Part 25, Part 27, Part 90, Part 96

12.2.1.3 FCC Regulatory Notices

Modification Statement

Telit has not approved any changes or modifications to this device by the user. Any changes or modifications could void the user's authority to operate the equipment.

Interference Statement

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Wireless Notice

This device complies with FCC radiation exposure limits set forth for an uncontrolled environment and meets the FCC radio frequency (RF) Exposure Guidelines. This transmitter must not be co-located or operate in conjunction with any other antenna or transmitter. The antenna should be installed and operated with a minimum distance of 20 cm between the radiator and your body

FCC Class B digital device notice

This equipment has been tested and found to comply with the limits for a Class B digital device, according to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used per the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by taking one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and the receiver.
- Connect the equipment to an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

Information for the OEMs and Integrators

The following statement must be included with all versions of this document supplied to an OEM or integrator but should not be distributed to the end user.

1. This device is intended for OEM integrators only.
2. See the full Grant of Equipment document for other restrictions

Manual Information to the End User

The OEM integrator should be aware not to provide information to the end-user on how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warming as shown in this manual

Information on test modes and additional testing requirements

The module has been evaluated in mobile stand-alone conditions. For operational conditions other than a stand-alone modular transmitter in a host (multiple, simultaneously transmitting modules or other transmitters in a host), additional testing may be required (collocation, retesting...). If this module is intended for use in a portable device, you are responsible for separate approval to satisfy the SAR requirements of FCC Part 2.1093.

Additional testing, Part 15 Subpart B disclaimer

The modular transmitter is only authorized by the FCC for the specific rule parts (for example, FCC transmitter rules) listed on the grant, and the host product manufacturer is responsible for compliance with any other FCC rules that apply to the host not covered by the modular transmitter grant of certification. If the grantee markets their product as being Part 15 Subpart B compliant (when it also contains unintentional-radiator digital circuity), then the grantee shall provide a notice stating that the final host product still requires Part 15 Subpart B compliance testing with the modular transmitter installed. The end product with an embedded module may also need to pass the FCC Part 15 unintentional emission testing requirements and be properly authorized per FCC Part 15.

12.2.1.4 FCC Antenna Info

This radio transmitter has been approved by FCC to operate with the antenna types listed below with the maximum allowable gain indicated. Antenna types not included in this list, with a gain greater than the maximum gain indicated for that type, are strictly prohibited from use with this device.

Table 45: FCC Antenna Type

Model	Antenna Type
FN920C04-WW	Omnidirectional Monopole Antenna

Table 46: Max Antenna gain for FCC in dBi

Max gain for FCC (dBi)		
Operation Mode	Freq. (MHz)	Max Allowable Antenna Gain (dBi)
LTE B2	1850.7	8.00
LTE B4	1710.7	5.00
LTE B5	824.7	9.41
LTE B7	2502.5	8.00
LTE B12	699.7	8.69

Max gain for FCC (dBi)		
LTE B13	799.5	9.17
LTE B14	788.0	9.21
LTE B17	704.0	8.72
LTE B25	1850.7	8.00
LTE B26	824.7	9.41
LTE B26 Part 90	814.7	9.36
LTE B30	2307.5	1.50
LTE B38	2572.5	8.00
LTE B41	2498.5	8.00
LTE B41 (HPUE)	2498.5	6.00
LTE B42	3600.0	11.00
LTE B43	3800.0	11.00
LTE B48	3552.50	1.00
LTE B66	1710.70	5.00
LTE B70	1710.0	5.50
LTE B71	665.50	8.48
5G NR n2	1850.7	8.00
5G NR n5	824.7	9.41
5G NR n7	2502.5	8.00
5G NR n12	699.7	8.69
5G NR n13	779.5	9.17
5G NR n14	788.0	9.21
5G NR n25	1850.7	8.00
5G NR n26	824.7	9.41
5G NR n26 Part90	814.7	9.36
5G NR n30	2307.5	1.50
5G NR n38	2572.5	8.00
5G NR n41_PC3	2498.5	8.00
5G NR n48	3552.50	1.00
5G NR n53	2495.0	11.00
5G NR n66	1710.70	5.00
5G NR n70	1710.0	5.00
5G NR n71	665.50	8.48
5G NR n77_PC3	4200.00	4.99
5G NR n78_PC3	3800.00	4.99

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

Labelling requirements for the host device

The host device shall be properly labelled to identify the modules within the host device. The certification label of the module shall be visible at all times when installed in the host device, otherwise, the host device must be labelled to display the FCC ID of the module, preceded by the words "Contains transmitter module", or the word "Contains", or similar wording expressing the same meaning, as in the below table.

Table 47: Host device FCC label

Model / HVIN	Host device FCC label
FN920C04-WW	Contains FCC ID: RI7FN920C04WW

* Data-only variants share the same HW as related voice variants and the voice features are disabled by SW. The new FVIN has the same stack as the originally approved voice variant but w/o voice support and this is to satisfy mainly the need of MNOs in the USA.

12.2.2 Canada ISED

12.2.2.1 ISED Database

The products ISED certified can be found here:

Les produits certifiés ISED peuvent être trouvés ici:
https://sms-sgs.ic.gc.ca/equipmentSearch/searchRadioEquipments?execution=e1s1&lang=en

12.2.2.2 Applicable ISED Rules / Liste des Règles ISDE Applicables

Table 48: Applicable ISED rules / Règles ISDE applicables

Model	Applicable ISED rules /Règles ISDE applicables 2
FN920C04-WW	RSS-Gen Issue 5, RSS-102 Issue 6, RSS-130 Issue 2, RSS-132 Issue 4, RSS-133 Issue 7, RSS-139 Issue 4, RSS-140 Issue 1, RSS-192 Issue 5, RSS-195 Issue 2, RSS-199 Issue 4, RSS-198 Issue 1

12.2.2.3 ISED Regulatory Notices

Modification Statement / Déclaration de modification

Telit has not approved any changes or modifications to this device by the user. Any changes or modifications could void the user's authority to operate the equipment.

Telit n'approuve aucune modification apportée à l'appareil par l'utilisateur, quelle qu'en soit la nature. Tout changement ou modification peuvent annuler le droit d'utilisation de l'appareil par l'utilisateur.

Interference Statement / Déclaration d'interférence

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Le présent appareil est conforme aux applicables RSS standards d'Industrie Canada. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Radio Exposure Notice / Avis d'exposition radio

This device complies with ISED radiation exposure limits set forth for an uncontrolled environment and meets the RSS-102 of the ISED radio frequency (RF) Exposure rules. Antenna gain must be less than the values reported in the table below:

Le présent appareil est conforme à l'exposition aux radiations FCC / ISED définies pour un environnement non contrôlé et répond aux directives d'exposition de la fréquence de la FCC radiofréquence (RF) et RSS-102 de la fréquence radio (RF) ISED règles d'exposition. Gain de l'antenne doit être ci-dessous:

Table 49: ISED Antenna Type

Model	Antenna Type / Type d'Antenne
FN920C04-WW	Antenne monopôle omnidirectionnelle

Table 50: Max Antenna gain for ISED in dBi / Gain d'antenne max pour ISED en dBi

Max gain for ISED/ Gain max pour ISED (dBi)		
Operation Mode	Freq. (MHz)	Max Allowable Antenna Gain (dBi)
LTE B2	1850.7	8.00
LTE B4	1710.7	5.00
LTE B5	824.7	6.10
LTE B7	2502.5	8.00
LTE B12	699.7	5.61
LTE B13	799.5	5.93
LTE B14	788.0	5.97
LTE B17	704.0	5.63
LTE B25	1850.7	8.00
LTE B26	824.7	6.10
LTE B30	2307.5	1.50
LTE B38	2572.5	8.00
LTE B41	2498.5	8.00
LTE B41 (HPUE)	2498.5	6.00
LTE B42	3600.0	8.00
LTE B42 (HPUE)	3600.0	4.00
LTE B43	3800.0	8.00
LTE B48	3552.50	1.00
LTE B66	1710.70	5.00
LTE B71	665.50	5.46
5G NR n2	1850.7	8.00
5G NR n5	824.7	6.10

Max gain for ISED/ Gain max pour ISED (dBi)		
5G NR n7	2502.5	8.00
5G NR n12	699.7	5.61
5G NR n13	779.5	5.93
5G NR n14	788.0	5.97
5G NR n25	1850.7	8.00
5G NR n26	824.7	6.10
5G NR n30	2307.5	1.50
5G NR n38	2572.5	8.00
5G NR n41_PC3	2498.5	8.00
5G NR n48	3552.50	1.00
5G NR n66	1710.70	5.00
5G NR n71	665.50	5.46
5G NR n77_PC3	4200.00	4.99
5G NR n78_PC3	3800.00	4.99

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

L'émetteur ne doit pas être colocalisé ni fonctionner conjointement avec à autre antenne ou autre émetteur.

This equipment must be installed and operated following provided instructions and the antenna(s) used for this transmitter must be installed to provide a separation distance of at least 20 cm from all persons and must not be co-located or operating in conjunction with any other antenna or transmitter. End-users and installers must be provided with antenna installation instructions and consider removing the no-collocation statement.

Cet équipement doit être installé et utilisé conformément aux instructions fournies et la ou les antennes utilisées pour cet émetteur doivent être installées pour fournir une distance de séparation d'au moins 20 cm de toutes les personnes et ne doivent pas être co-localisées ou fonctionner en conjonction avec toute autre antenne ou émetteur. Les utilisateurs finaux et les installateurs doivent recevoir les instructions d'installation de l'antenne et envisager de supprimer la déclaration de non-collocation.

Information on test modes and additional testing requirement / Informations sur les modes de test et exigences de test supplémentaires

The module has been evaluated in mobile stand-alone conditions. For operational conditions other than a stand-alone modular transmitter in a host (multiple, simultaneously transmitting modules or other transmitters in a host), additional testing may be required (collocation, retesting...) If this module is intended for use in a portable device, you are responsible for separate approval to satisfy the SAR requirements IC RSS-102.

Le module a été évalué dans des conditions mobiles autonomes. Pour des conditions de fonctionnement autres qu'un émetteur modulaire autonome dans un hôte (plusieurs modules transmettant simultanément ou d'autres émetteurs dans un hôte), des tests supplémentaires peuvent être nécessaires (colocalisation, retest...) Si ce module est destiné à être utilisé dans un appareil portable , vous êtes responsable de l'approbation séparée pour satisfaire aux exigences SAR IC RSS-102.

Labelling requirements for the host device /
Exigences d'étiquetage pour le périphérique hôte

The host device shall be properly labelled to identify the modules within the host device. The certification label of the module shall be visible at all times when installed in the host device, otherwise, the host device must be labelled to display the IC of the module, preceded by the words "Contains transmitter module", or the word "Contains", or similar wording expressing the same meaning, as in the following table.

L'appareil hôte doit être étiqueté comme il faut pour permettre l'identification des modules qui s'y trouvent. L'étiquette de certification du module donné doit être posée sur l'appareil hôte à un endroit bien en vue en tout temps. En l'absence d'étiquette, l'appareil hôte doit porter une étiquette donnant le IC du module, précédé des mots « Contient un module d'émission », du mot « Contient » ou d'une formulation similaire exprimant le même sens, comme en tableau suivant.

Table 51: Host device IC label / Ètiquette IC du dispositif hôte

Model / HVIN	Host device IC label / Ètiquette IC du dispositif hôte
FN920C04-WW	Contains IC: 5131A-FN920C04WW

* Data-only variants share the same hardware as related voice variants and the voice features are disabled by SW. The new FVIN has the same stack as the originally approved voice variant but w/o voice support and this is to satisfy mainly the need of MNOs in the USA.

Les variantes avec soulement data fonctionnalité partagent le même hardware que les variantes avec les fonctionnalités vocales qui sont désactivées par le SW. Le nouveau FVIN a le même stack que la variante avec fonctionnalités vocales approuvée d'origine, mais sans la prise en charge vocale et cela pour satisfaire principalement le besoin des MNO aux États-Unis.

CAN ICES-3 (B) / NMB-3 (B)

This Class B digital apparatus complies with Canadian ICES-003.

Cet appareil numérique de classe B est conforme à la norme canadienne ICES-003.

12.2.3 Brazil ANATEL [TBD]

12.2.3.1 ANATEL Regulatory Notices

Agência Nacional de Telecomunicações (ANATEL) of Brazil

Note: For Wi-Fi or BT modules, insert following lines:

"Este equipamento não tem direito à proteção contra interferência prejudicial e não pode causar interferência em sistemas devidamente autorizados".

"This equipment is not entitled to protection against harmful interference and must not cause interference in duly authorized systems"

FN920C04-WW Homologation # TBD

12.3 APAC Approvals [TBD]

12.3.1 Australia RCM

Following the above Approval Compliance Summary table, where applicable (green dot), hereby, Telit Communications S.p.A declares that the equipment complies with the Regulatory Compliance Mark (RCM) of Australia.

Note: The equipment listed may not work when the main power fails.

12.3.2 Japan JRL/JTBL [TBD]

12.3.2.1 JRL/JTBL Regulatory Notices

Antenna info

According to Japanese regulatory rules, module certification is valid only with the specific antennas registered to and approved by Japan Radio Law (JRL) certified body to module certification. Customers who are going to use modules under JRL are responsible to contact Telit technical support or sales to get the list of these antennas.

Dial Function

The Japan Telecommunication Business Law (JTBL) Module Certification for “FN920C04-WW” is for a “non-Auto Redial Function” device.

In case the customer implements the “Auto Redial” function into the Application Device by controlling FN920C04-WW, the customer cannot utilize FN920C04-WW JTBL certification, and they must apply JTBL as an “Application Device” System.

12.3.3 Taiwan NCC [TBD]

12.3.3.1 NCC Regulatory Notices

According to National Communication Commission (NCC) Taiwan requirements, the module and the packaging shall be identified as described in the following lines. Shall be added also the specified safety warning statement.

Brand name: Telit Cinterion or

Model name: FN920C04-WW

Product name: 5G Radio module

NCC logo: 

NCC ID: TBD

NCC safety warning statement: “減少電磁波影響，請妥適使用”

12.4 EMEA Approvals

12.4.1 EU RED

12.4.1.1 EU Declaration of Conformity

Following the above Approval Compliance Summary table, where applicable (green dot), hereby, Telit Communications S.p.A declares that the equipment complies with Directive 2014/53/EU.

The full text of the EU Declaration of Conformity is available at the following internet address: https://www.telit.com/red

Text of 2014/53/EU Directive (RED) requirements can be found here:

https://eur-lex.europa.eu/legal-content/EN/TXT/?uri=CELEX:32014L0053

12.4.1.2 RED Antennas

This radio transmitter has been approved under RED to operate with the antenna types listed below with the maximum permissible gain indicated. The usage of a different antenna in the final hosting device may need a new assessment of host conformity to RED.

Table 52: RED Antenna Type

Model	Antenna Type
FN920C04-WW	Omnidirectional Monopole Antenna

Table 53: Max Antenna gain for RED in dBi

Max gain for RED (dBi)		
Operation Mode	Freq. (MHz)	Max Allowable Antenna Gain (dBi)
LTE B1	1920.00	11.85
LTE B3	1710.00	11.35
LTE B5	824.00	8.17
LTE B7	2500.00	11.96
LTE B8	880.00	8.46
LTE B20	832.00	8.22
LTE B34	2010.00	11.96
LTE B38	2570.00	11.96
LTE B40	2302.50	11.96

Max gain for RED (dBi)		
LTE B41(HPUE)	2496.00	9.96
LTE B28	703.00	7.48
LTE B42_PC3	3400.00	14.96
LTE B43_PC3	3600.00	14.96
5G NR n1	1920.00	11.85
5G NR n3	1710.00	11.35
5G NR n5	824.00	8.17
5G NR n7	2500.00	11.96
5G NR n8	880.00	8.46
5G NR n20	832.00	8.22
5G NR n38	2570.00	11.96
5G NR n40	2302.50	11.96
5G NR n41_PC3	2496.00	11.96
5G NR n28	703.00	7.48
5G NR n77_PC3	3300.00	11.96
5G NR n78_PC3	3300.00	11.96

12.4.2 UK UKCA

12.4.2.1 UKCA Declaration of Conformity

Following the above Approval Compliance Summary table, where applicable (green dot), hereby, Telit Communications S.p.A declares that the equipment complies with the Radio Equipment Regulations 2017 for UKCA.

The full text of the UKCA declaration of conformity is available at the following internet address: https://www.telit.com/ukca

The UKCA requirements can be found here:

https://www.gov.uk/guidance/using-the-ukca-marking

12.5 RoHS, REACH, and WEEE Info

12.5.1 RoHS Info

Any requests on information related to RoHS certifications can be addressed to Chemical.Certifications@telit.com.

12.5.2 REACH Info

Any requests on information related to REACH certifications can be addressed to Chemical.Certifications@telit.com.

12.5.3 WEEE Info

This symbol means that according to local laws and regulations your product and/or its battery shall be disposed of separately from household waste. When this product reaches its end of life, take it to a collection point designated by local authorities. Proper recycling of your product will protect human health and the environment.

13 Acronyms and Abbreviations

Table 54: Acronyms and Abbreviations

Acronym	Definition
ADC	Analog – Digital Converter
CLK	Clock
CMOS	Complementary Metal – Oxide Semiconductor
DTE	Data Terminal Equipment
EN-DC	E-UTRA – NR Dual Connectivity
ESR	Equivalent Series Resistance
E-UTRA	Evolved UMTS Terrestrial Radio Access
FDD	Frequency Division Duplex
GPIO	General Purpose Input Output
HS	High Speed
HSDPA	High Speed Downlink Packet Access
HSUPA	High Speed Uplink Packet Access
I/O	Input Output
I2C	Inter-integrated Circuit
I2S	Inter-IC Sound
LTE	Long Term Evolution
NR	New Radio
PCB	Printed Circuit Board
PCIE	Peripheral Component Interconnect Express
RTC	Real-Time Clock
SDL	Supplementary Down Link
SIM	Subscriber Identification Module
SOC	System-on-Chip
SMPS	Switching Mode Power Supply
TDD	Time Division Duplex
TTSC	Telit Technical Support Center
UART	Universal Asynchronous Receiver Transmitter
UMTS	Universal Mobile Telecommunication System
USB	Universal Serial Bus
VNA	Vector Network Analyzer
VSWR	Voltage Standing Wave Radio
WDMA	Wideband Code Division Multiple Access
CA	Carrier aggregation
CLK	Clock
CMOS	Complementary Metal – Oxide Semiconductor
DTE	Data Terminal Equipment

Acronym	Definition
EN-DC	E-UTRA – NR Dual Connectivity
ESR	Equivalent Series Resistance

14 Related Documents

Refer to https://dz.telit.com/ for current documentation and downloads.

Table 55: Related Documents

S.no	Book Code	Document Title
1	1VV0301882	FN920C04 SW User Guide
2	1VV0301883	FN920C04 AT Commands Reference Guide
3	1VV0301249	Generic EVB HW User Guide
4	1VV0301732	Telit EVB 2.0 HW User Guide

15 Document History

Table 56: Document History

Revision	Date	Changes
3	2025-03-31	Updated Table 46,50,53 Max Antenna gain
2	2025-03-28	Updated Table 6 conducted Transmit Output Power - Removed 5G NR Sub-6 Power class2 n41, n77, n78, n79 Updated 5.1 Power Supply Requirements - Peak current consumption: 3.3 V @ 2.5 A Updated 5.2. Power Consumption Updated 6.2 Recommended Operating condition - Peak current on pin VPH_PWR: Max 2.5 A Updated Table 38 GNSS Receiver - Frequency range Updated 12 Conformity Assessment Issue. Updated 7.2 Power ON/OFF/RESET
1	2025-01-09	Updated 3.2.1 Frequency Bnads Table 4 Main Features: Added SRS(1T2R) and Supported bands n53 Updated 3.6.1 Conducted Transmit Output Power Updated 3.6.2 Conducted Receiver Sensitivity Updated 5.2 Power Consumption Table 35 Antenna Configuration: Added n53 Updated 7.3.1.1 Host Interface Switch Function Updated 8.6 GNSS Characteristics
0	2024-09-24	Initial release

From Mod.0818 Rev.17