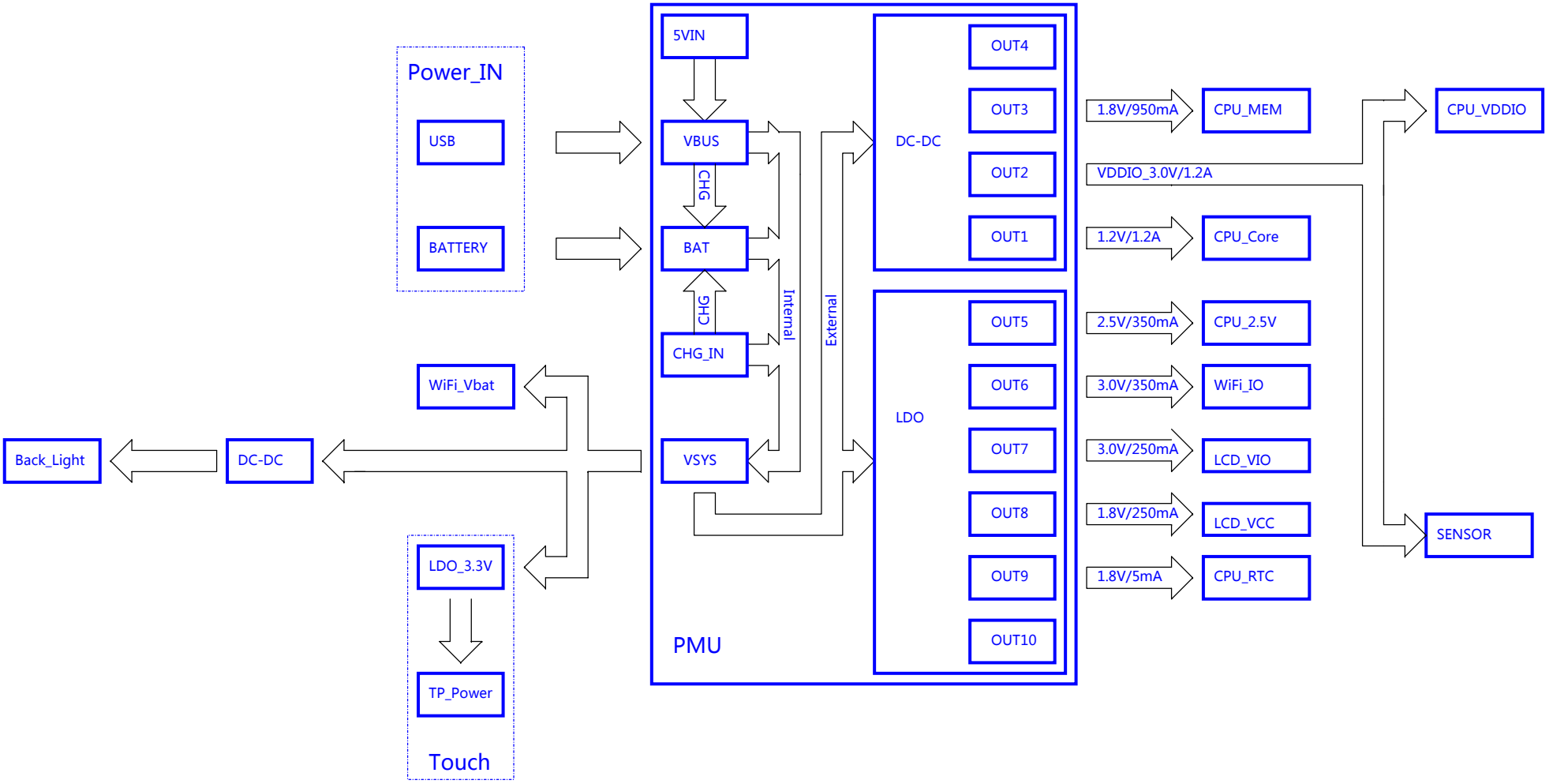


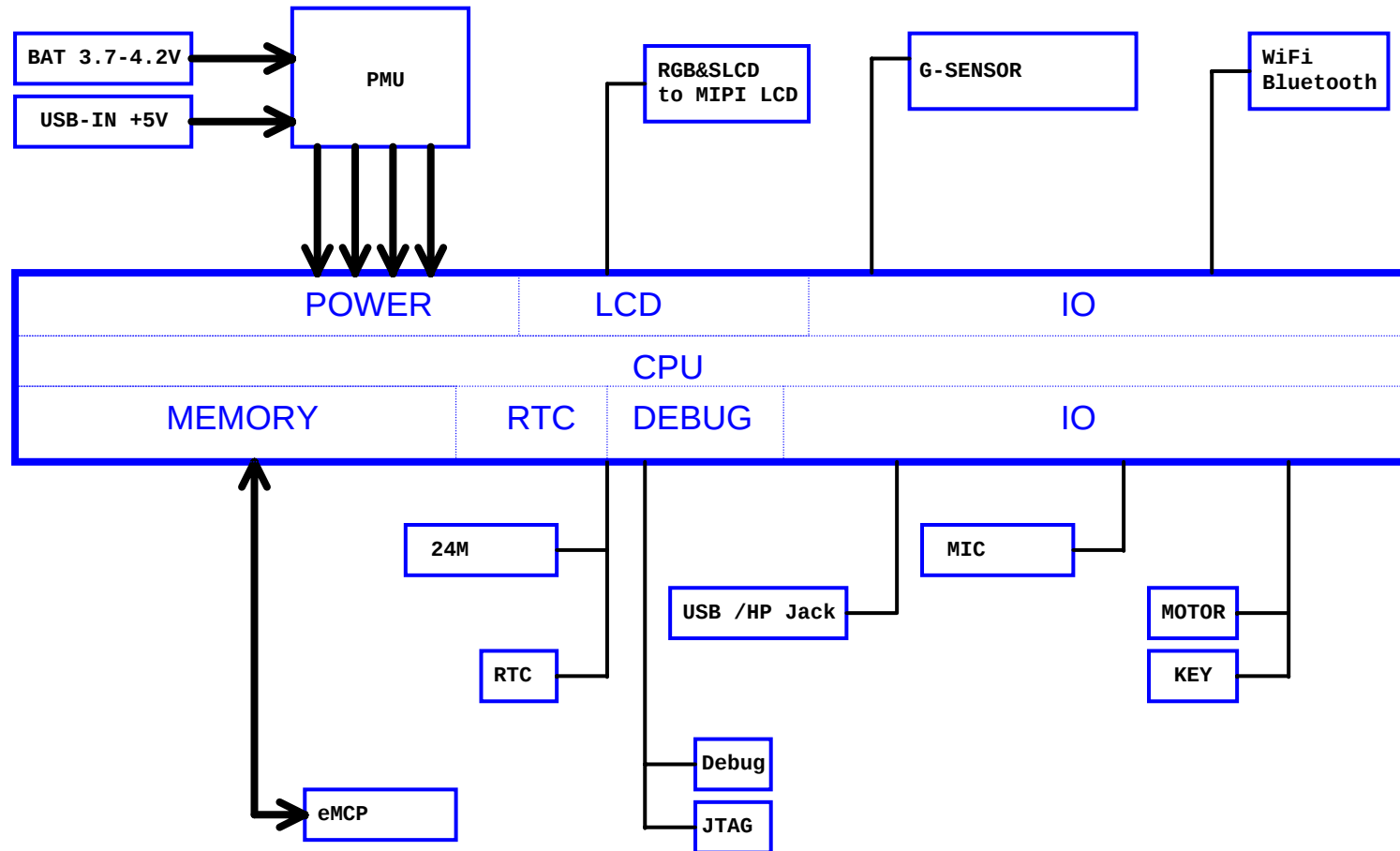
Z1 Schematic Revision 2

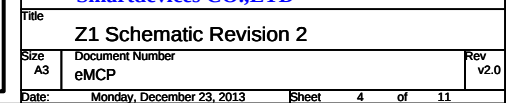
Title	Page	REMARK
COVER SHEET	1	
Power Block Diagram	2	
System Block Diagram	3	
eMCP	4	
CPU_IO	5	
CPU_PWR	6	
Power_PMU	7	
USB/MIC	8	
SENSOR	9	
MIPI LCD/TP	10	
WiFi and BT	11	BT&WIFI Module
	12	

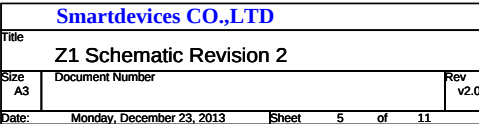
Z1 V2.0 Power Block Diagram

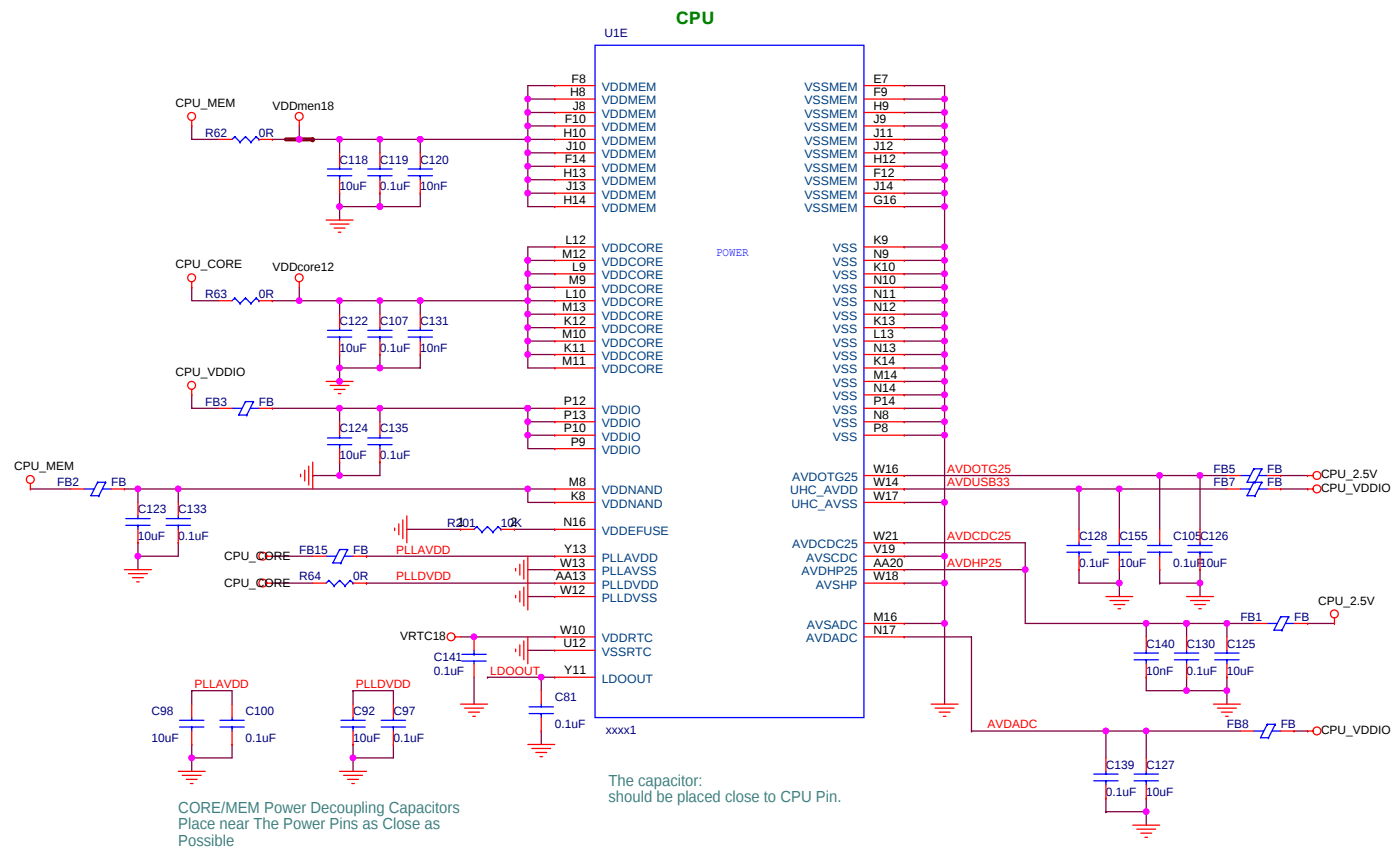


Z1 V2.0 System Block Diagram



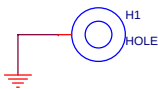
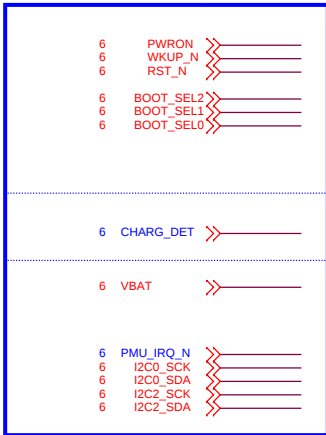




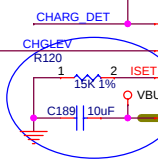


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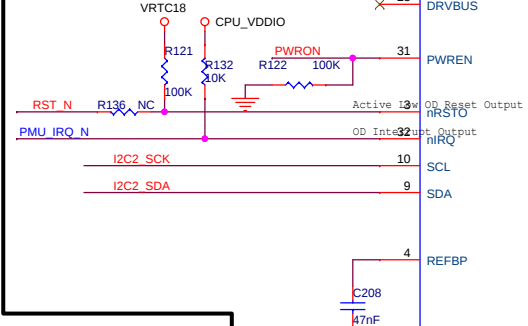
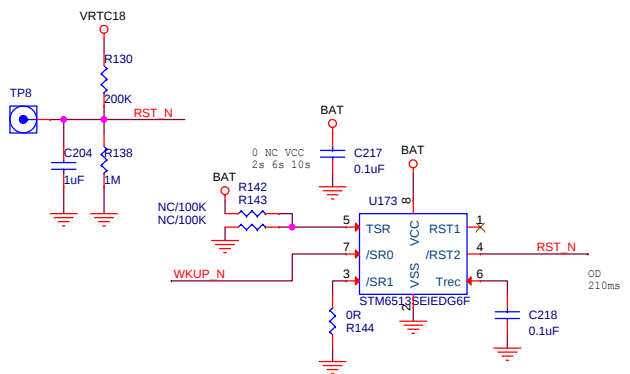
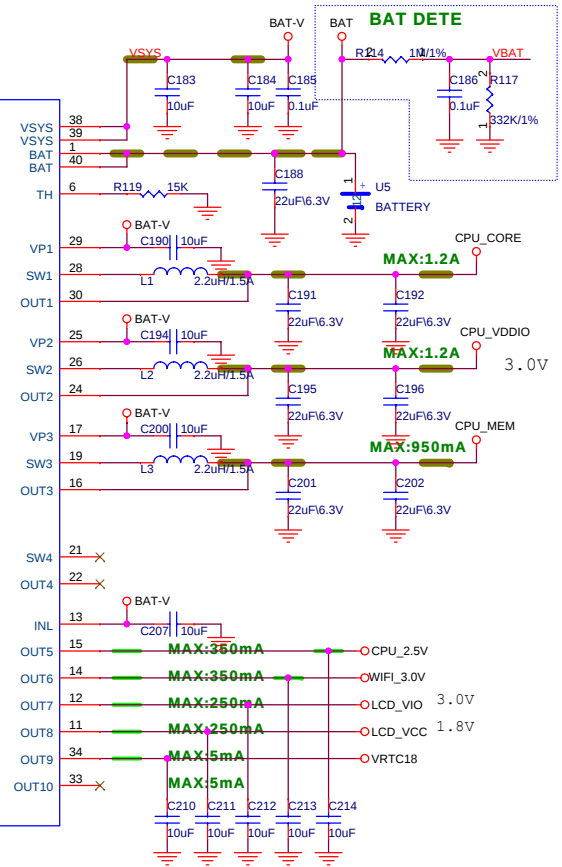
Title		
Z1 Schematic Revision 2		
Size	Document Number	Rev
A3	CPU_PWR	v2.0
Date:	Monday, December 23, 2013	Sheet 6 of 11



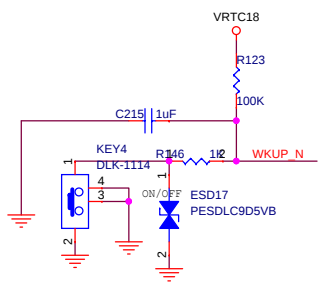
$$R_{ISET} (k\Omega) = 2336 \times (1V / I_{CHG} (mA)) - 0.205$$



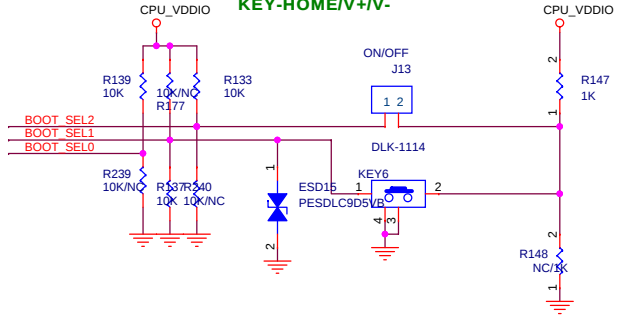
PMU



KEY_POWER-ON/OFF



KEY-HOME/V+/V-



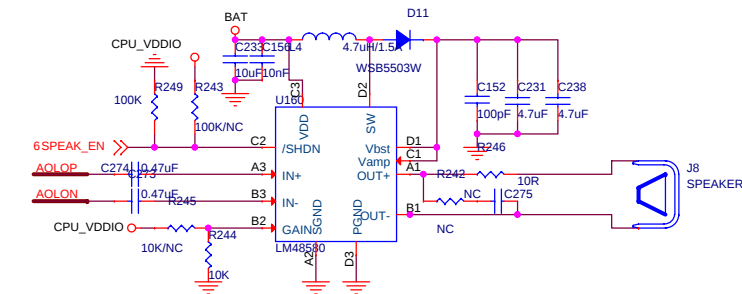
Boot Mode Select

BOOT_SEL[2:1:0]	BOOT FROM	USB 24M R147 1K, R148 NC
101	SD Boot	USB 26M R148 1K, R147 NC
111	USB Boot 24M	
001	USB Boot 26M	

Main Power Test

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Z1 Schematic Revision 2



W>20mil

USB_DETE

CPU_VDDIO

Q13 SPN7002 G 1

R200 100K

R183 10K

C235 10uF

C237 1uF

D10M3Z5V1T1G

TP10

TP11

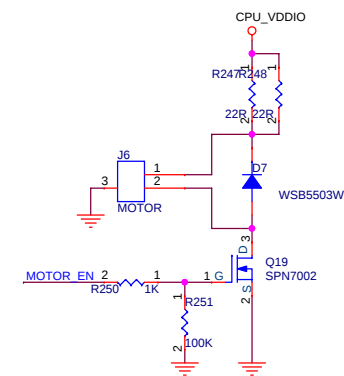
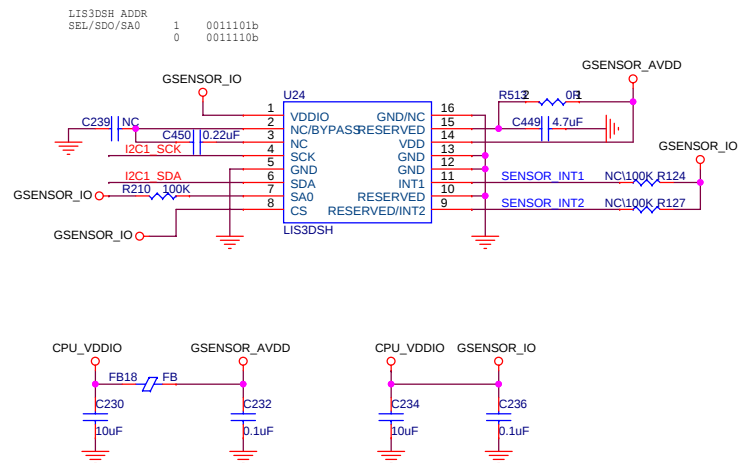
OTG_DM

OTG_DP

Differential pairs
Z0= 90 ohm

[illegible]

G-SENSOR



MOTOR

