

August 11, 2003

Mr. Tri Luu

Ultratech Engineering Labs

Re: Power Calibration Procedure

Dear Tri,

The following is the procedure for tuning output power on the Chantry BP100 units.

The chipset used in the product is provided by Intersil Corp., which also supplies the executable test software used in manufacturing to test and configure the units. One of these tests is called RF-Test, and it is this test which configures the unit's output power. The procedure is as follows:

- 1) Set channel to channel 1.
- 2) Sweep power level across this channel, and determine output power level which corresponds to 30dBc sidelobe specification.
- 3) Record two values in the unit firmware: #1 The power set level, and #2 The power detector level
- 4) Repeat for channels 2 to 14

When the units are fully tested, they are deemed completed assemblies. These completed assemblies function in a closed-loop calibrated power mode. The power set and power detector levels programmed into the flash device are used to automatically adjust the power set level to maintain 30dBc sidelobes over all operating conditions.

Sincerely,  
Tomasz Czernecki