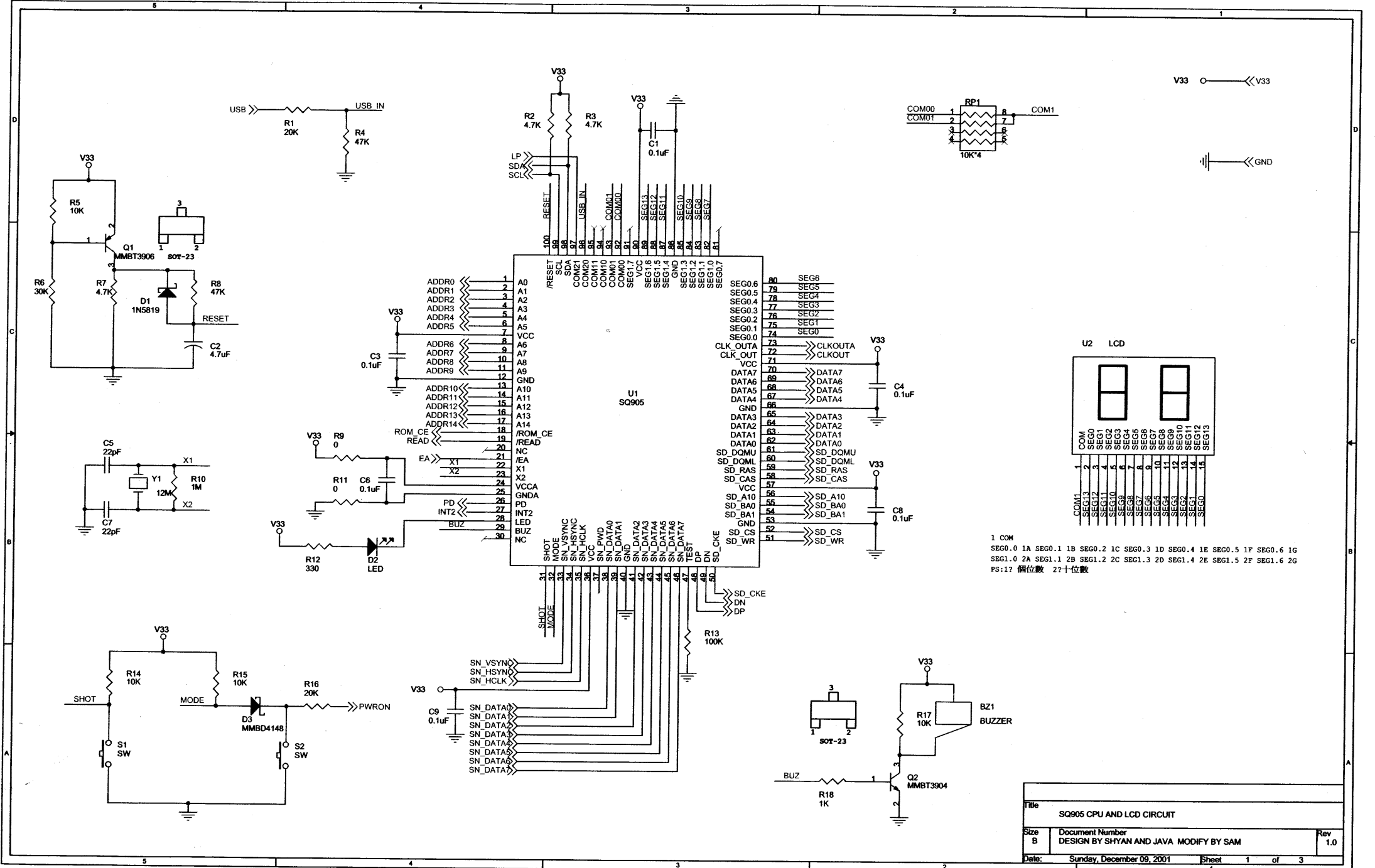
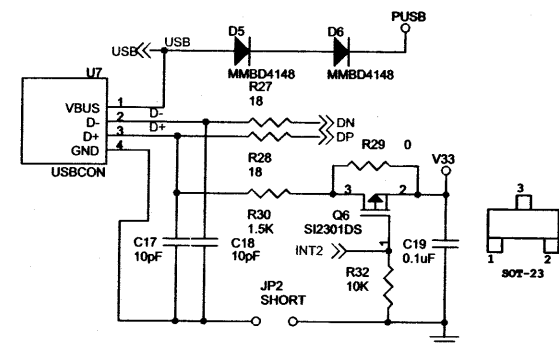
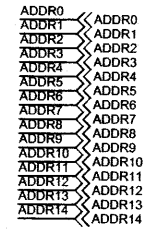
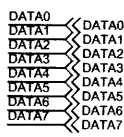
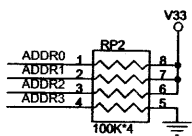
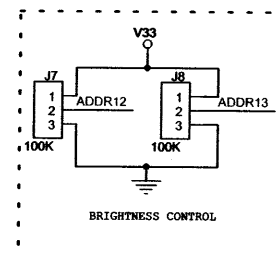
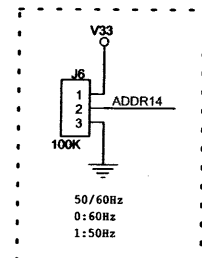
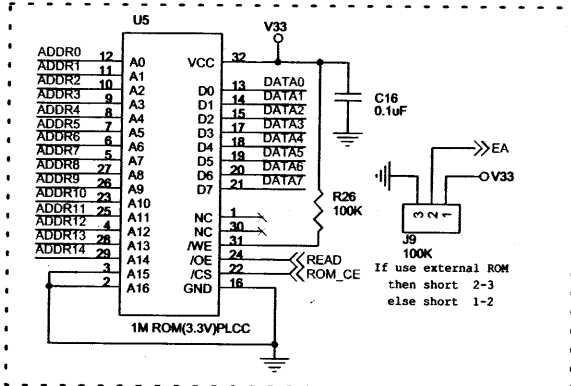
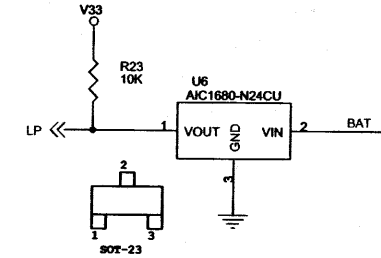
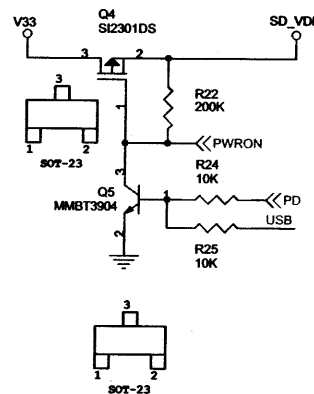
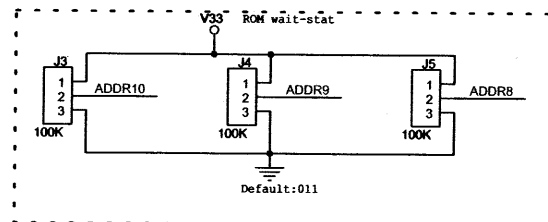
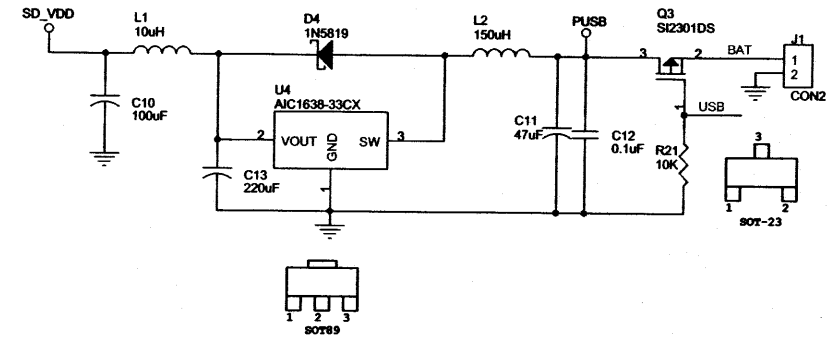
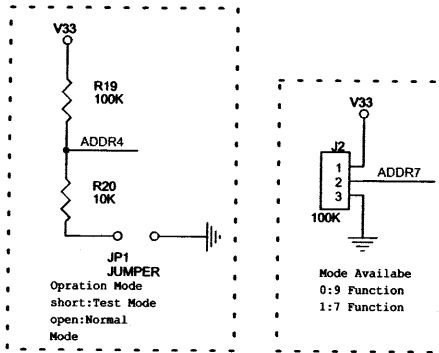
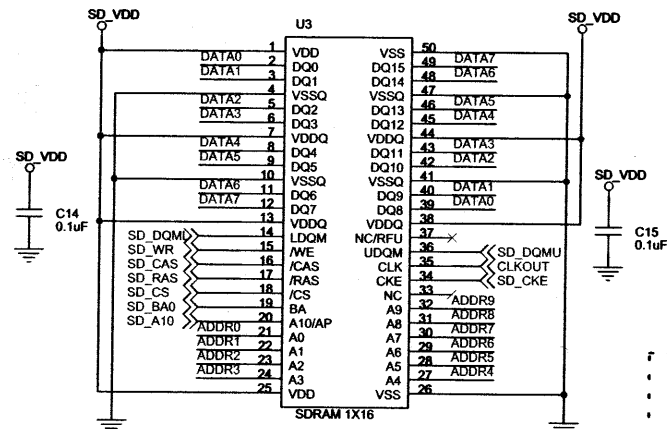




1 COM  
SEG0.0 1A SEG0.1 1B SEG0.2 1C SEG0.3 1D SEG0.4 1E SEG0.5 1F SEG0.6 1G  
SEG1.0 2A SEG1.1 2B SEG1.2 2C SEG1.3 2D SEG1.4 2E SEG1.5 2F SEG1.6 2G  
PS:1? 偶位數 2?十位數



|                                 |                           |              |
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| MEMORY CONFIG AND POWER CIRCUIT |                           |              |
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| Date:                           | Sunday, December 09, 2001 | Sheet 2 of 3 |

