

#91001 TRANSMITTER SCHEMATIC DIAGRAM

The schematic diagram illustrates the internal circuitry of a security alarm transmitter. The central component is the PT12262 microcontroller (U1), which is powered by a 9V battery (BT1) through a switch (SW-PB). The microcontroller's VDD pin (18) is connected to the positive supply, and its VSS pin (9) is connected to ground. The address pins (A0-A7) are connected to a common ground line. The oscillator pins (OSC0 and OSC1) are connected to a 433MHz crystal (XTAL1) and a 47K resistor (R3). The data output pin (DO, pin 17) is connected to the base of a KSP10 transistor (Q1) through a 47K resistor (R3). The transistor's emitter is connected to ground, and its collector is connected to an antenna (ANT) through a 47K resistor (R5). The antenna is also connected to a 102 capacitor (C2) and a 100 capacitor (C4). A 1K resistor (R2) is connected to the LED (LD1) pin (16) of the microcontroller. The LED is connected to ground through a 47K resistor (R3). The microcontroller's pins 10, 11, 12, and 13 are connected to ground. The circuit is labeled with various components: BT1 (9V battery), SW-PB (switch), U1 (PT12262), LD1 (LED), XTAL1 (433MHz crystal), Q1 (KSP10 transistor), R1 (1.2M), R2 (1K), R3 (47K), R4 (100), R5 (47), C1 (203), C2 (102), and C3 (1PF).

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