

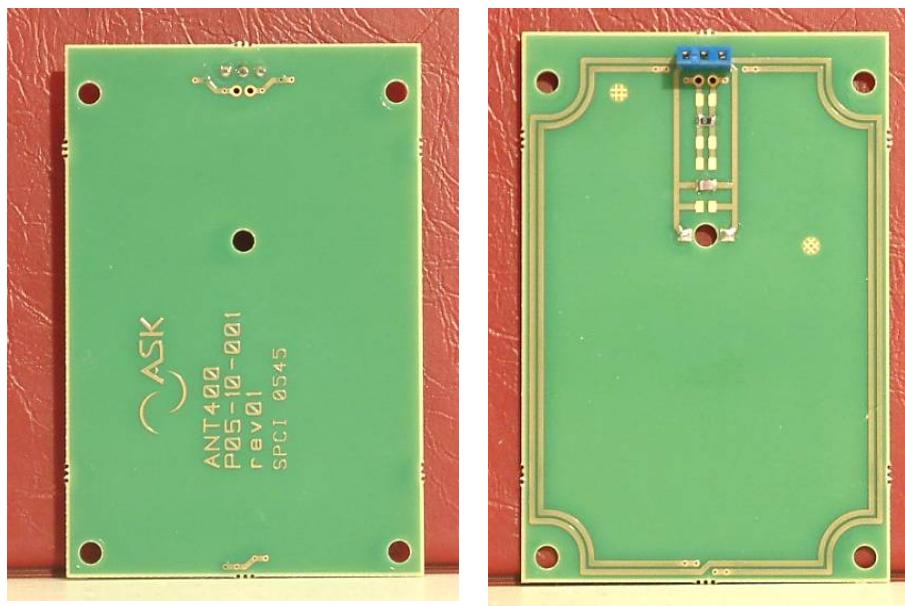
1 INTERNAL PHOTOS OF TESTED EUT

1.1 General view of equipment



1.2 PCB view





PCB view with ferrite square stuck on FPGA chip

1.3 General view of PCB (assembled)

