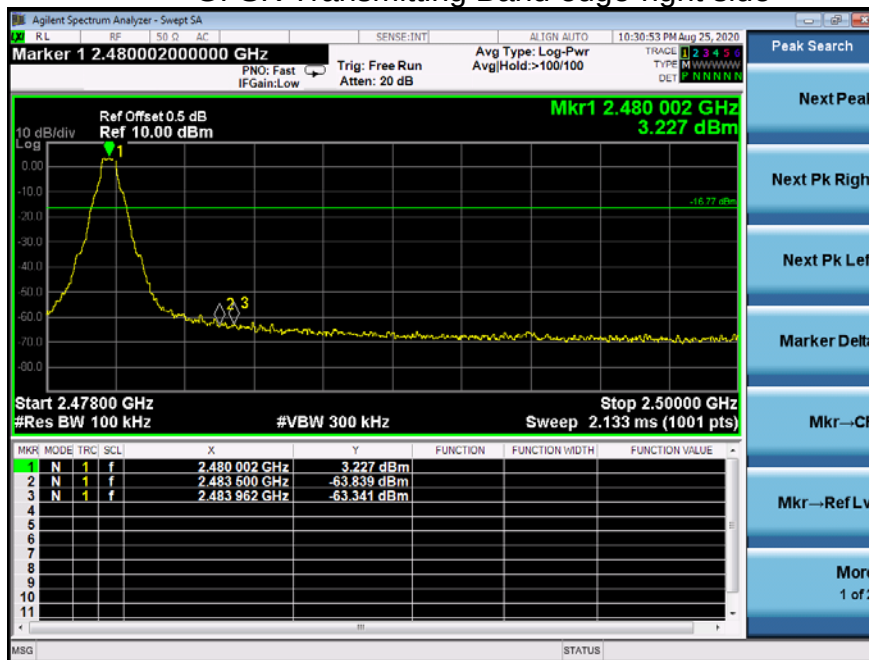
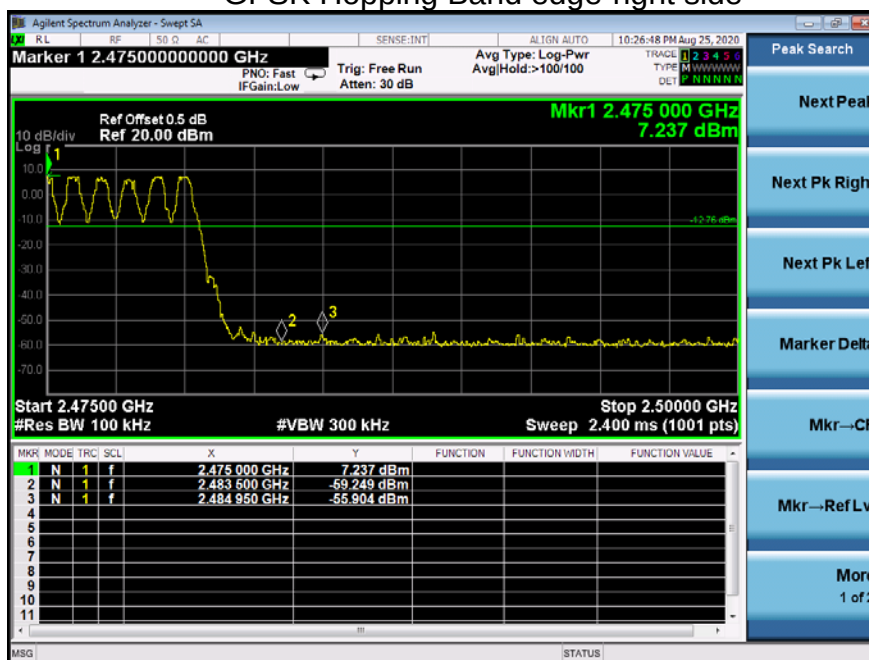


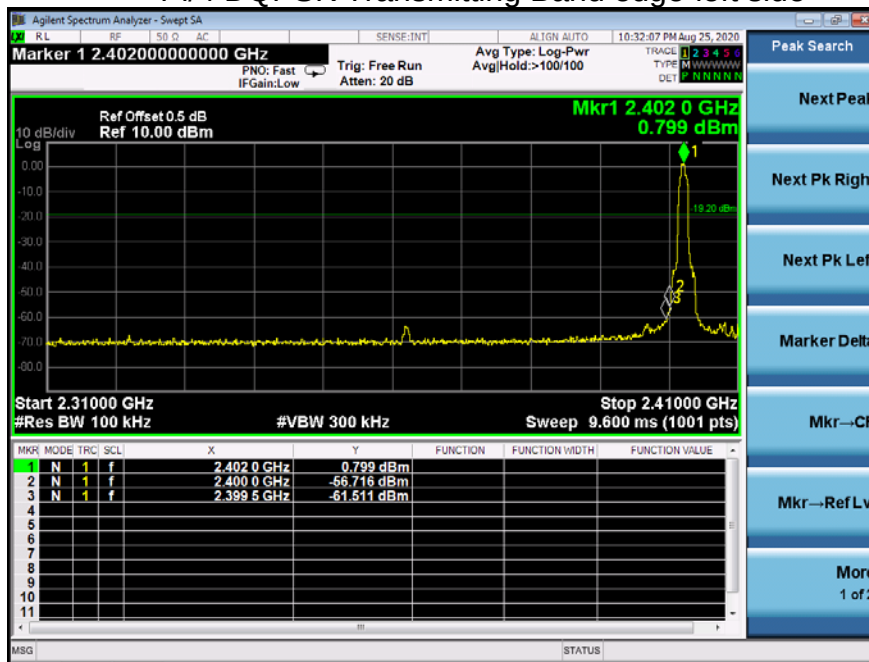
GFSK Transmitting Band edge-right side



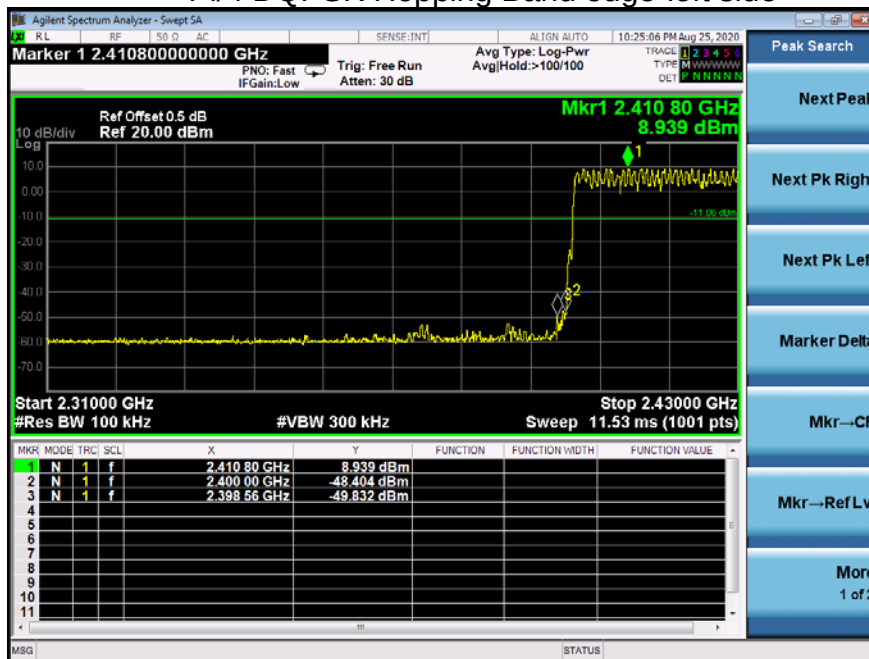
GFSK Hopping Band edge-right side



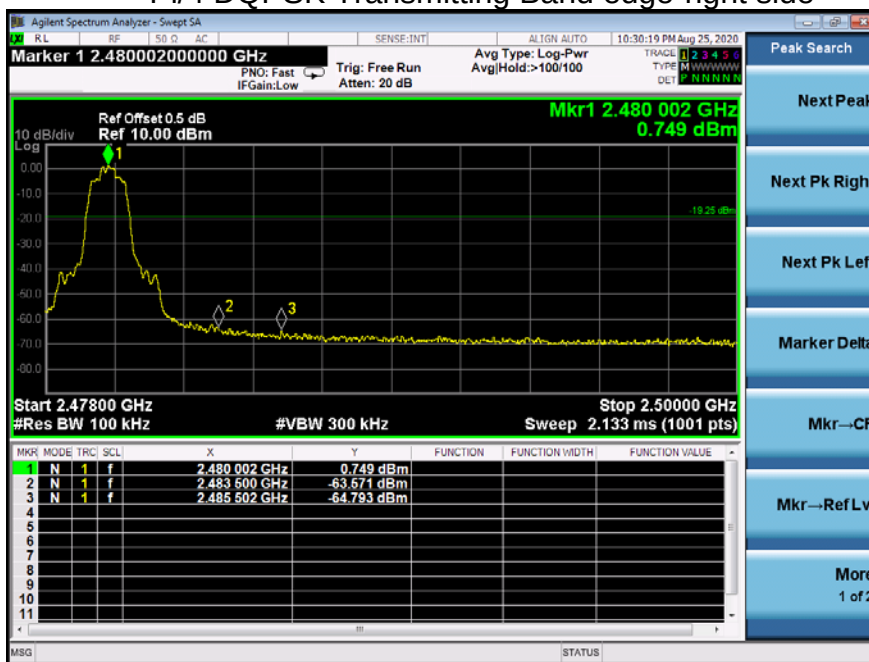
Pi/4 DQPSK Transmitting Band edge-left side



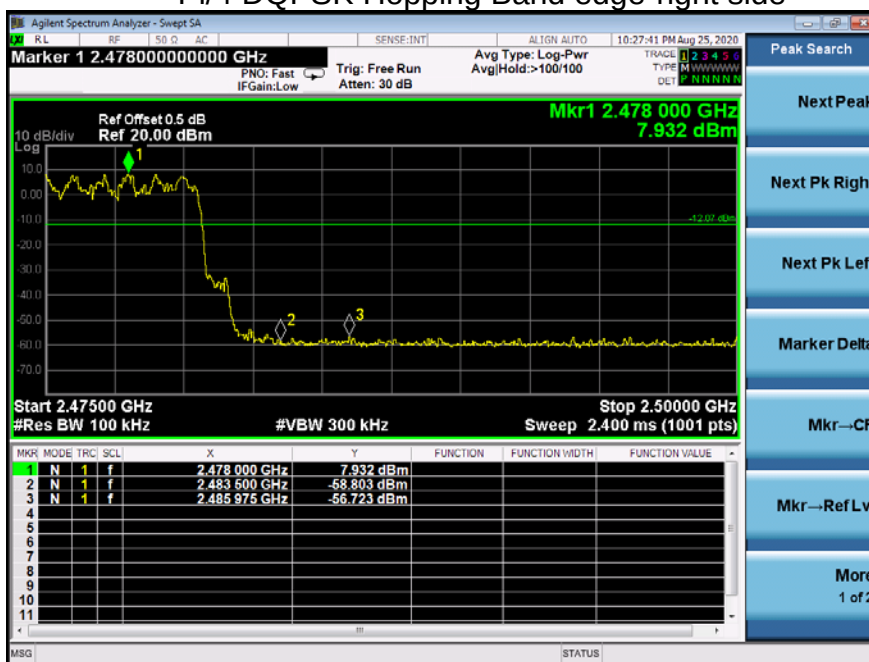
Pi/4 DQPSK Hopping Band edge-left side



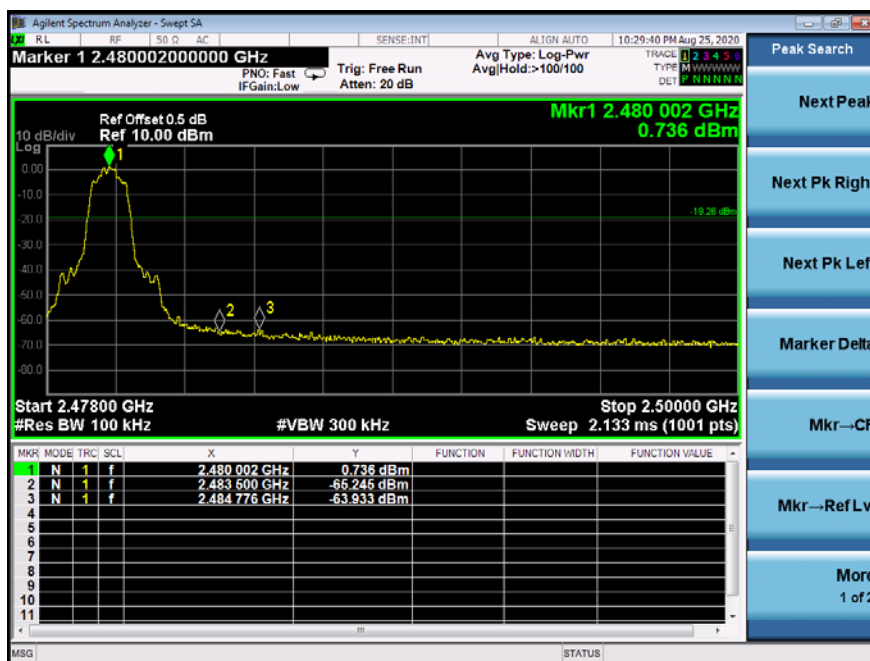
Pi/4 DQPSK Transmitting Band edge-right side



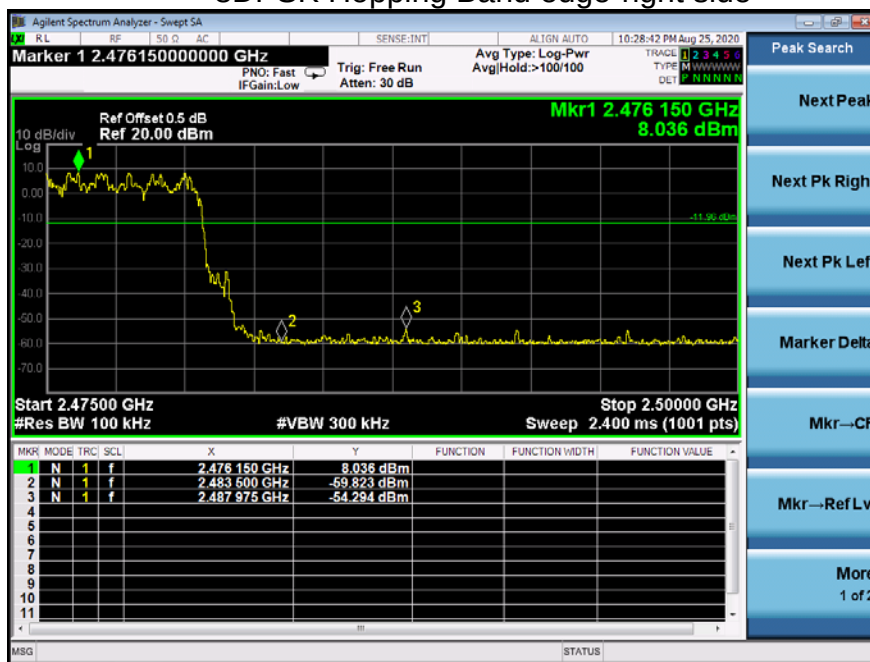
Pi/4 DQPSK Hopping Band edge-right side



8DPSK Transmitting Band edge-right side

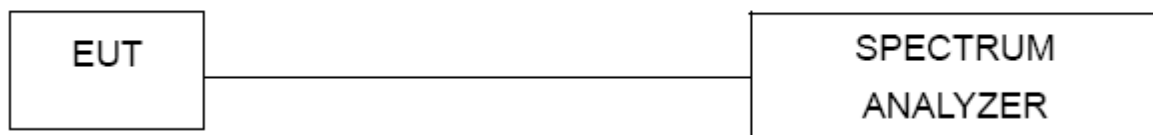


8DPSK Hopping Band edge-right side



10. 20 DB BANDWIDTH

10.1 Block Diagram Of Test Setup



10.2 Limit

N/A

10.3 Test procedure

1. Set RBW = 30kHz.
2. Set the video bandwidth (VBW) $\geq 3 \times$ RBW.
3. Detector = Peak.
4. Trace mode = max hold.
5. Sweep = auto couple.
6. Allow the trace to stabilize.
7. Measure the maximum width of the emission that is constrained by the frequencies associated with the two outermost amplitude points (upper and lower frequencies) that are attenuated by 6 dB relative to the maximum level measured in the fundamental emission. .

10.4 Test Result

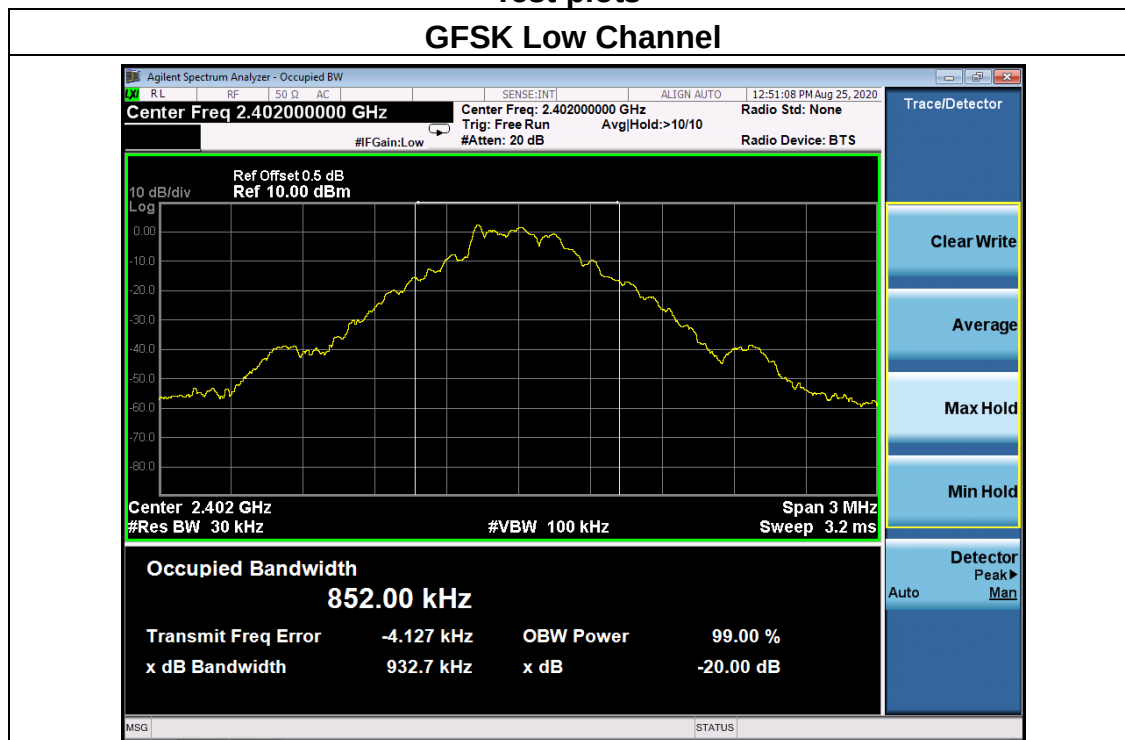
Temperature :	26°C	Relative Humidity :	54%
Test Voltage :	DC 3.7V	Remark	N/A

Left

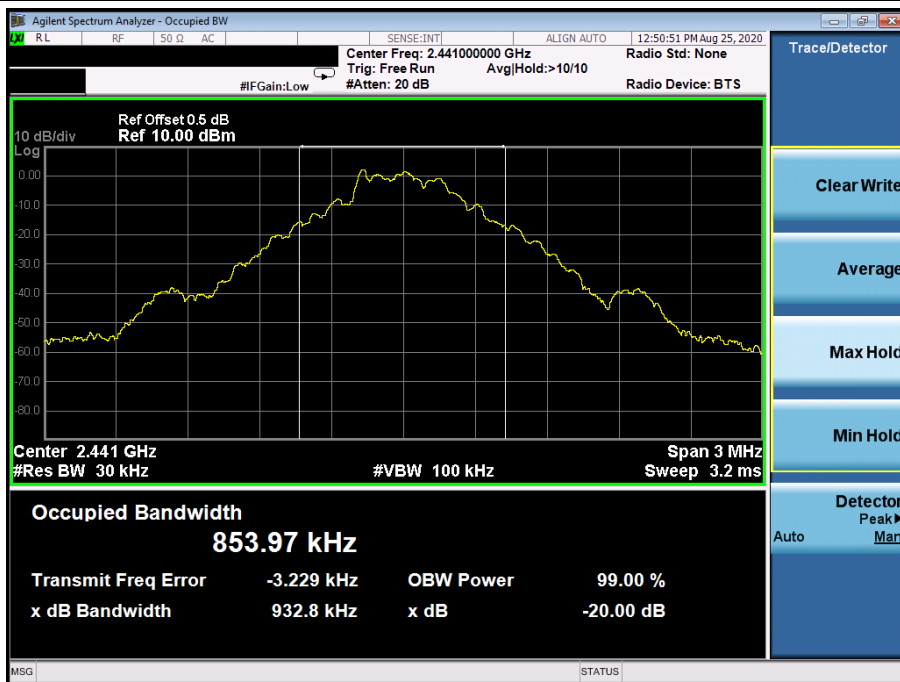
Modulation	Test Channel	Bandwidth(MHz)
GFSK	Low	0.933
GFSK	Middle	0.933
GFSK	High	0.933
Pi/4 DQPSK	Low	1.248
Pi/4 DQPSK	Middle	1.247
Pi/4 DQPSK	High	1.254
8DPSK	Low	1.224
8DPSK	Middle	1.221
8DPSK	High	1.220

Test plots

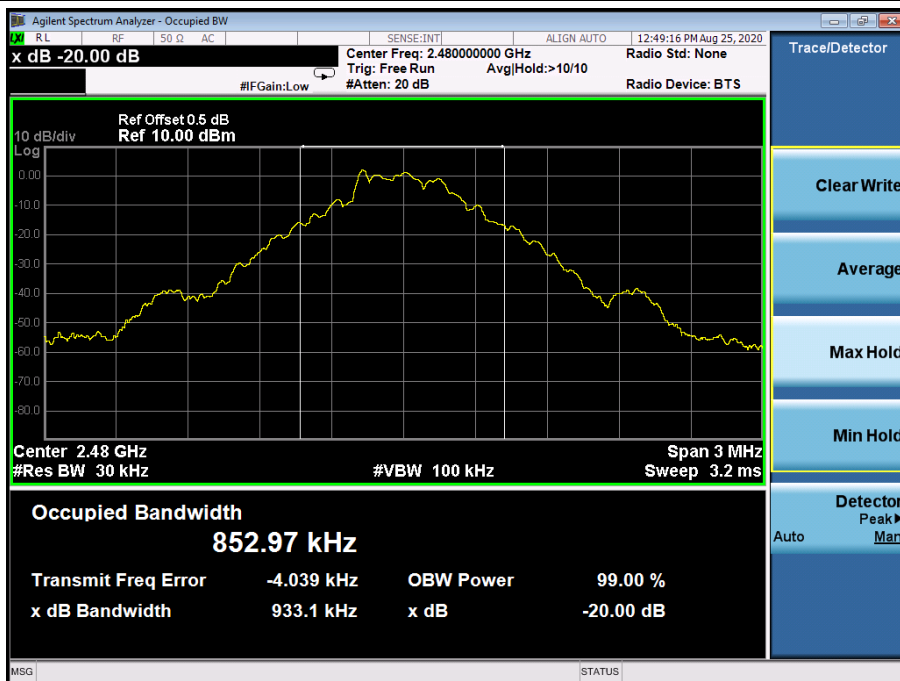
GFSK Low Channel



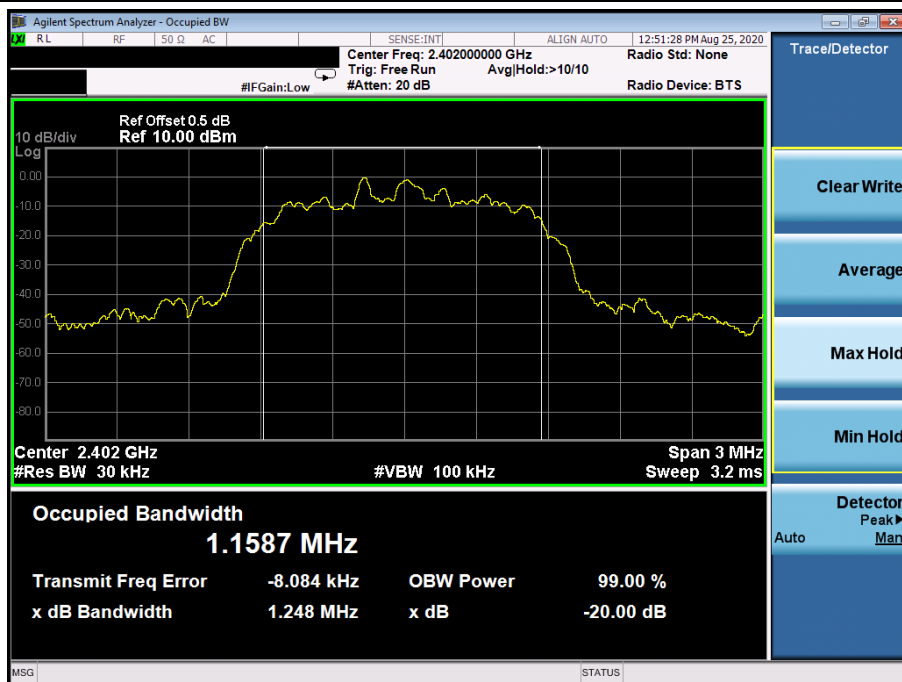
GFSK Middle Channel



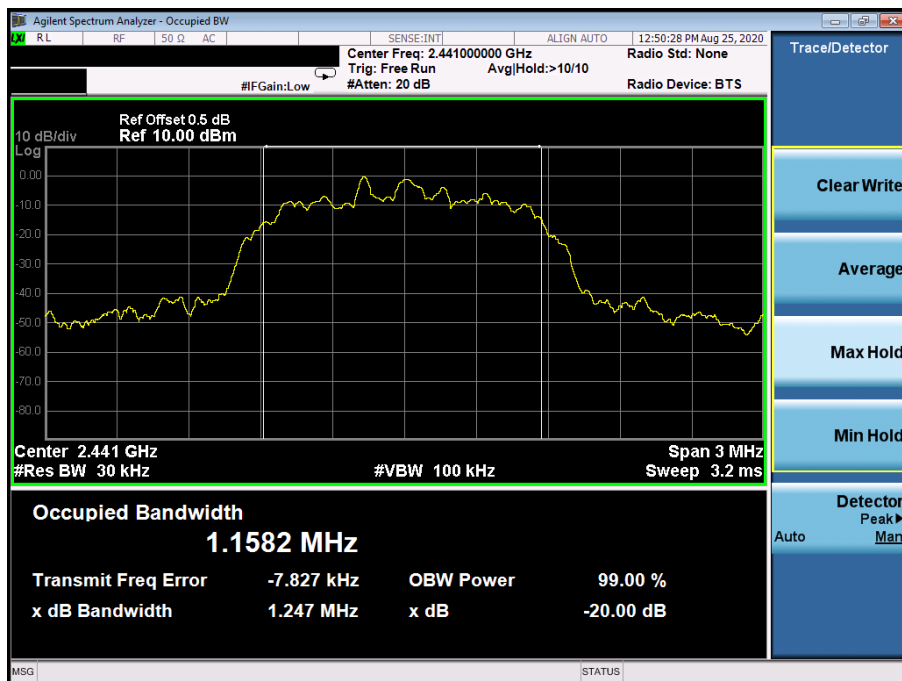
GFSK High Channel



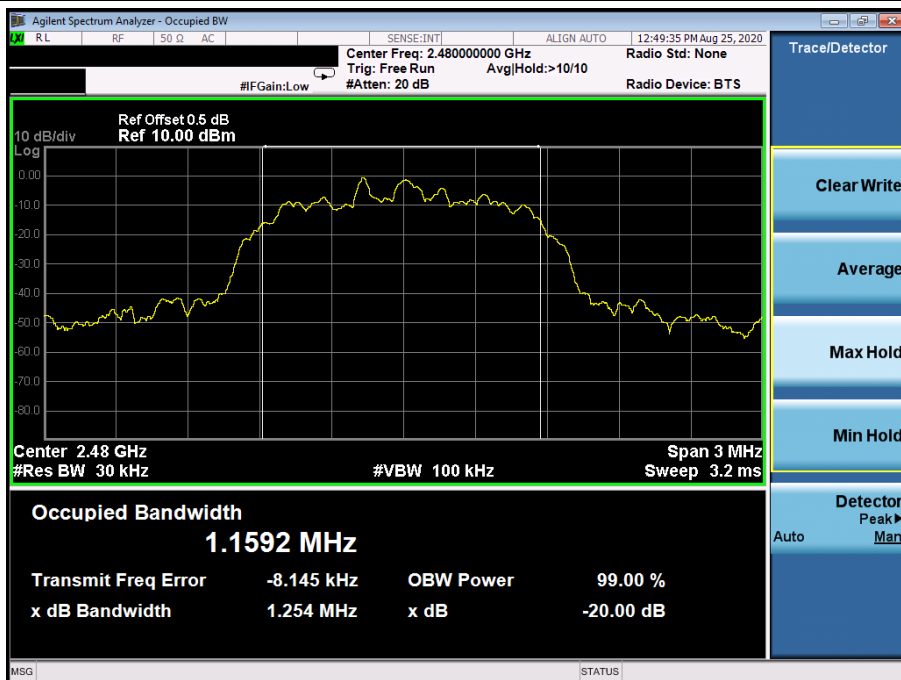
Pi/4 DQPSK Low Channel



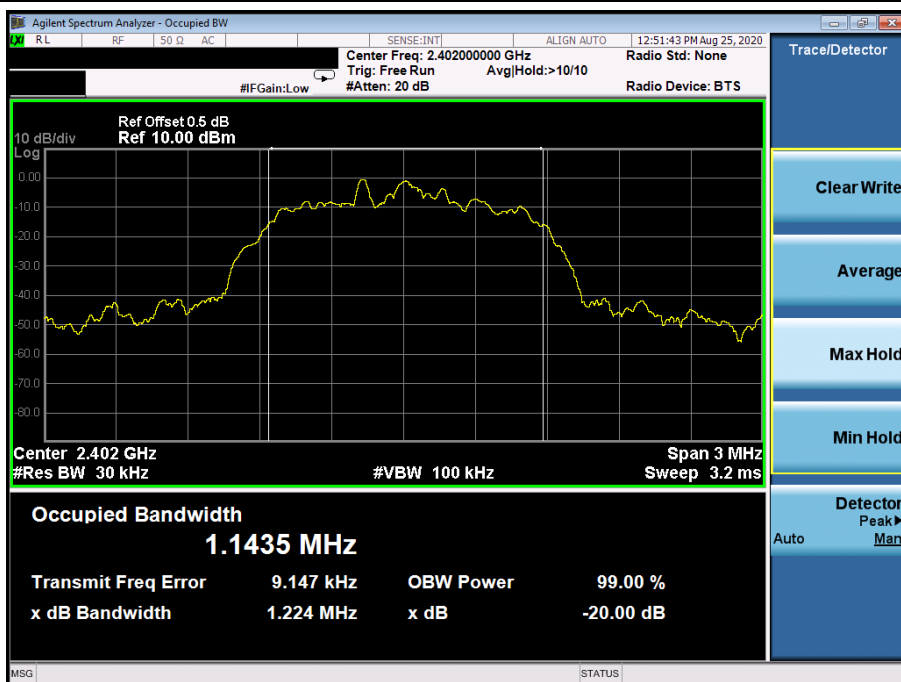
Pi/4 DQPSK Middle Channel



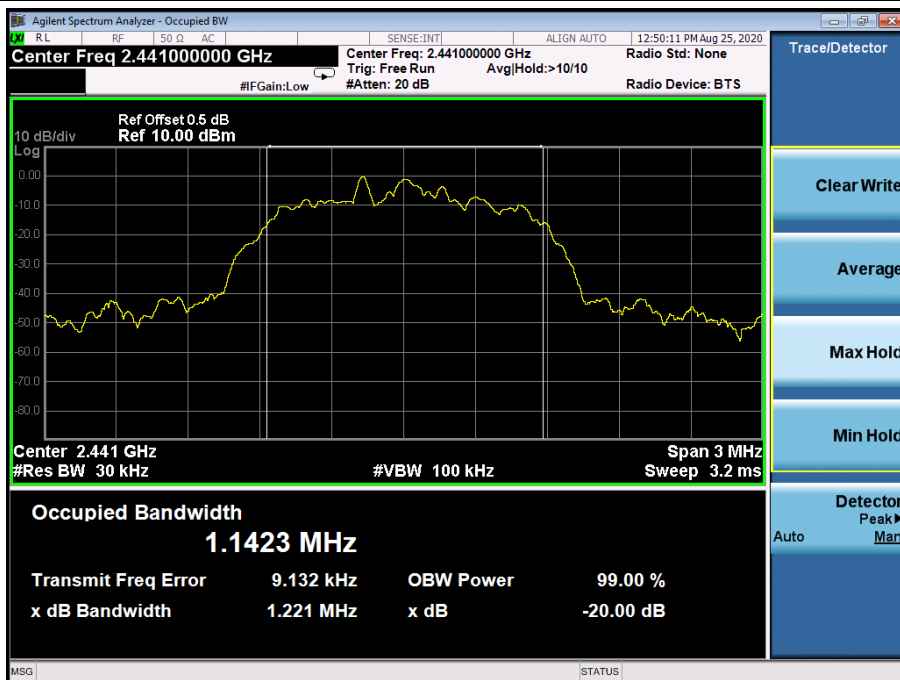
Pi/4 DQPSK High Channel



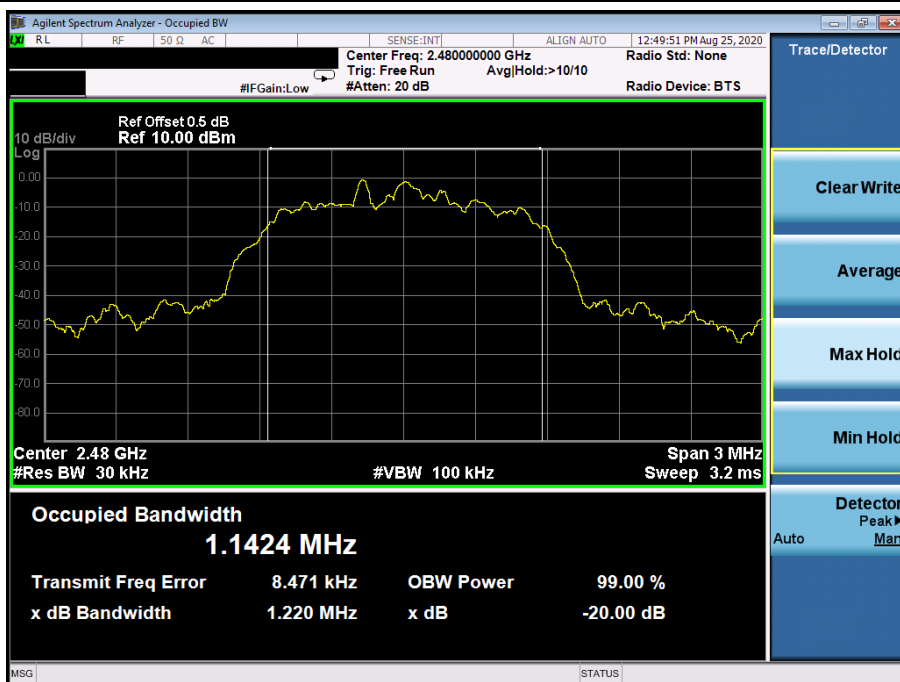
8DPSK Low Channel



8DPSK Middle Channel



8DPSK High Channel

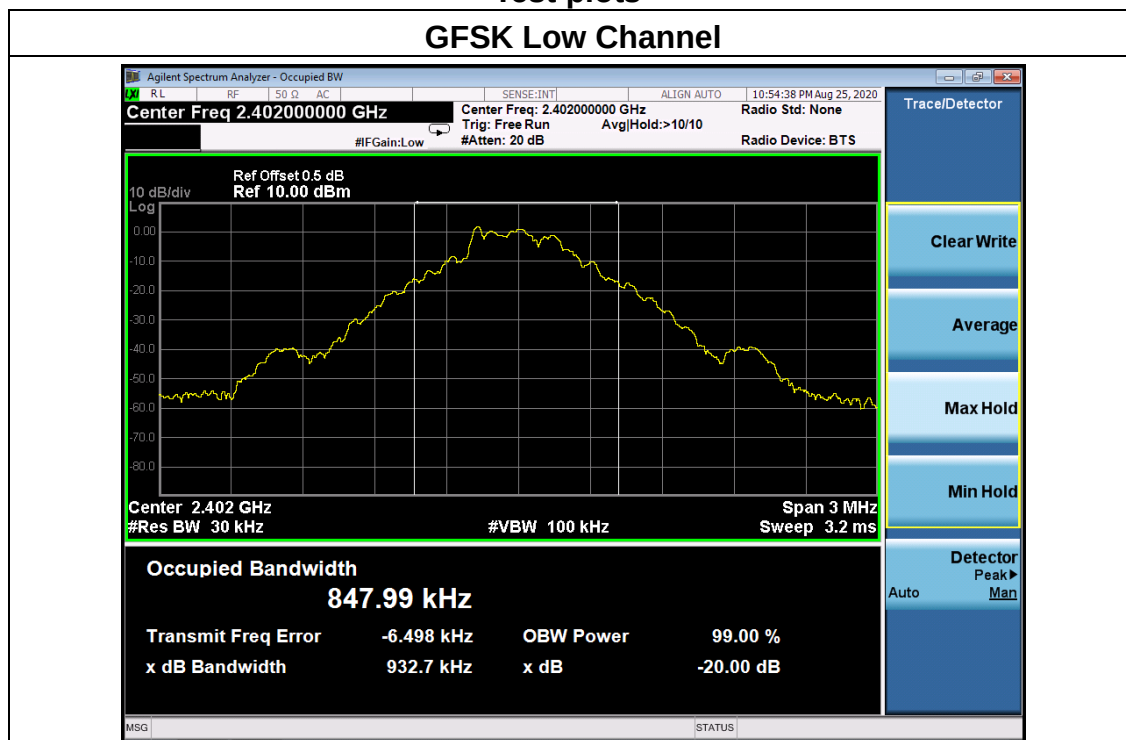


Right

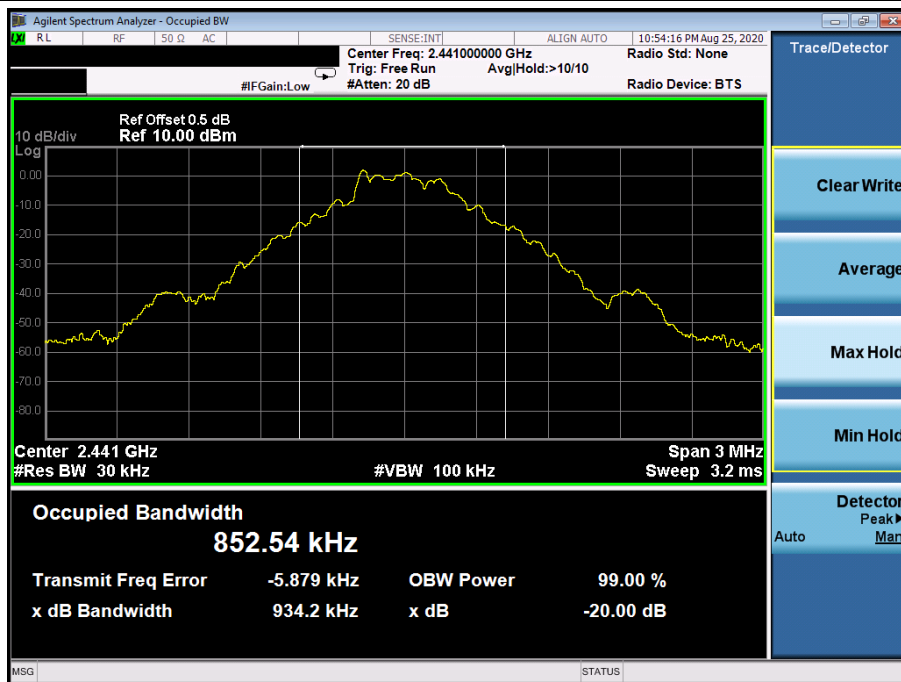
Modulation	Test Channel	Bandwidth(MHz)
GFSK	Low	0.933
GFSK	Middle	0.934
GFSK	High	0.932
Pi/4 DQPSK	Low	1.258
Pi/4 DQPSK	Middle	1.252
Pi/4 DQPSK	High	1.246
8DPSK	Low	1.220
8DPSK	Middle	1.222
8DPSK	High	1.221

Test plots

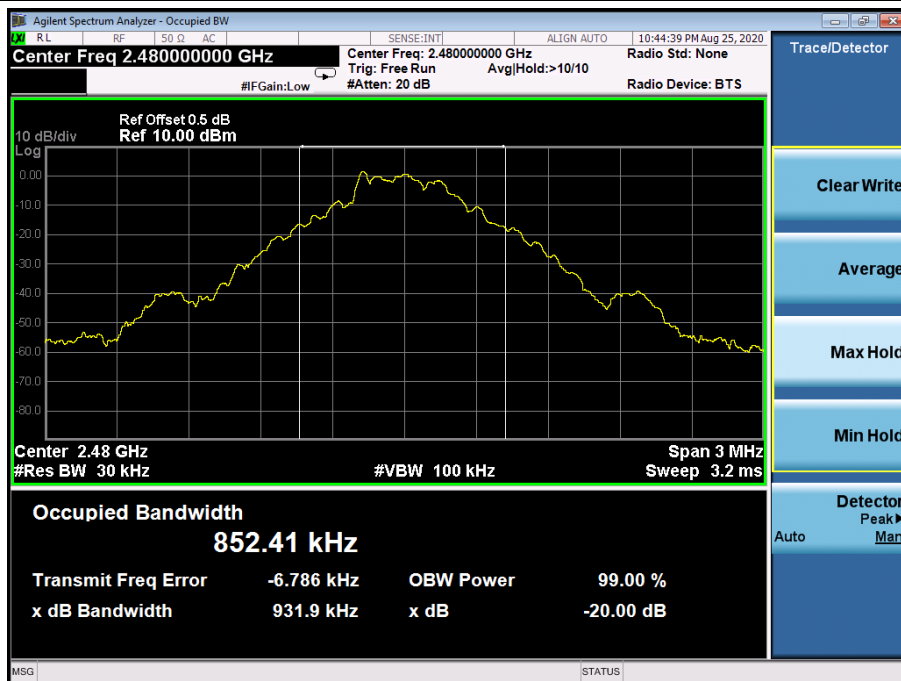
GFSK Low Channel



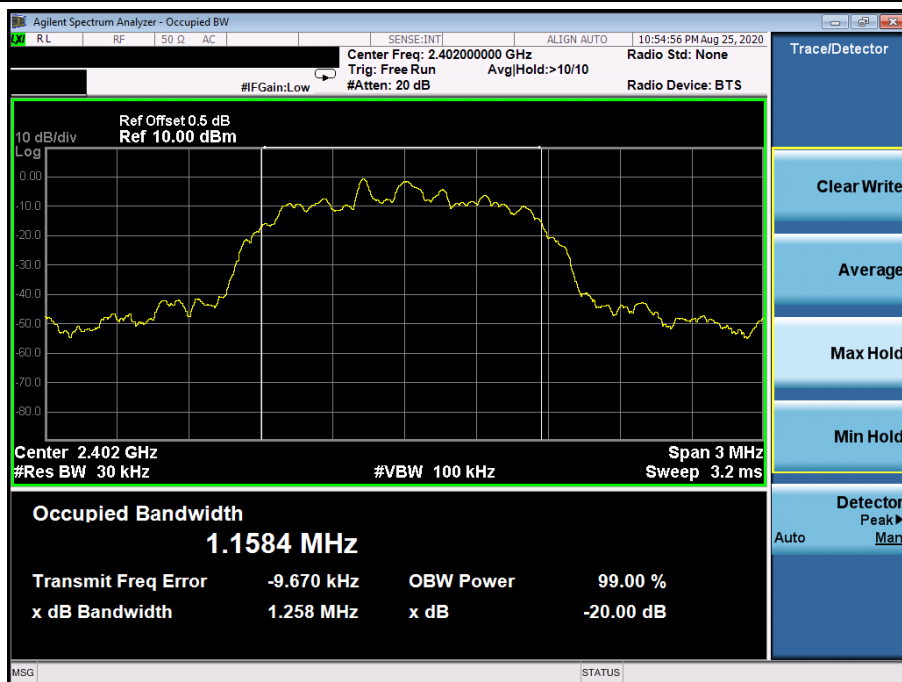
GFSK Middle Channel



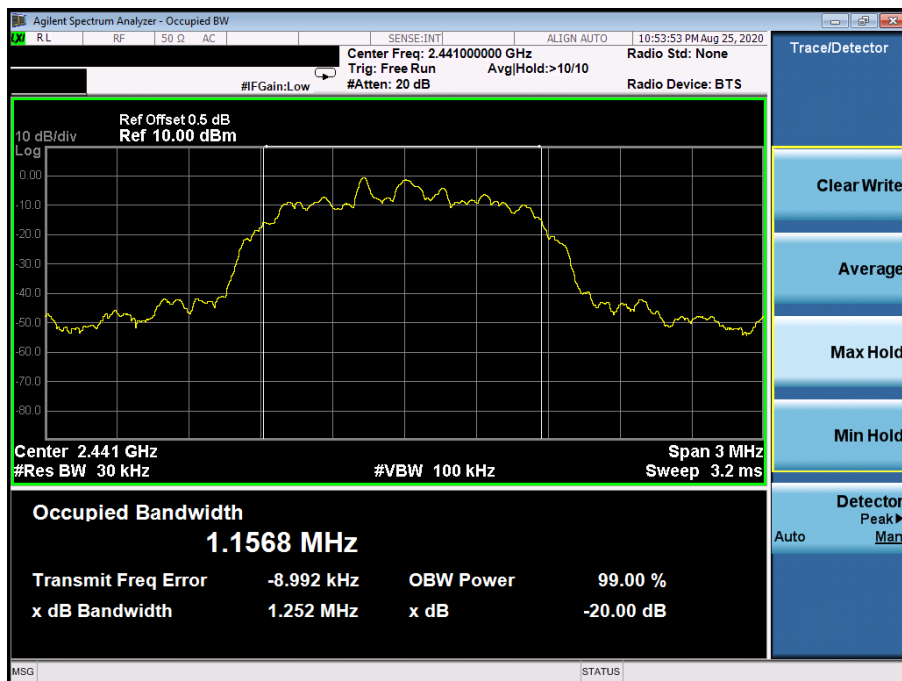
GFSK High Channel



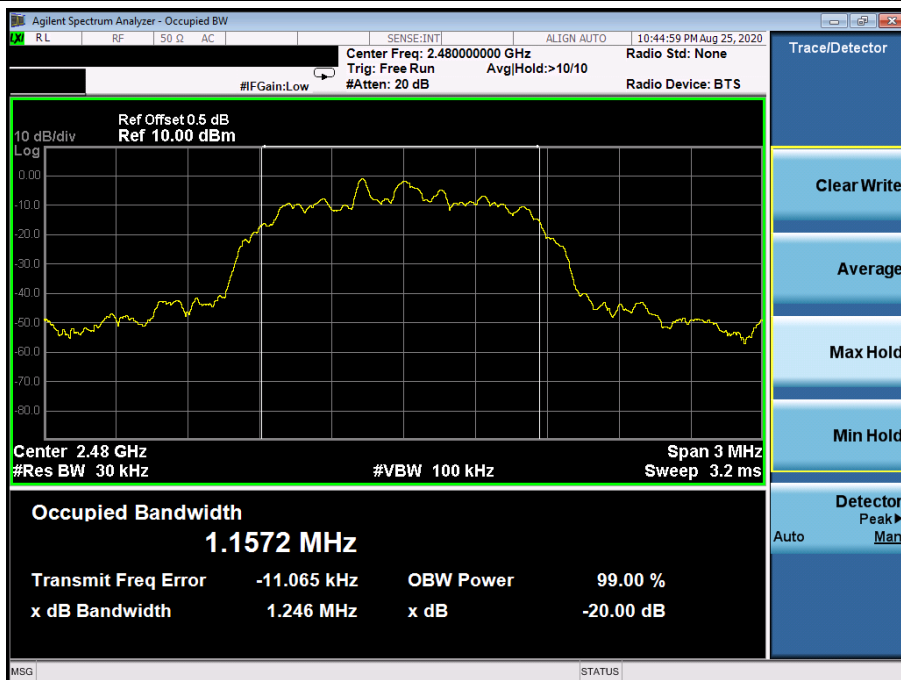
Pi/4 DQPSK Low Channel



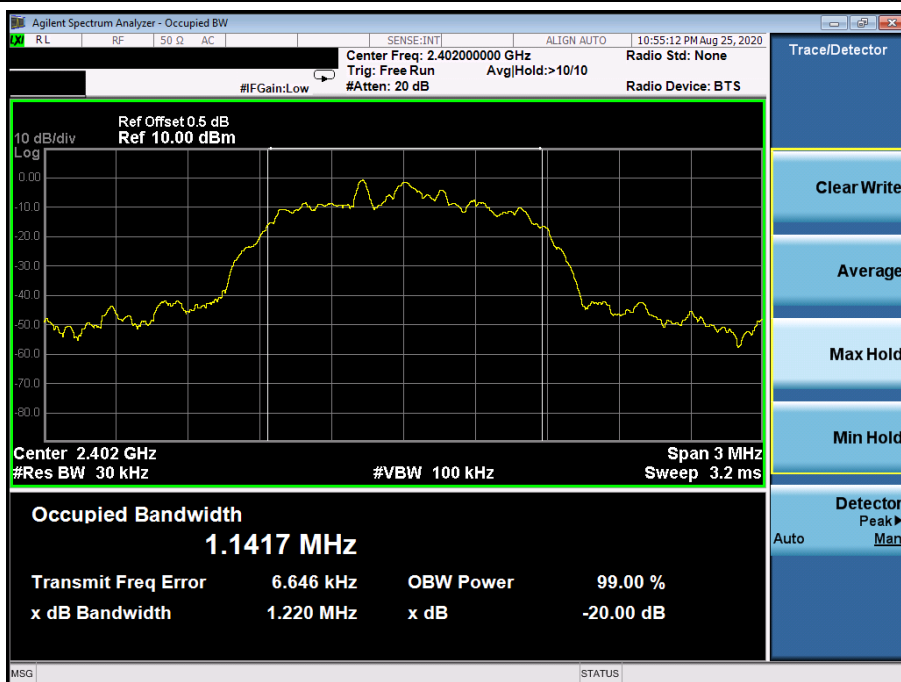
Pi/4 DQPSK Middle Channel



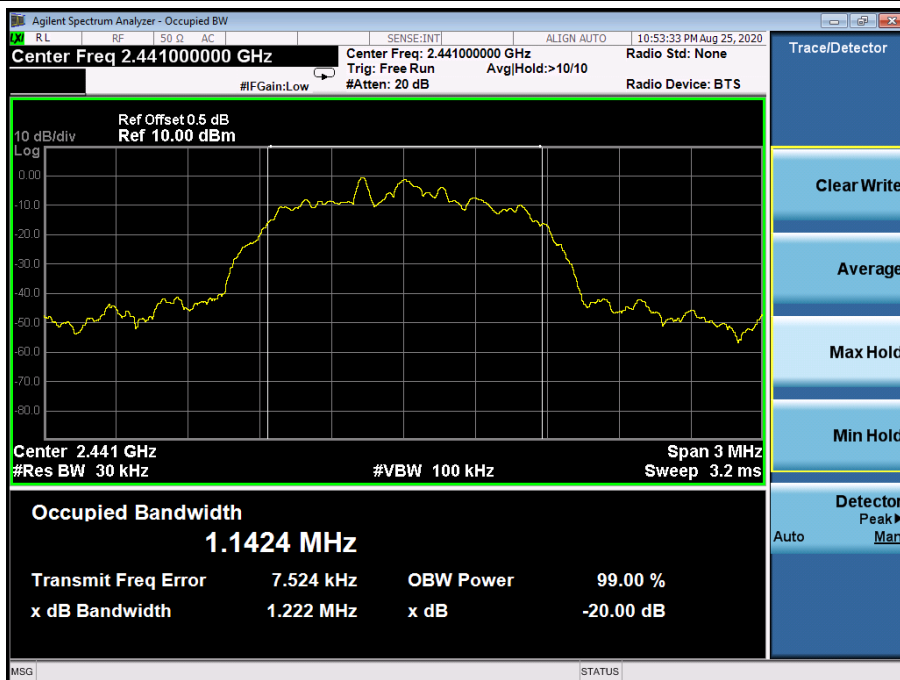
Pi/4 DQPSK High Channel



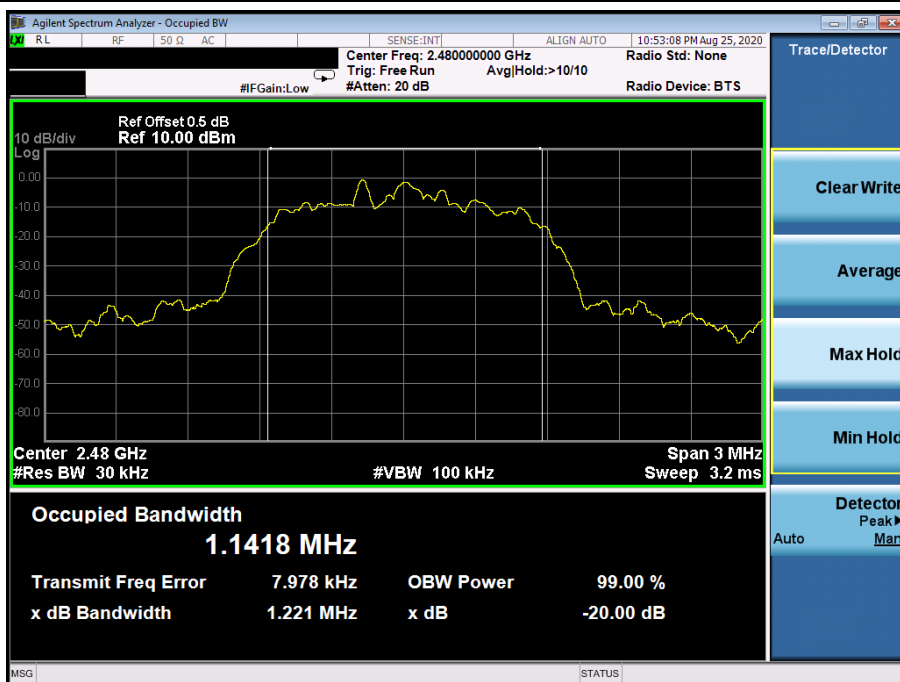
8DPSK Low Channel



8DPSK Middle Channel

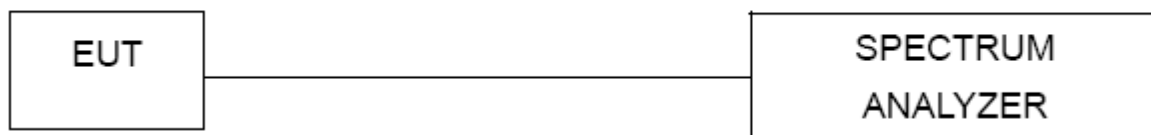


8DPSK High Channel



11. MAXIMUM PEAK OUTPUT POWER

11.1 Block Diagram Of Test Setup



11.2 Limit

FCC Part15 (15.247) , Subpart C				
Section	Test Item	Limit	Frequency Range (MHz)	Result
15.247(b)(1)	Peak Output Power	0.125 watt or 21dBm	2400-2483.5	PASS

11.3 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 3MHz. VBW = 3MHz. Sweep = auto; Detector Function = Peak.
3. Keep the EUT in transmitting at lowest, medium and highest channel individually. Record the max value.

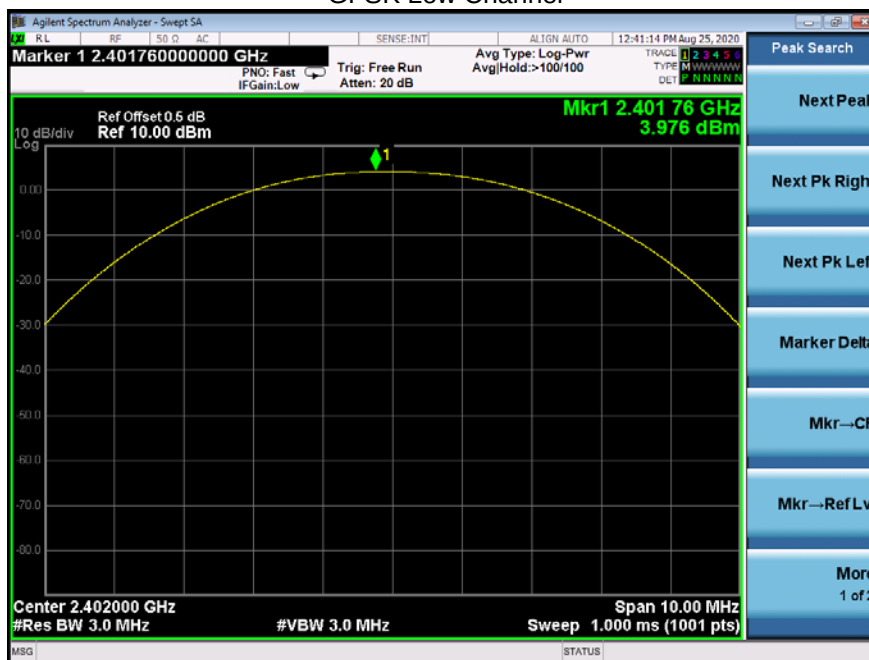
11.4 Test Result

Temperature :	26°C	Relative Humidity :	54%
Test Voltage :	DC 3.7V	Remark:	N/A

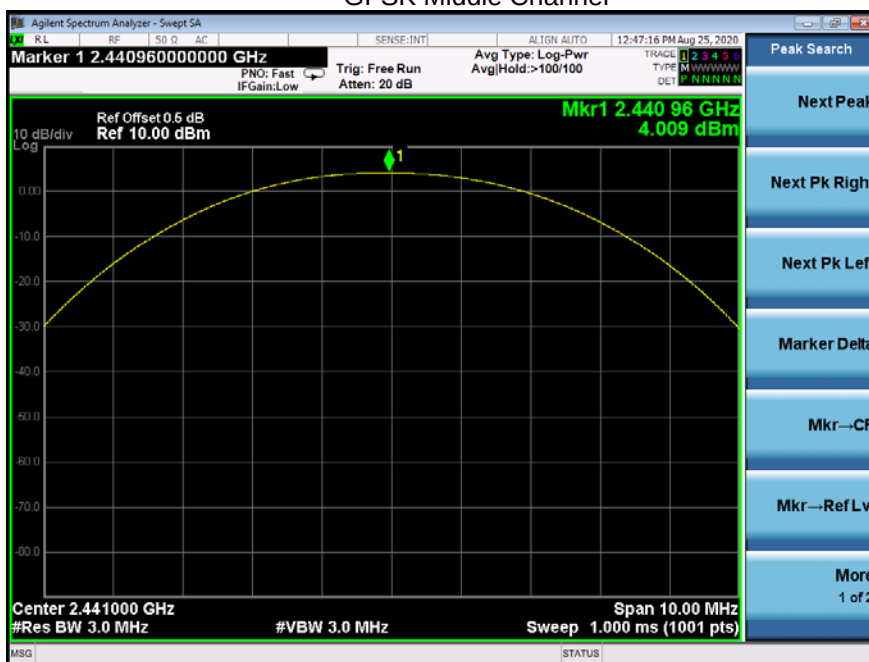
Left

Modulation	Test Channel	Output Power (dBm)	Limit (dBm)
GFSK	Low	3.98	21
GFSK	Middle	4.01	21
GFSK	High	3.62	21
Pi/4 DQPSK	Low	2.70	21
Pi/4 DQPSK	Middle	2.69	21
Pi/4 DQPSK	High	2.41	21
8DPSK	Low	3.43	21
8DPSK	Middle	3.29	21
8DPSK	High	3.03	21

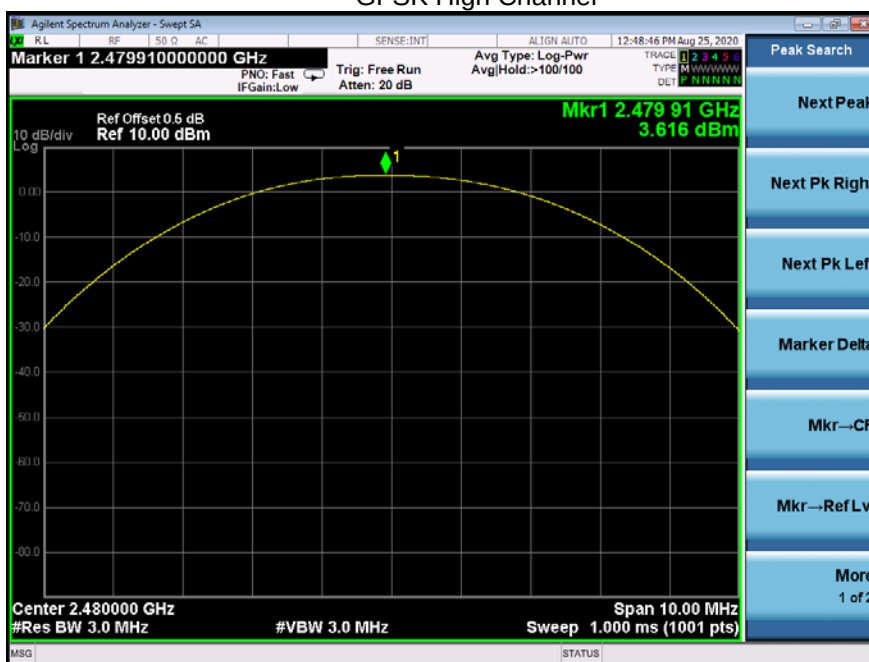
Test plots
GFSK Low Channel



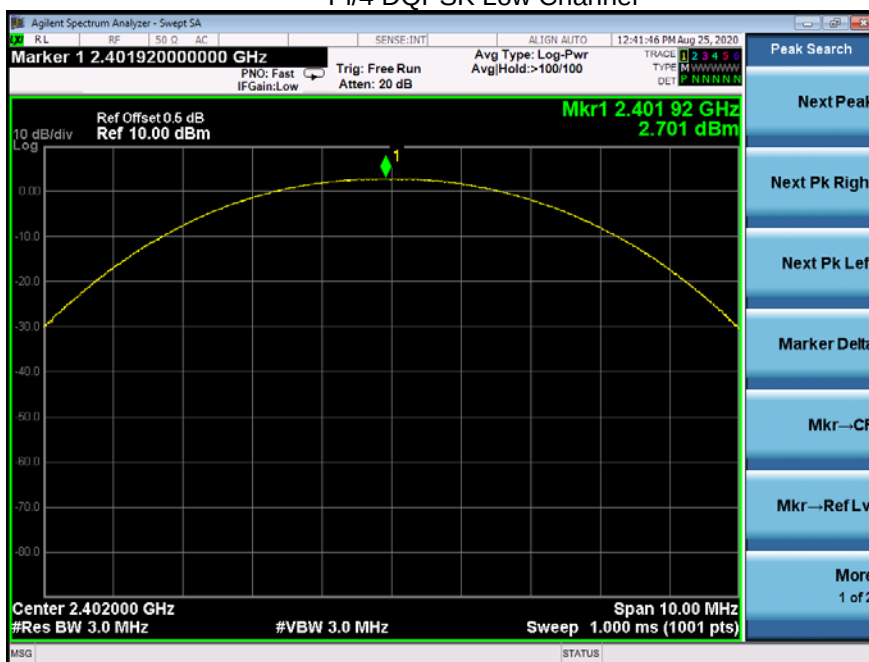
GFSK Middle Channel



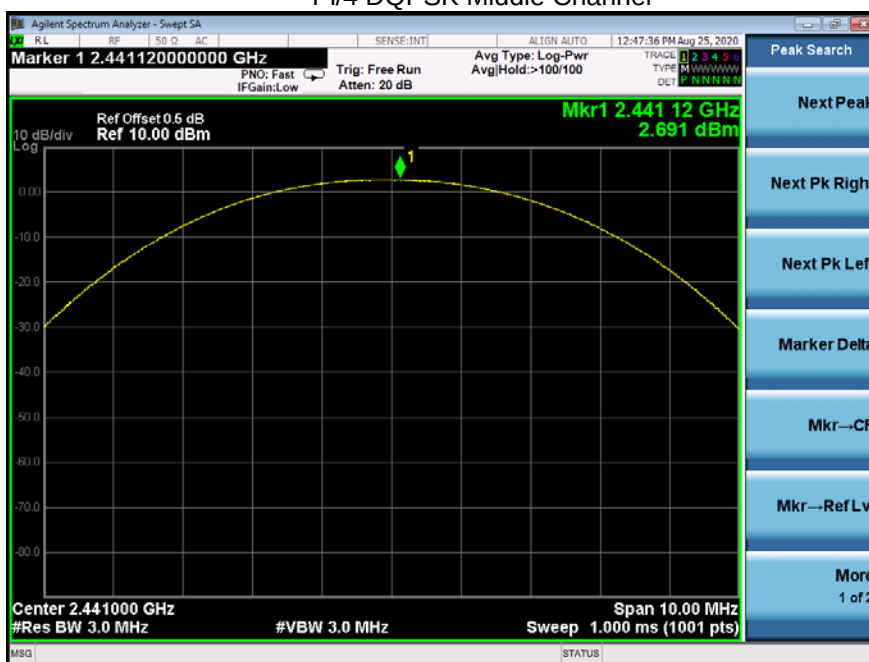
GFSK High Channel



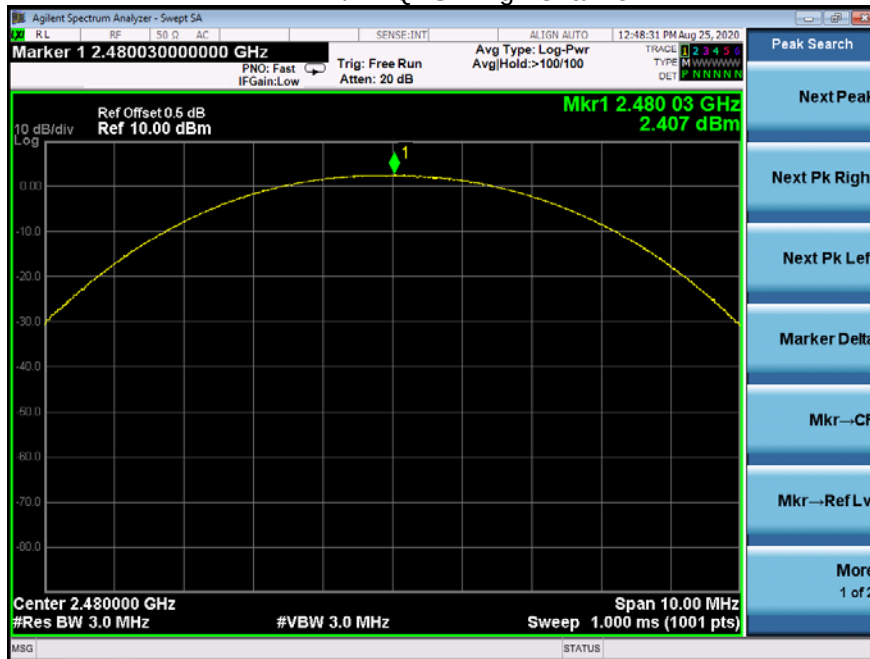
Pi/4 DQPSK Low Channel



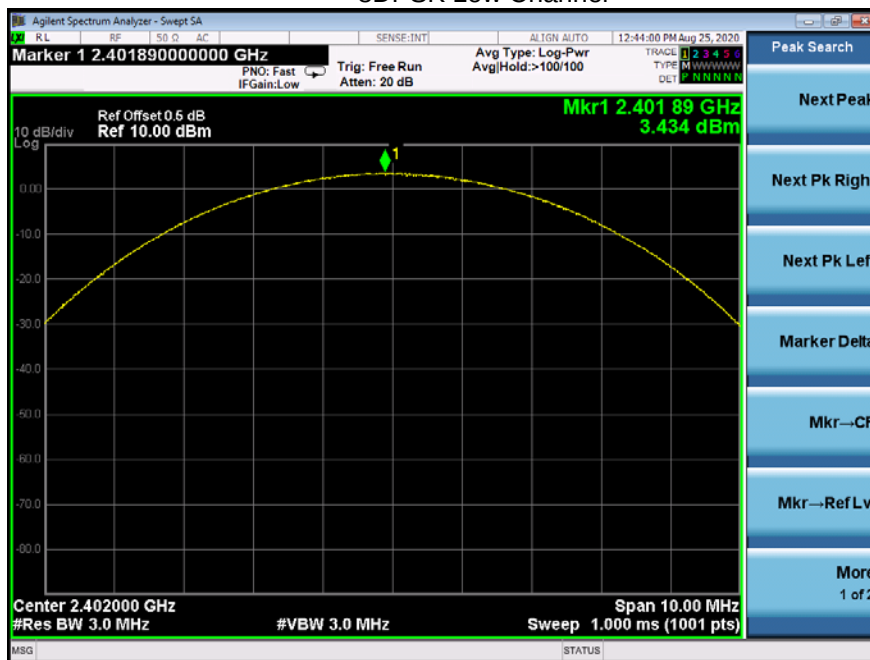
Pi/4 DQPSK Middle Channel



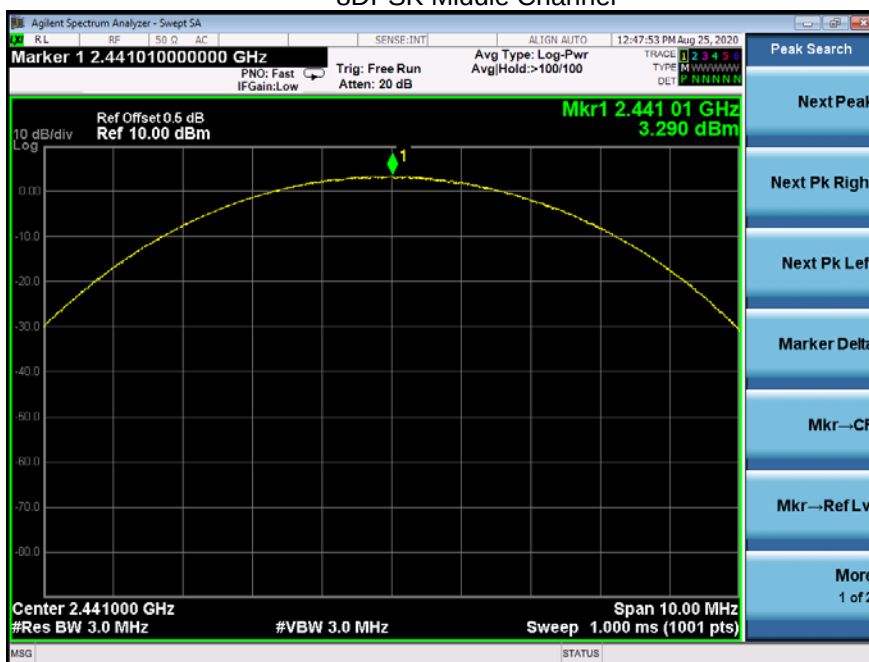
Pi/4 DQPSK High Channel



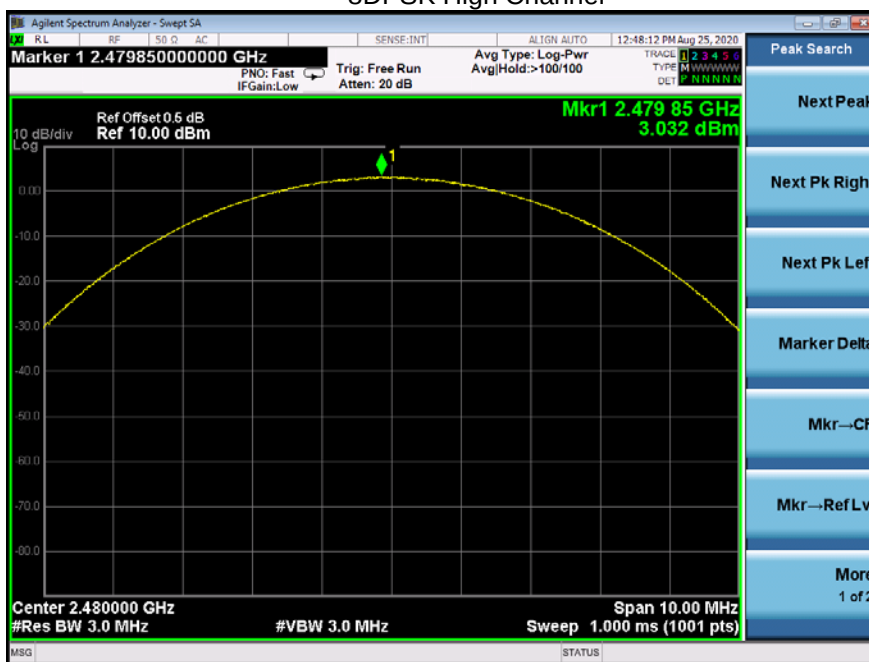
8DPSK Low Channel



8DPSK Middle Channel



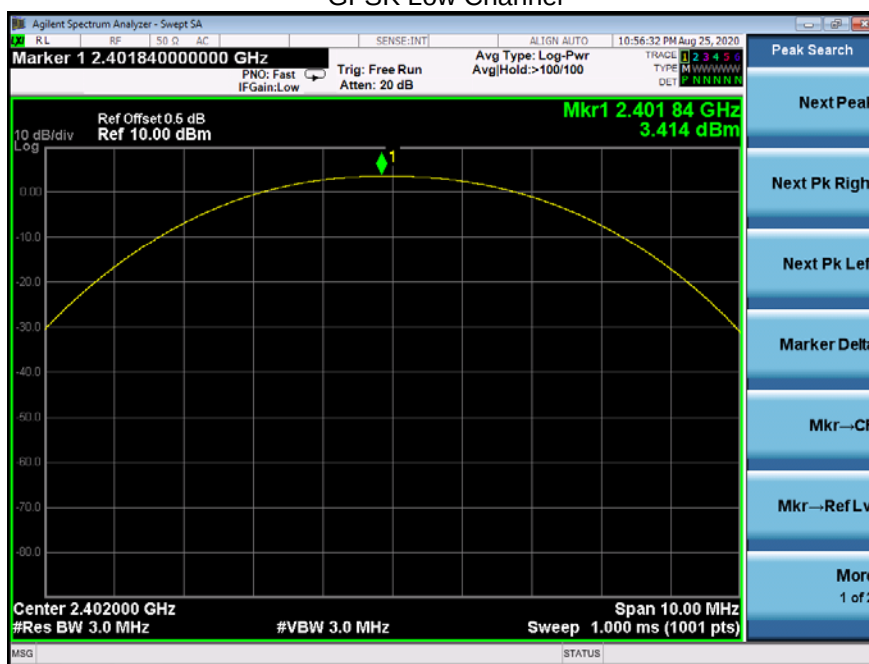
8DPSK High Channel



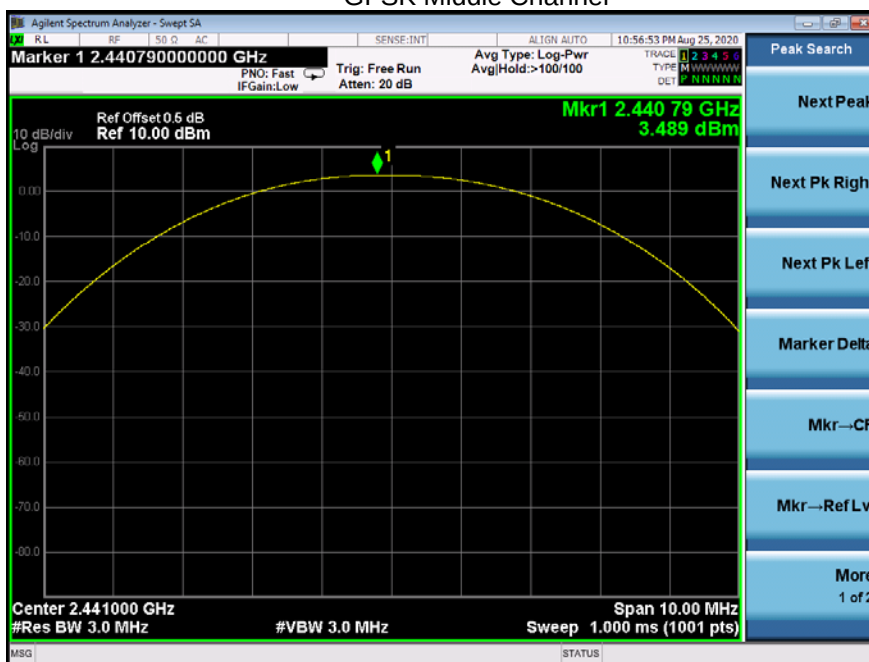
Right

Modulation	Test Channel	Output Power (dBm)	Limit (dBm)
GFSK	Low	3.41	21
GFSK	Middle	3.49	21
GFSK	High	3.22	21
Pi/4 DQPSK	Low	2.19	21
Pi/4 DQPSK	Middle	2.24	21
Pi/4 DQPSK	High	1.95	21
8DPSK	Low	2.75	21
8DPSK	Middle	2.86	21
8DPSK	High	2.60	21

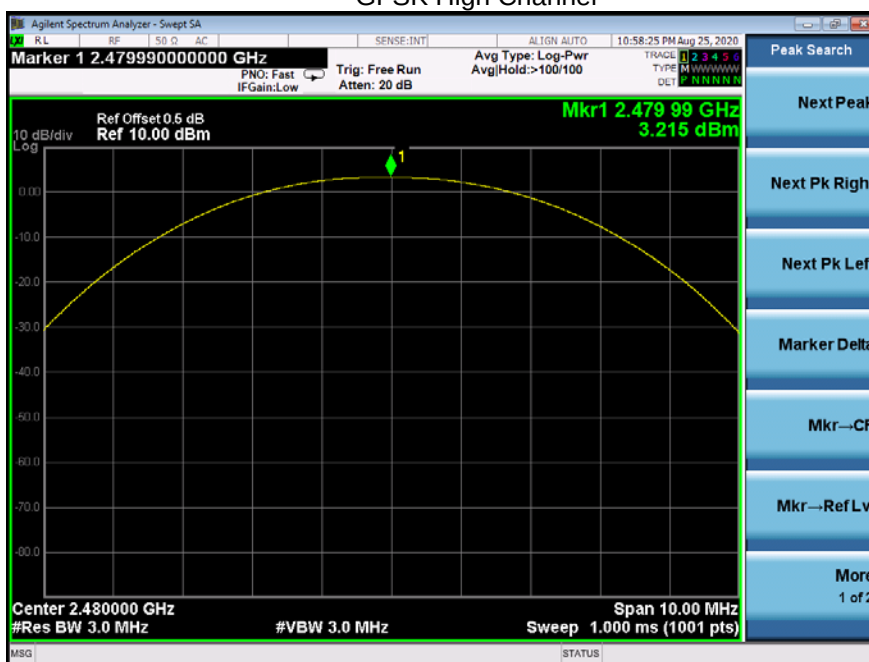
Test plots
GFSK Low Channel



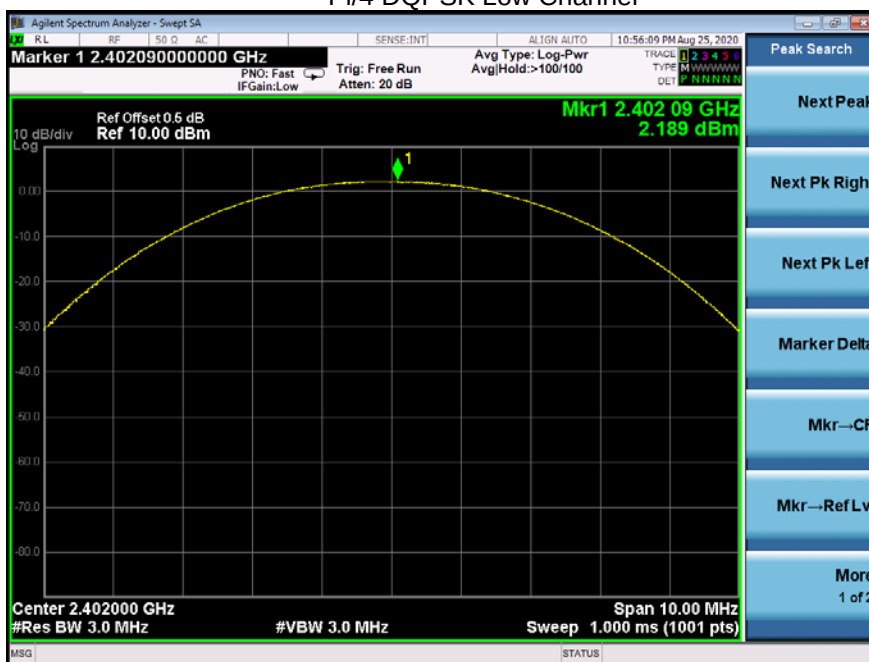
GFSK Middle Channel



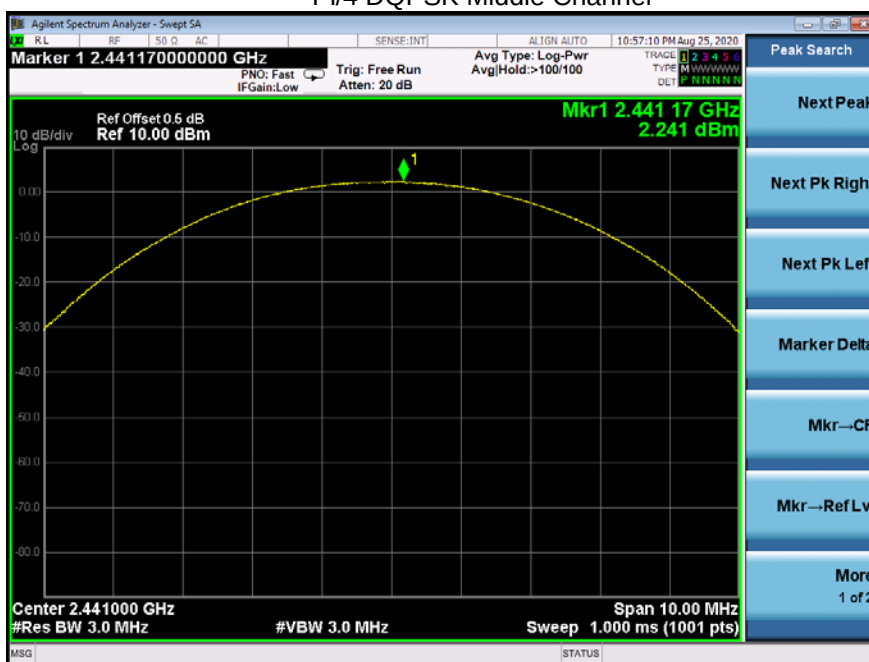
GFSK High Channel



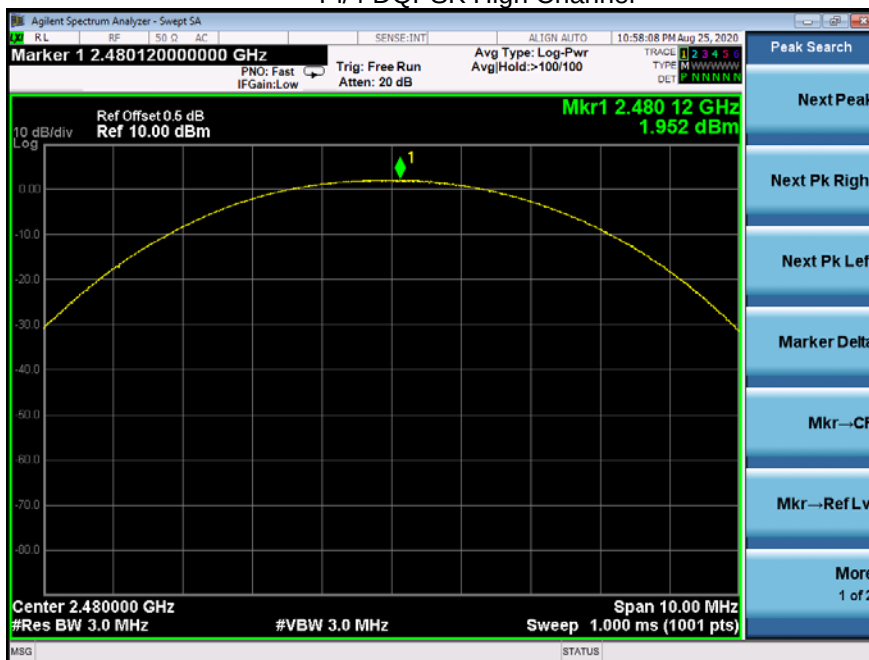
Pi/4 DQPSK Low Channel



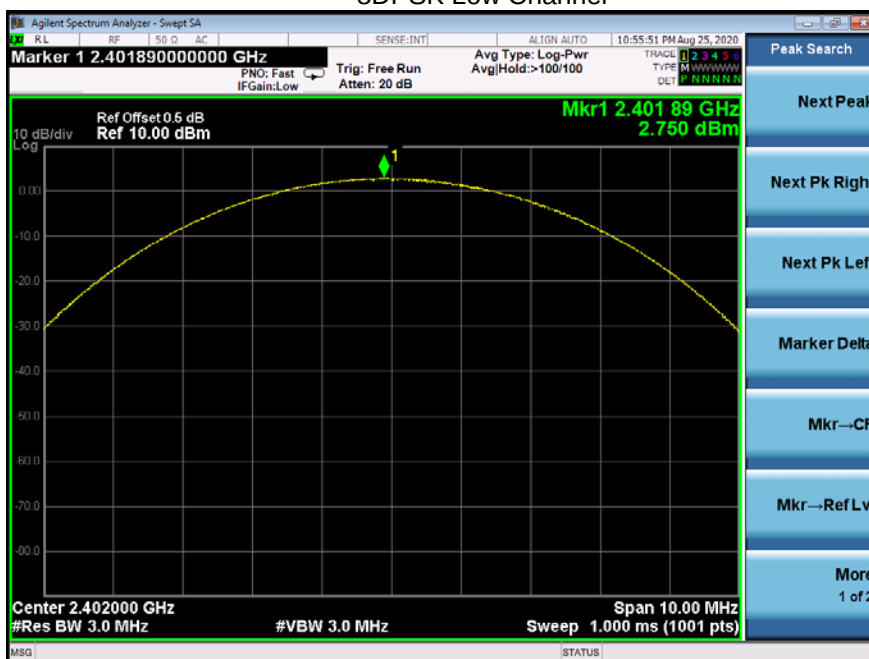
Pi/4 DQPSK Middle Channel



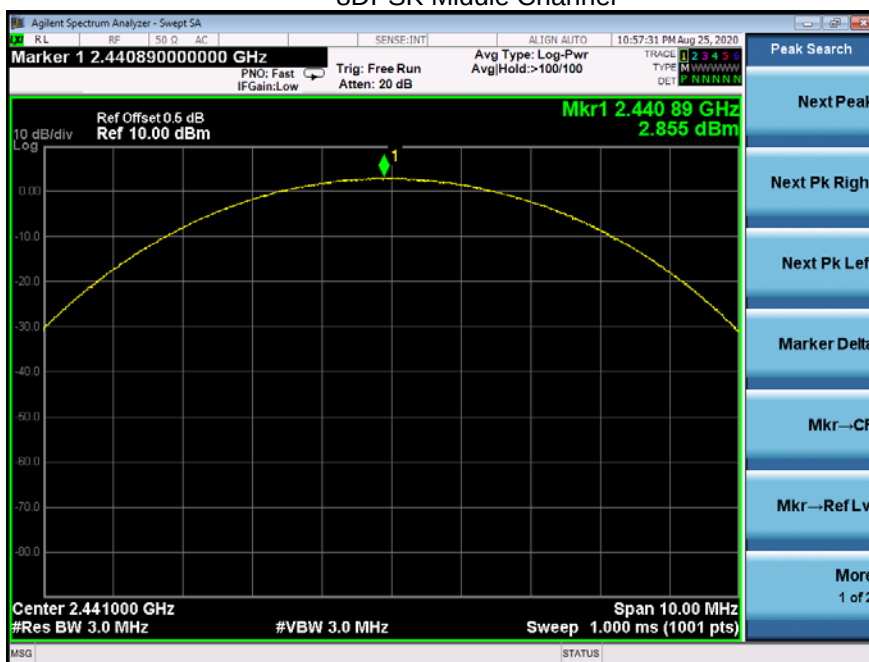
Pi/4 DQPSK High Channel



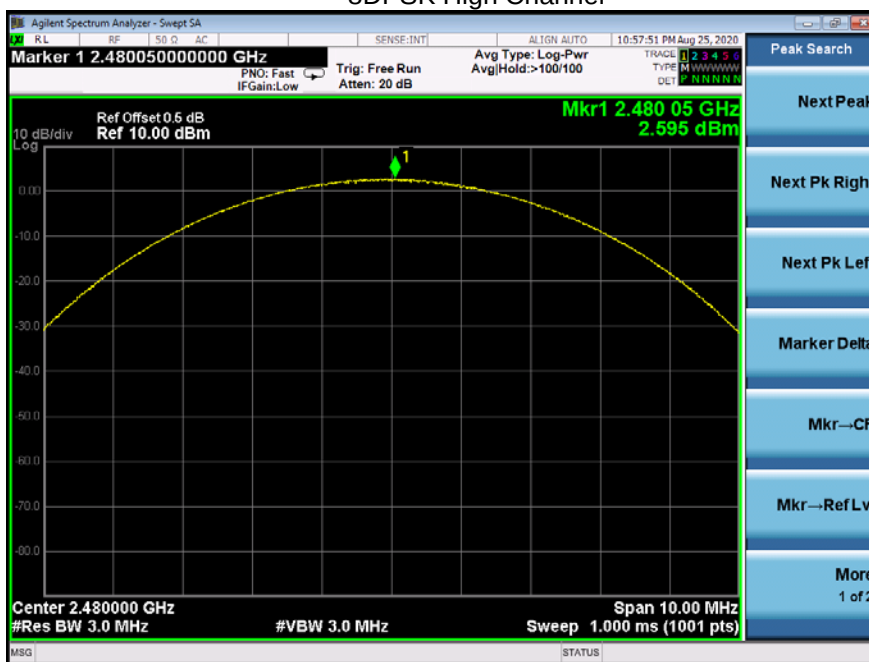
8DPSK Low Channel



8DPSK Middle Channel

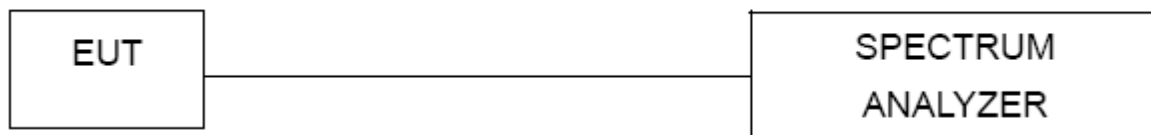


8DPSK High Channel



12. HOPPING CHANNEL SEPARATION

12.1 Block Diagram Of Test Setup



12.2 Limit

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20 dB bandwidth of the hopping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400-2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 0.125W.

12.3 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 30kHz. VBW = 100kHz , Span = 2.0MHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. Use the marker-delta function to determine the separation between the peaks of the adjacent channels. The limit is specified in one of the subparagraphs of this Section Submit this plot.

12.4 Test Result

Left

Modulation	Test Channel	Separation (MHz)	Limit(MHz)	Result
GFSK	Low	1.000	0.622	PASS
GFSK	Middle	1.002	0.622	PASS
GFSK	High	1.000	0.622	PASS
Pi/4 DQPSK	Low	1.000	0.832	PASS
Pi/4 DQPSK	Middle	1.000	0.831	PASS
Pi/4 DQPSK	High	1.000	0.836	PASS
8DPSK	Low	0.998	0.816	PASS
8DPSK	Middle	1.000	0.814	PASS
8DPSK	High	1.000	0.813	PASS

Test plots
GFSK Low Channel



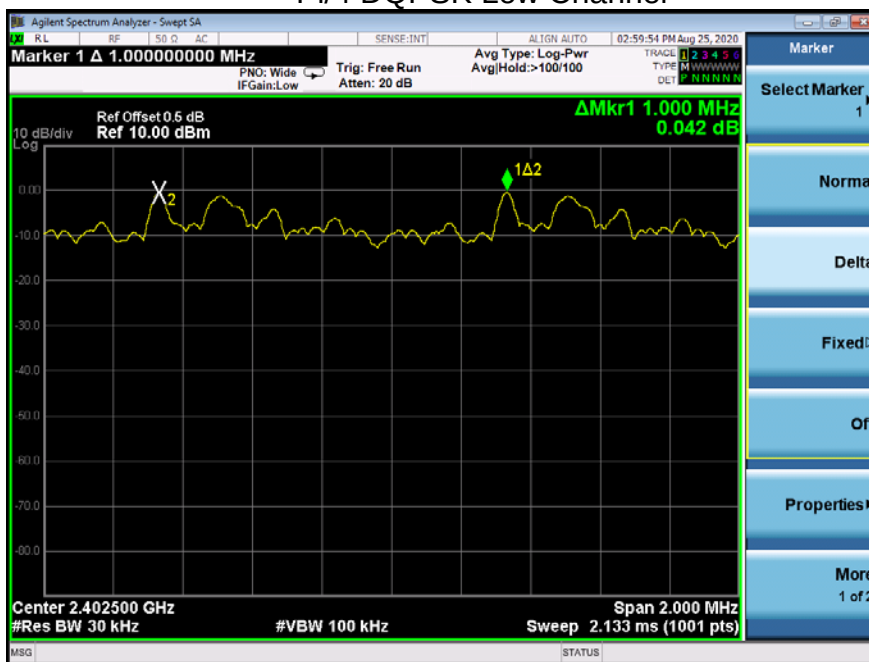
GFSK Middle Channel



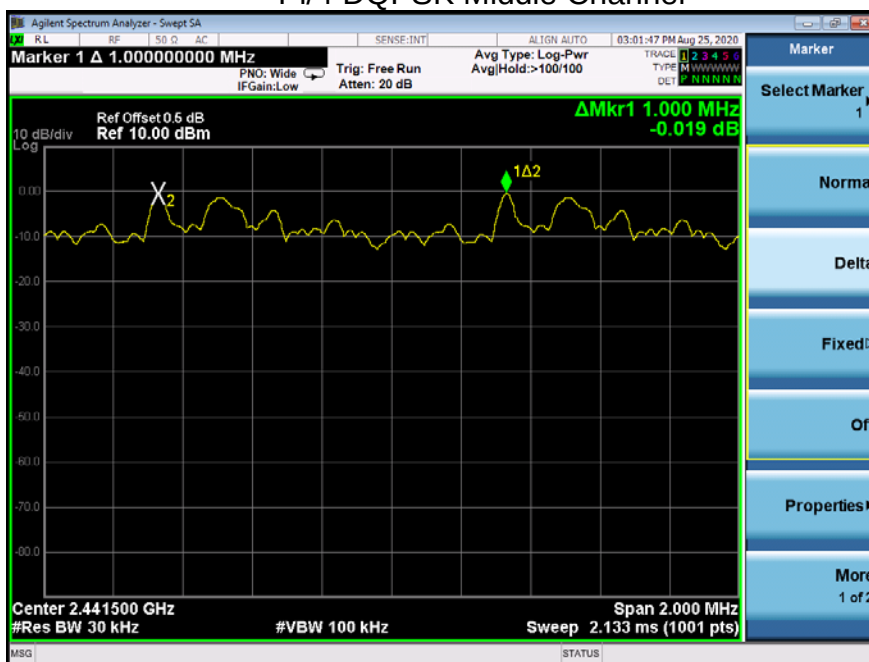
GFSK High Channel



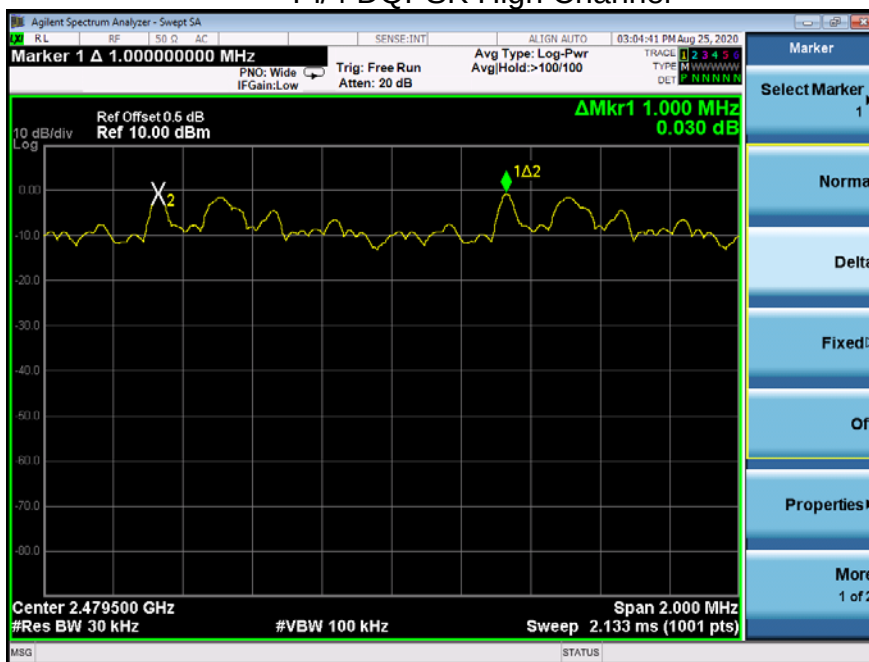
Pi/4 DQPSK Low Channel



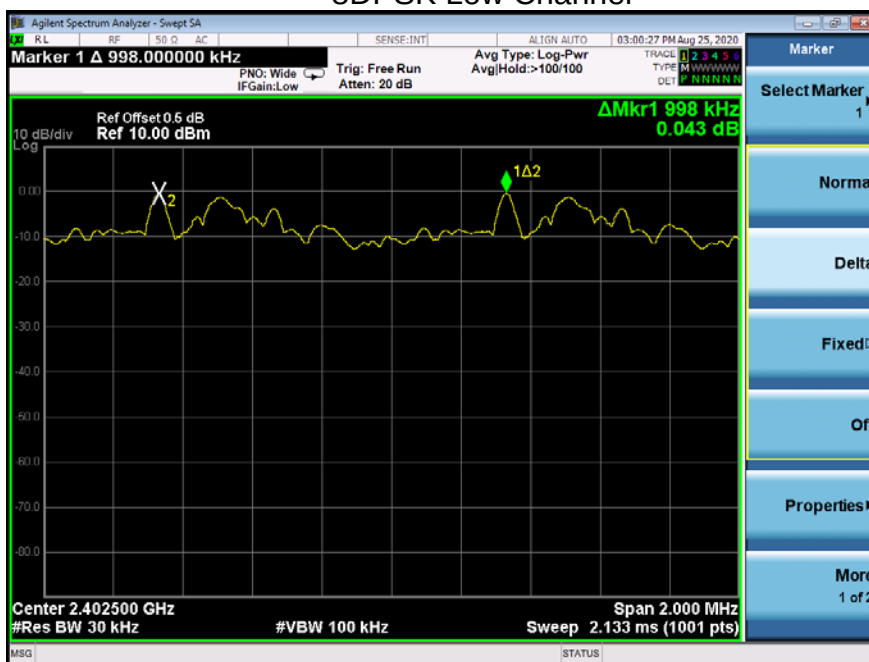
Pi/4 DQPSK Middle Channel



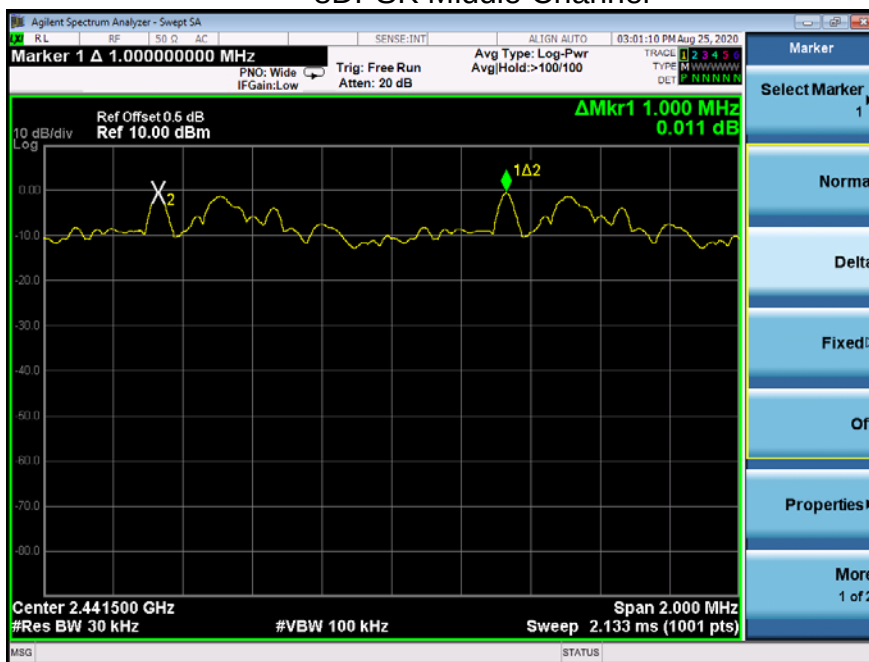
Pi/4 DQPSK High Channel



8DPSK Low Channel



8DPSK Middle Channel



8DPSK High Channel

