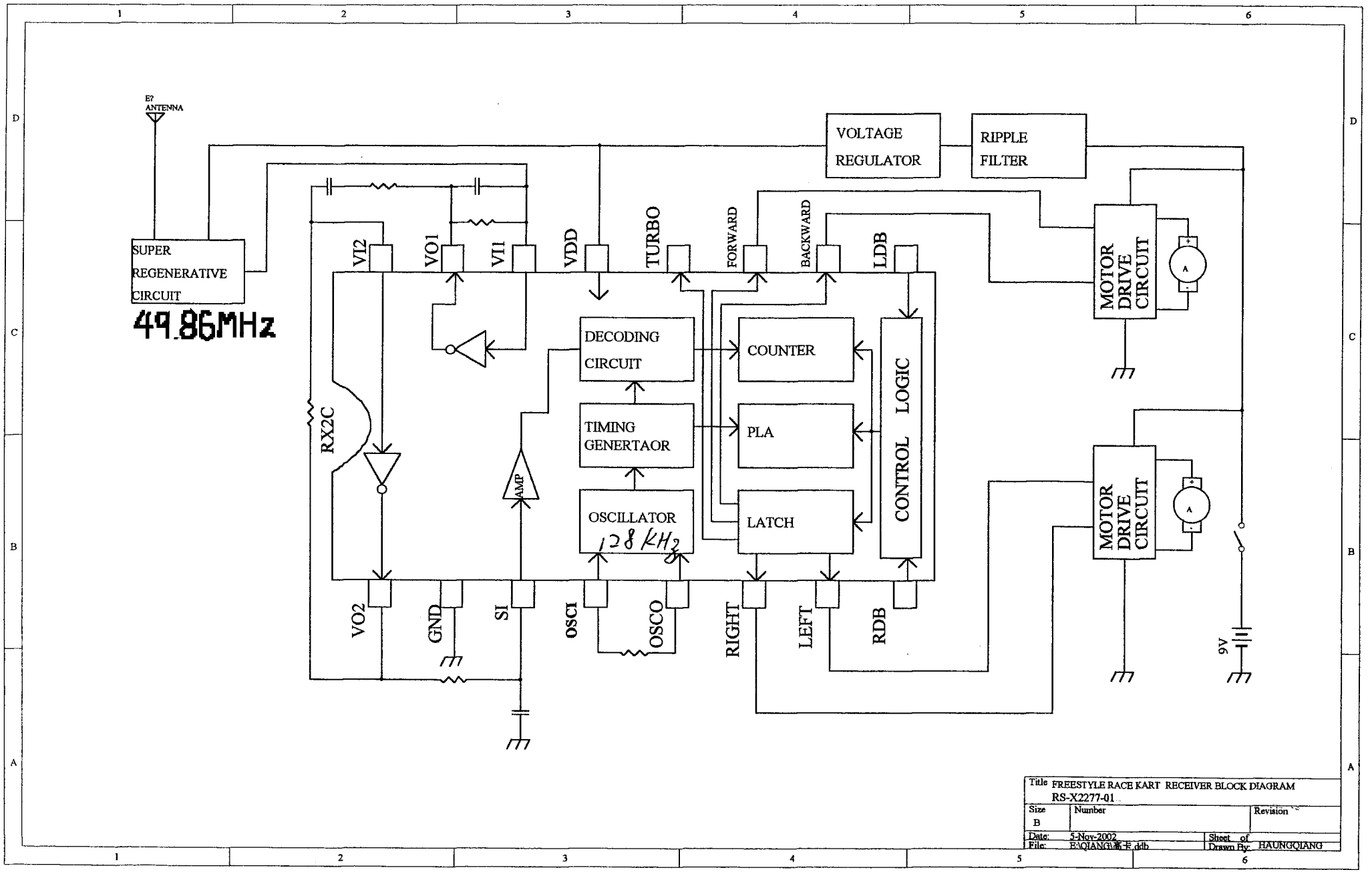
E?
ANTENNA
SUPER
REGENERATIVE
CIRCUIT
49.86MHz
VOLTAGE
REGULATOR
RIPPLE
FILTER
VI2
VO1
VI1
VDD
TURBO
FORWARD
BACKWARD
LDB
RX2C
AMP
DECODING
CIRCUIT
TIMING
GENERTAOR
OSCILLATOR
128 KHz
COUNTER
PLA
LATCH
CONTROL LOGIC
MOTOR
DRIVE
CIRCUIT
MOTOR
DRIVE
CIRCUIT
VO2
GND
SI
OSCI
OSCO
RIGHT
LEFT
RDB
9V
Title FREESTYLE RACE KART RECEIVER BLOCK DIAGRAM
RS-X2277-01
Size
B
Number
Revision
Date: 5-Nov-2002
File: E:\QIANGW~E.ddb
Sheet of
Drawn By: HAUNGQIANG