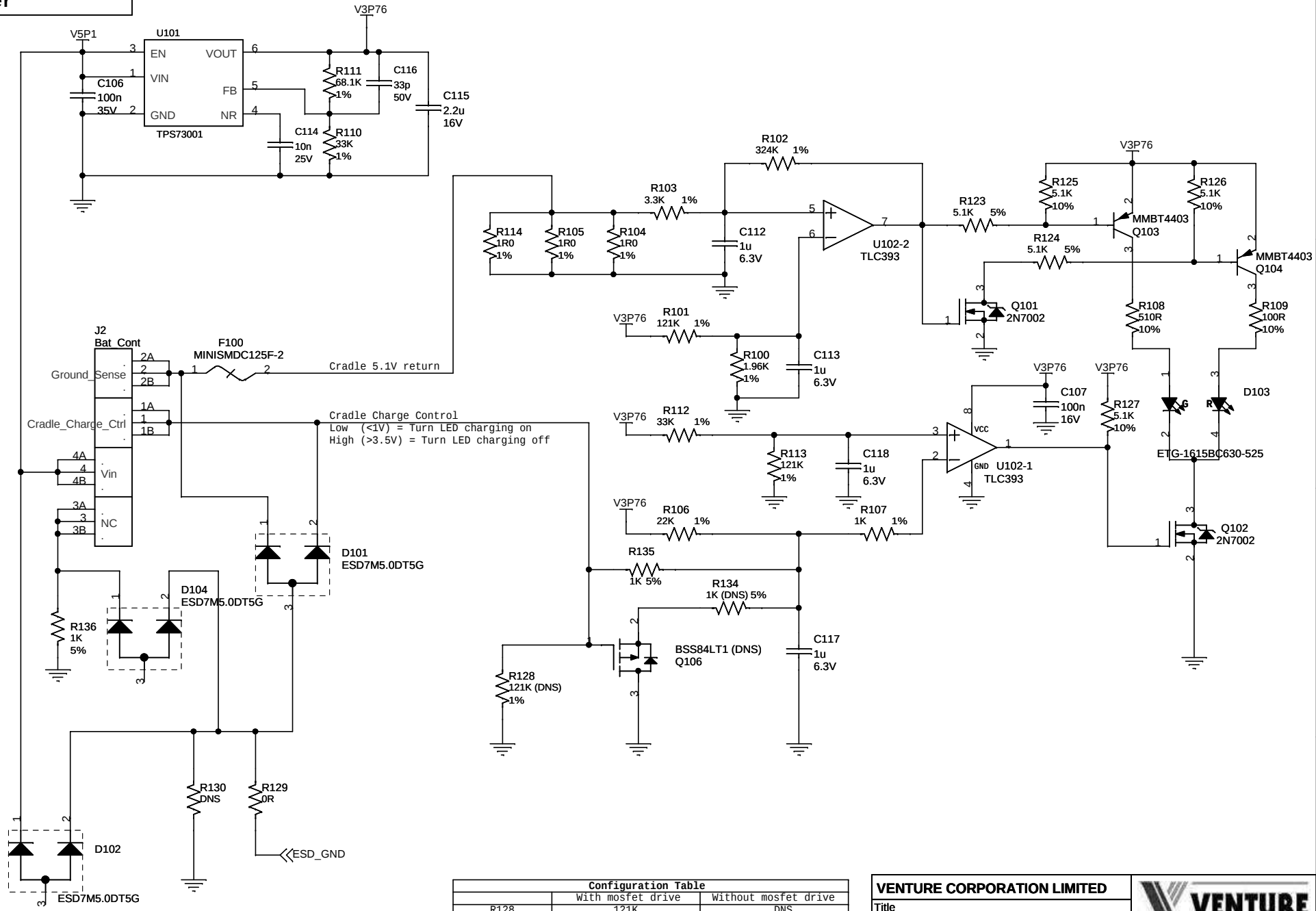


A1 Charger

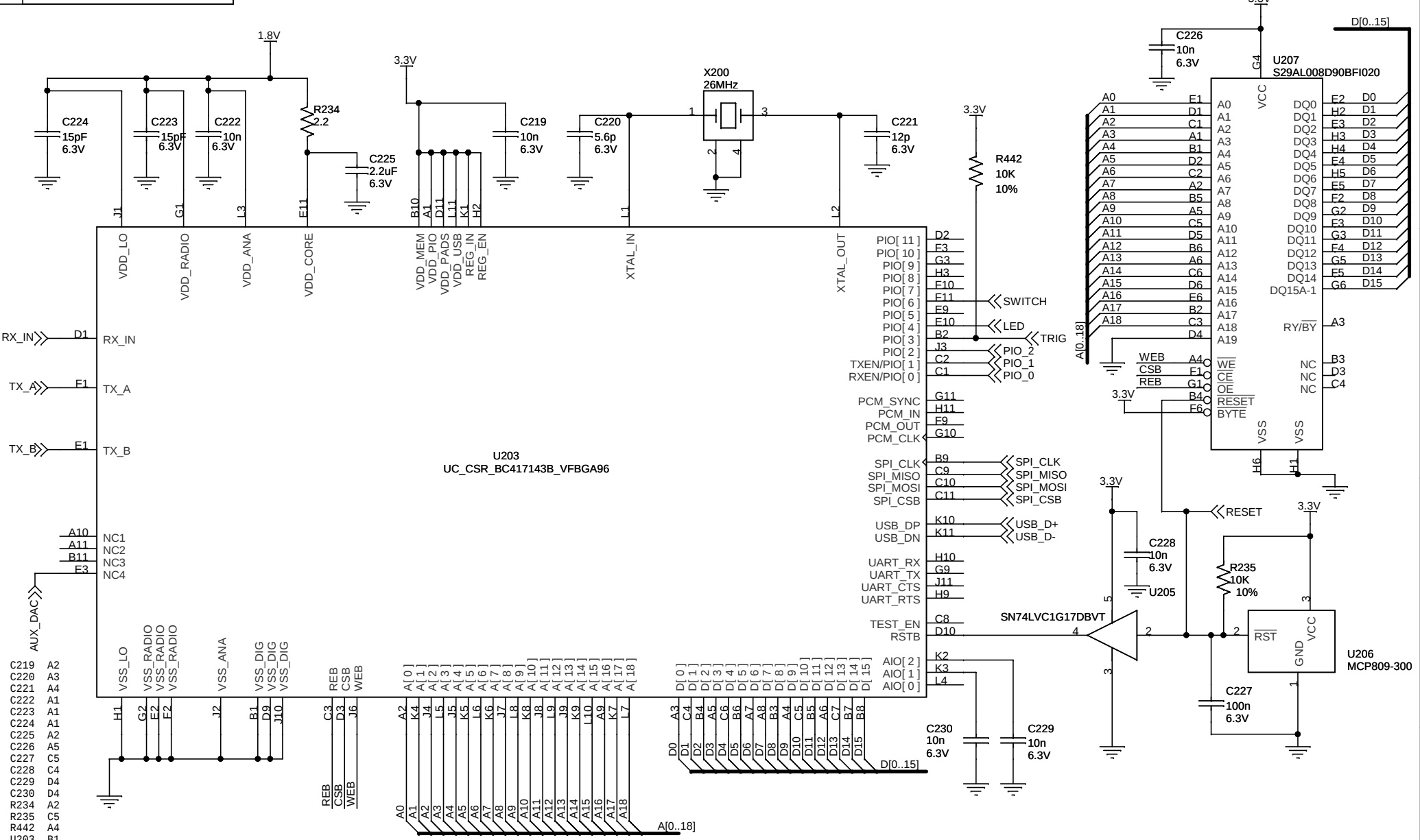
C106 A1
C107 B5
C112 B3
C113 B4
C114 A2
C115 A2
C116 A2
C117 C4
C118 B4
D101 C2
D102 D1
D103 B5
D104 C1
F100 B2
J2 B1
Q101 B4
Q102 C5
Q103 A5
Q104 B5
Q106 C3
R100 B3
R101 B3
R102 A4
R103 A3
R104 A3
R105 A3
R106 C3
R107 C4
R108 B5
R109 B5
R110 A2
R111 A2
R112 B3
R113 B4
R123 A4
R124 B5
R125 A5
R126 B5
R127 B5
R128 C3
R129 D2
R130 D2
R134 C3
R135 C3
R136 C1
U101 A1
U102 A4
U102 B4



Configuration Table		
	With mosfet drive	Without mosfet drive
R128	121K	DNS
R134	1K	DNS
R135	DNS	1K
Q106	BSS84LT1	DNS

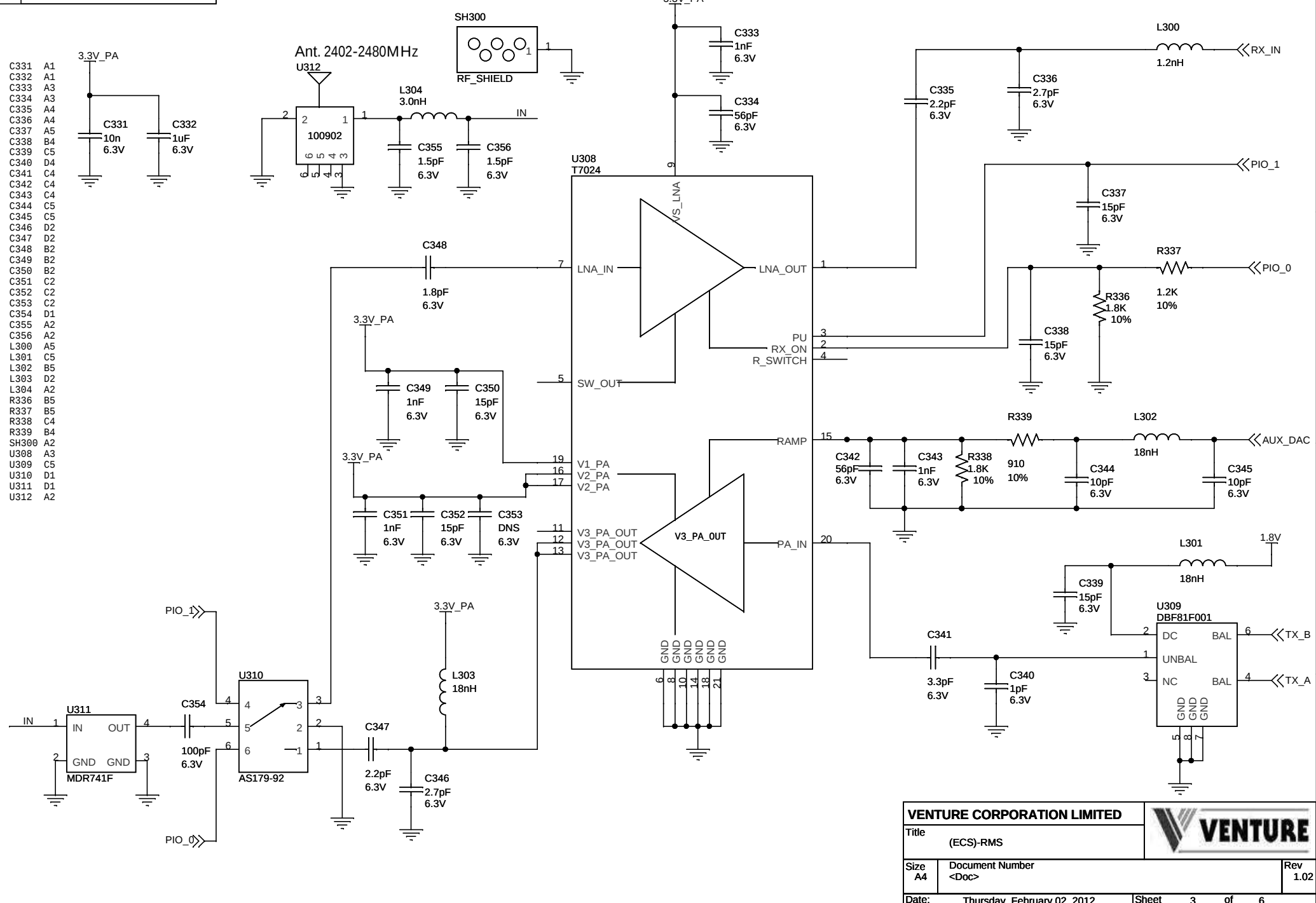
VENTURE CORPORATION LIMITED		
Title (ECS)-RMS		
Size A4	Document Number <Doc>	Rev 1.02
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A2 MCU and Memory



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A3	RF Amplifier
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Date: Thursday, February 02, 2012

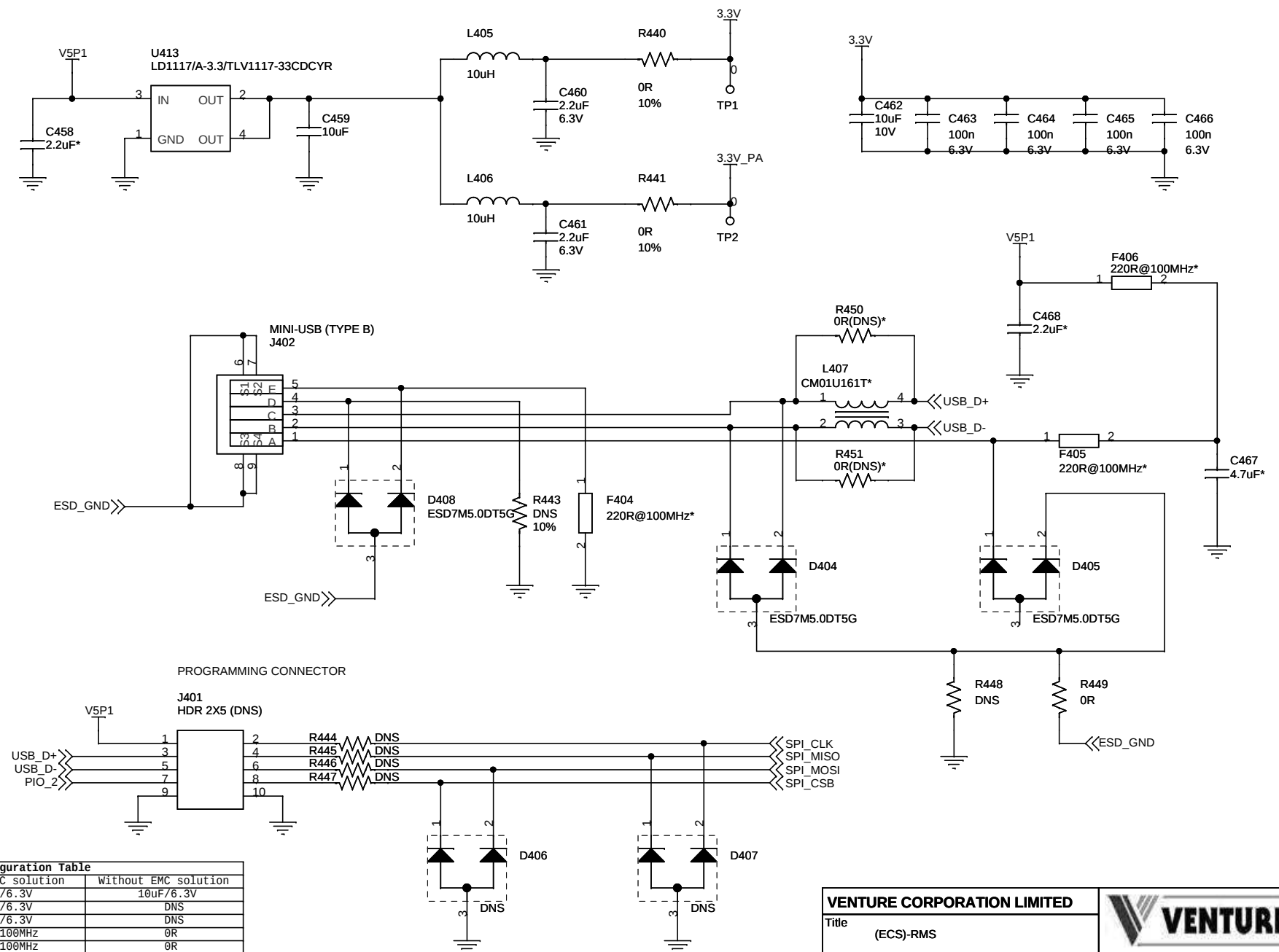

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1.02

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Rev
1.02

A4 Connectors

- C458 A1
- C459 A2
- C460 A3
- C461 B3
- C462 A4
- C463 A4
- C464 A5
- C465 A5
- C466 A5
- D404 C4
- D405 C4
- D406 D3
- D407 D3
- D408 C2
- F404 C3
- F405 B5
- F406 B5
- J401 C2
- J402 B2
- L405 A3
- L406 A3
- L407 B4
- R440 A3
- R441 A3
- R443 C3
- R444 C2
- R445 D2
- R446 D2
- R447 D2
- R448 C4
- R449 C5
- R450 B4
- R451 B4
- TP1 A4
- TP2 B4
- U413 A2

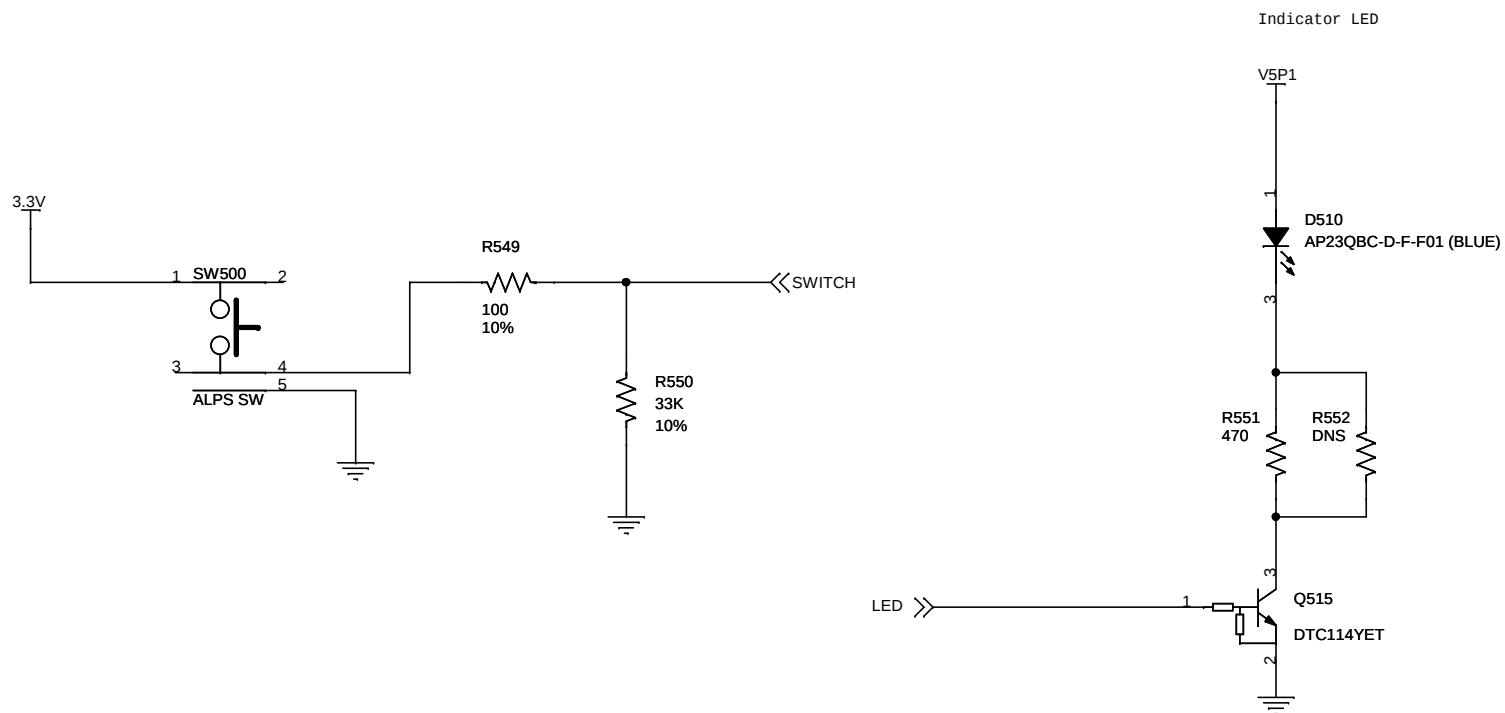


Configuration Table		
	With EMC solution	Without EMC solution
C458	2.2uF/6.3V	10uF/6.3V
C468	2.2uF/6.3V	DNS
C467	4.7uF/6.3V	DNS
F404	220R@100MHZ	0R
F405	220R@100MHZ	0R
F406	220R@100MHZ	0R
R450	DNS	0R
R451	DNS	0R
L407	CM01U161T	DNS

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A5	Miscellaneous
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D510	B4
Q515	C4
R549	B2
R550	B3
R551	B4
R552	B4
SW500	B2



REV NO	DATE	RELEASE BY	HISTORY	MATCHING BOM REVISION NUMBER / DATE	MATCHING PCB RELEASED REVISION NUMBER / NAME
1.00	13 APR 2011	K.S. KOH	Changes 1) Change RJ-50 connector to Mini-usb 2) Delete serial communications 3) Add in circuits for battery charging status 4) Change Cradle charge control mosfet to P-channel. 5) Delete LED for stand. 6) Provide provision for esd gnd return through shield of usb cable 7) Ground pin 3 of antenna as per specification of antenna	1.00	(ECS) RMS 20110613.asc
1.01	27 SEP 2011	K.S. KOH	Changes 1) Change pinning of J2 (Bat_Cont) , J402 ((MINI-USB) TYPE B). 2) Change footprints of L405 and L406 from 0805 package to 1210 package (LQH32CN100K23L)-Murata. 3) Change D103 from AAA3528SURKCGKS to ETG-1615BC630-525. 4) Change SW500 from E-Switch TL3303 to ALPS SKHUPME010. 5) Delete F401,F402,F403 Add in F404,F405,F406 (BLM21PG221SN1) 6) Add in C467,C468,L407 7) Swap pin1 and 2 of D404 for layout optimisations. A) Change U205 from inverter (SN74LVC1G04) to buffer (SN74LVC1G17DBVT) (BOM change only). B) Update R128,Q106 and R134 as DNS, and add in R135 (1K). (BOM change only). C) Change C458 from 10uF to 2.2uF. (BOM change only). D) Change R551 from 300 ohms to 470 ohms and delete R552. (BOM change only). E) Change R108 from 56R to 510R. (BOM change only). F) Change R109 from 56R to 100R. (BOM change only).	1.01	(ECS) RMS 20111017.asc Proto 1
1.02	10 JAN 2012	K.S. KOH	Changes (layout changes only, no impact to schematics) 1) Error correction for 3.3V via shorts 2) DFM on wave-soldering for diodes and resistors near J2. 3) Increase the soldering pads for SH300. 4) 2 locating slots for PCB.	1.01	(ECS) RMS 020212.pcb ----> named "CCC006644_R2" on PCB. (ECS) RMS 020212.asc

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Title (ECS)-RMS			
Size A4	Document Number <Doc>	Drawn by := KS Koh(Snr EE Engineer) Checked by := KH Liew (R&D Prj Manager)	Rev 1.02
Date: Thursday, February 02, 2012		Sheet	6 of 6