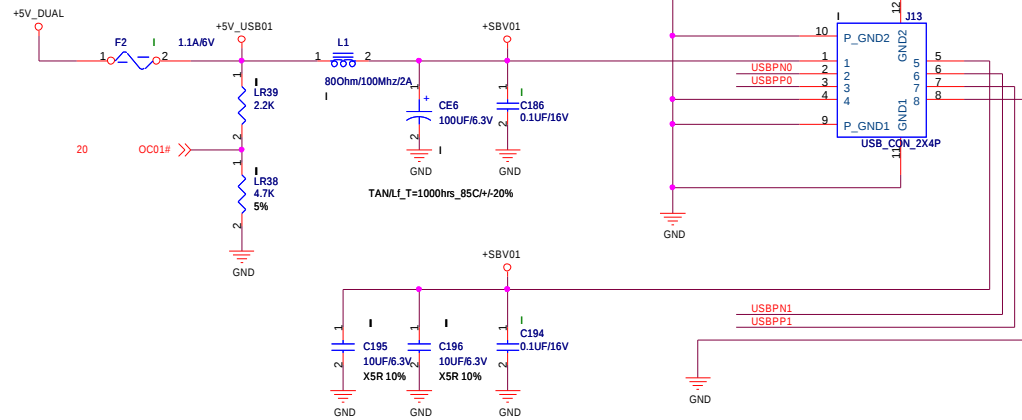
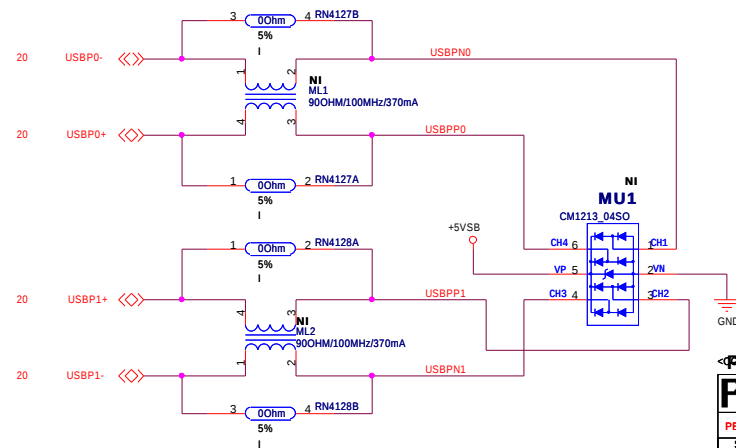
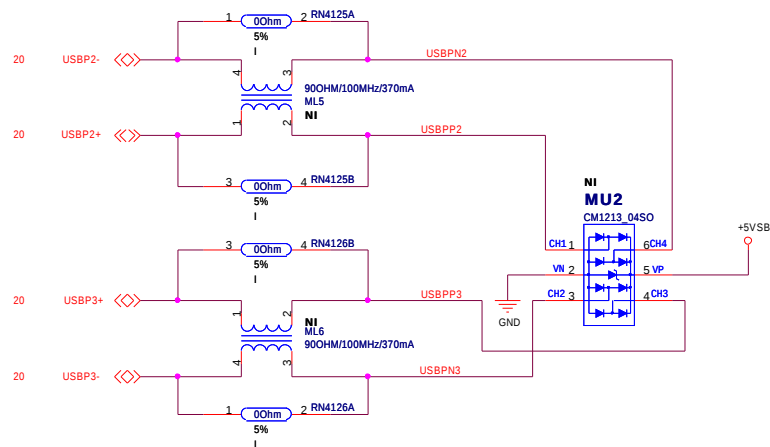
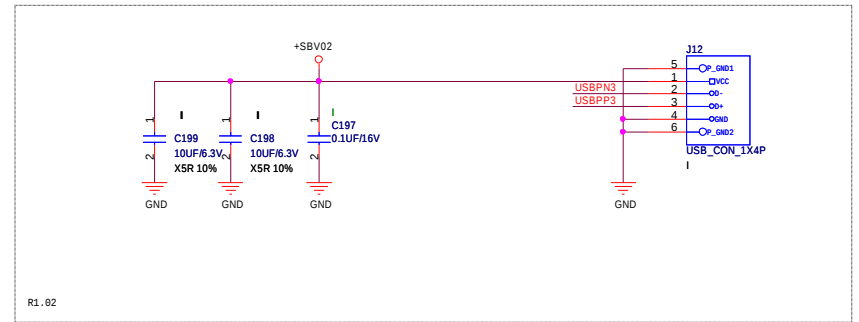
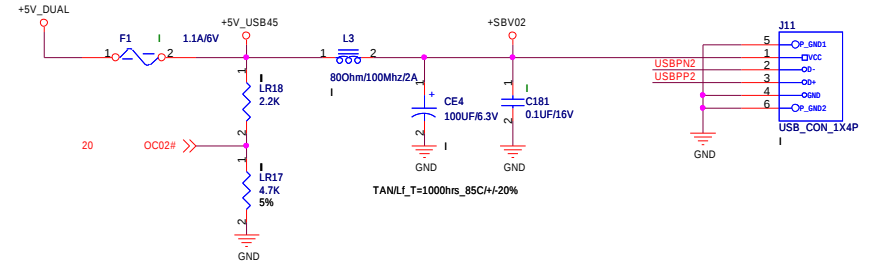


USB 2.0



USB 2.0



11.50 VGA_RED >>

11.50 VGA_GREEN >>

11.50 VGA_BLUE >>

NOTE:
Place there VGA filter components
within 500 mils of the VGA connector

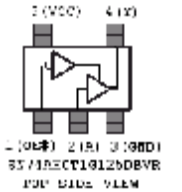
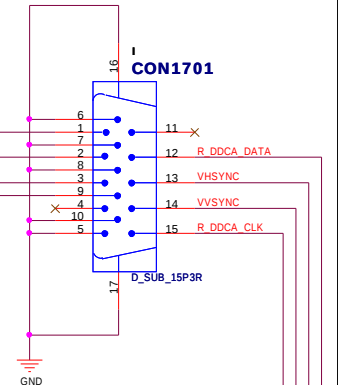
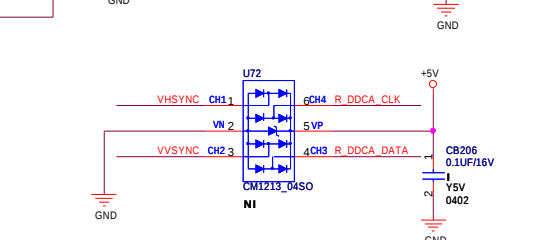
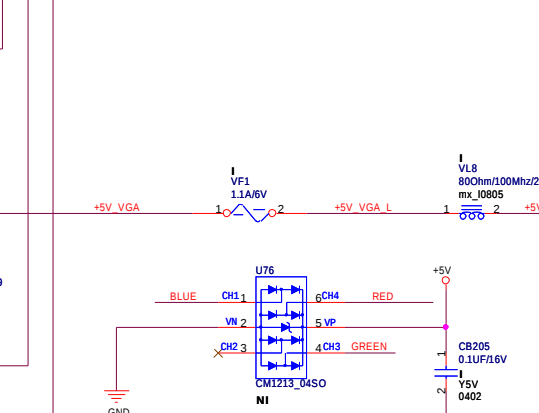
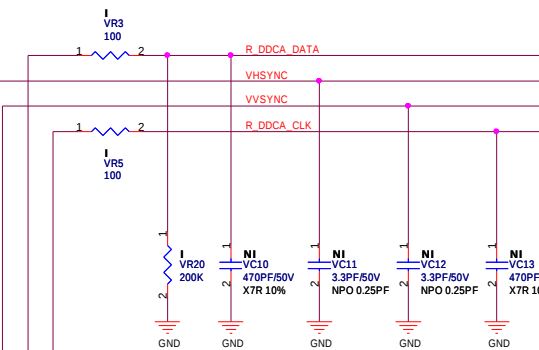
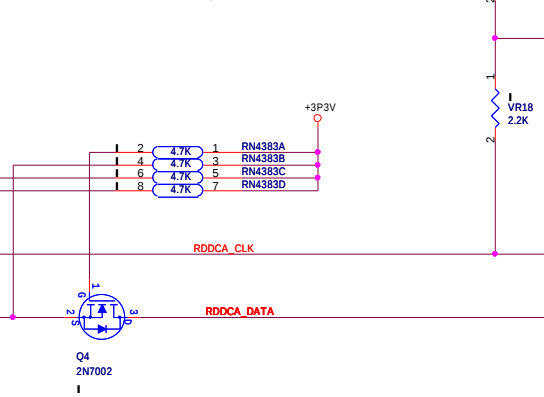
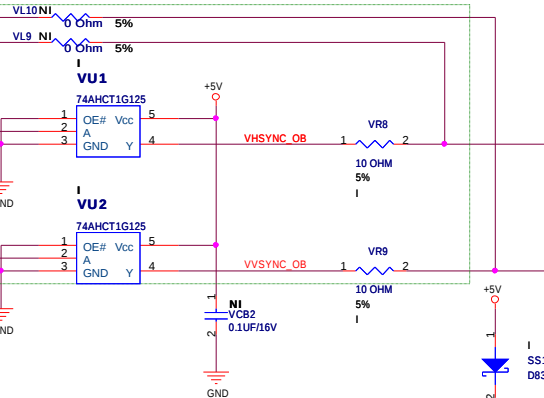
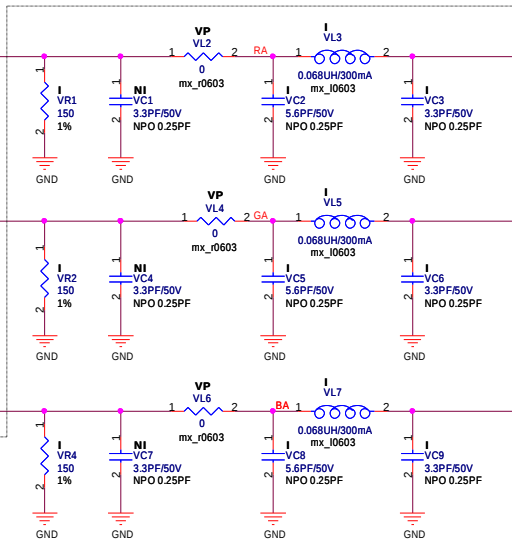
VR6,VR7 NI and VU1,VU2,VR8,VR9 I for EA issue
VR8, VR9 = 10 ohm (CRB)

11.50 VGA_HSYNC >>

11.50 VGA_VSYNC >>

11.52 DDCA_CLK <<

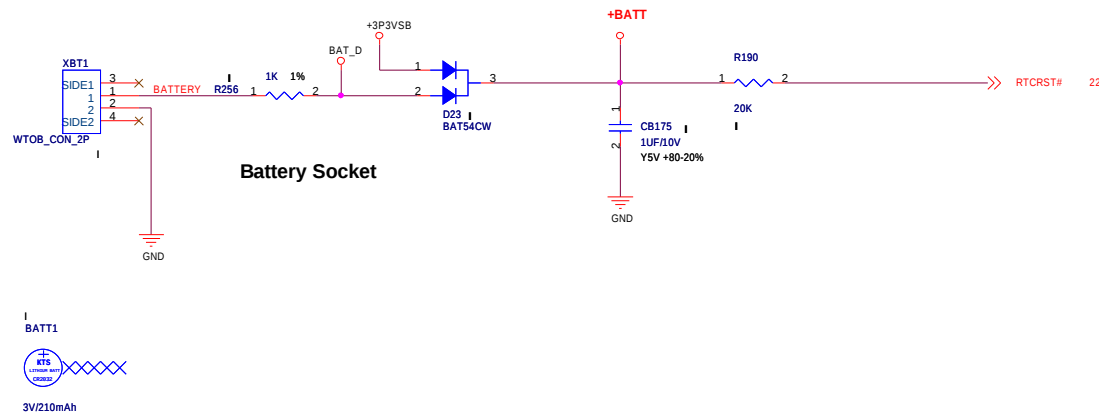
11.52 DDCA_DATA <<



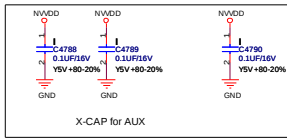
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : VGA PORT	
PEGATRON CORPORATION Engineer: Maple Feng	
Size A3	Project Name IPPPV-DCN
Date: Tuesday, June 29, 2010	Sheet 18 of 54

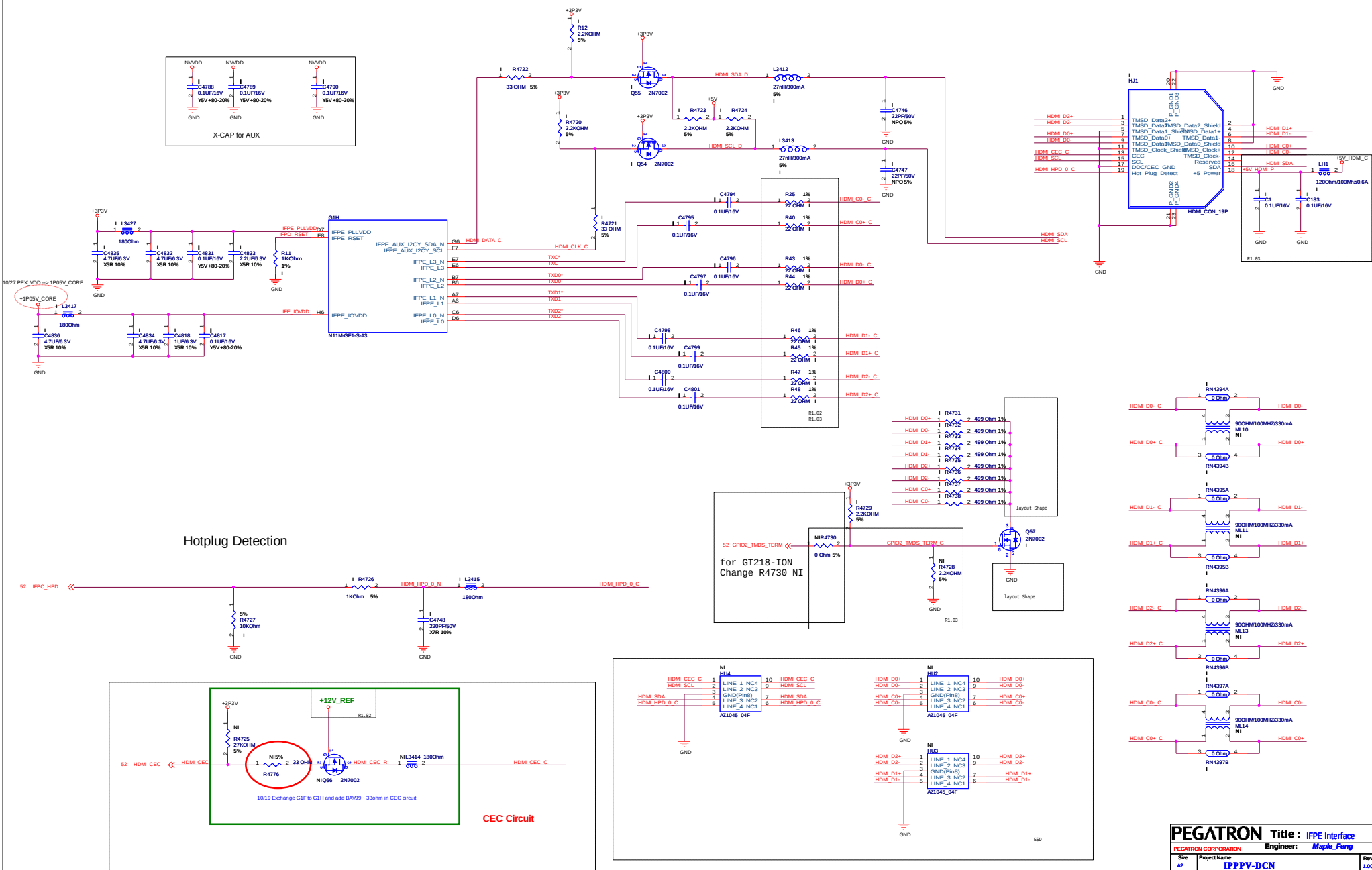
External RTC Circuitry



IFPC, IFPE Interface, Mechanical

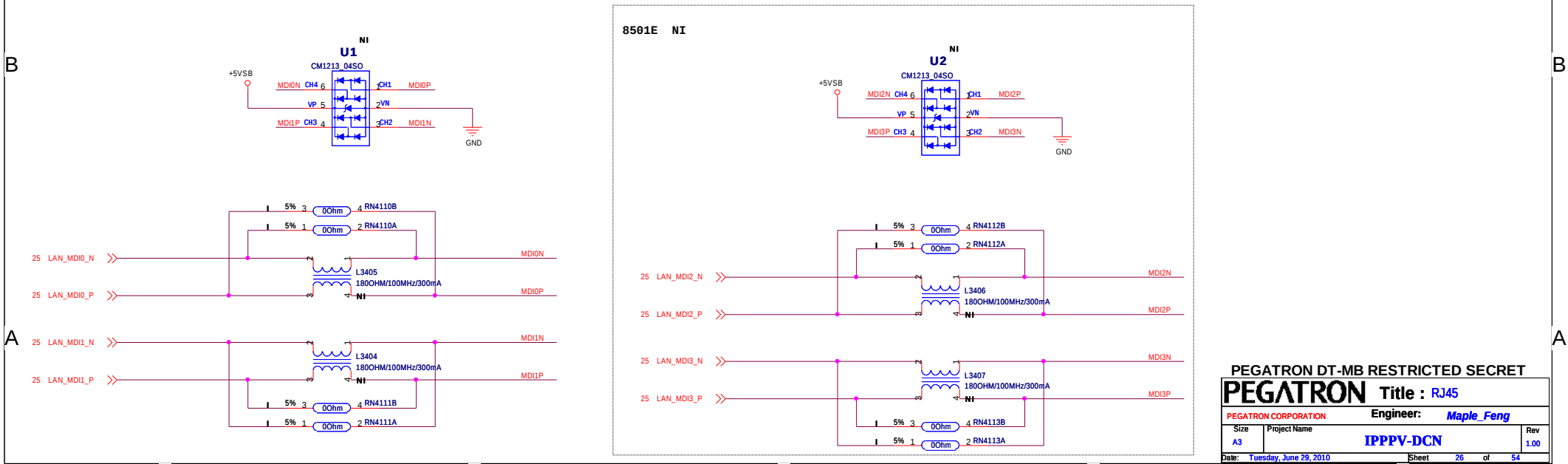
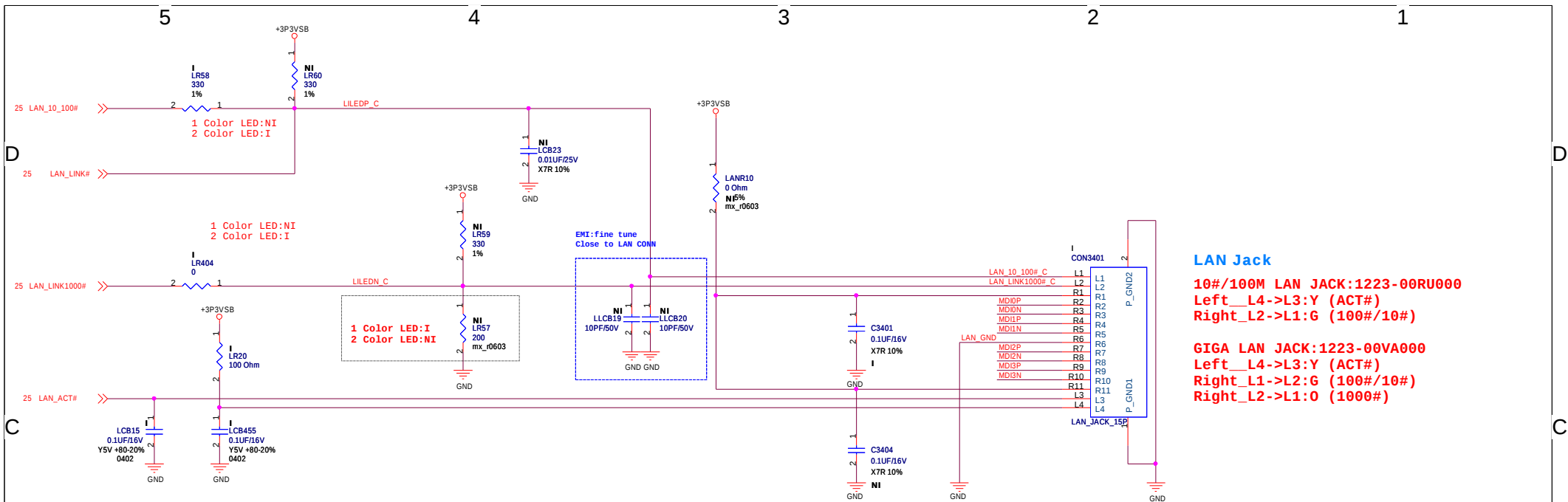


X-CAP for AUX



Hotplug Detection

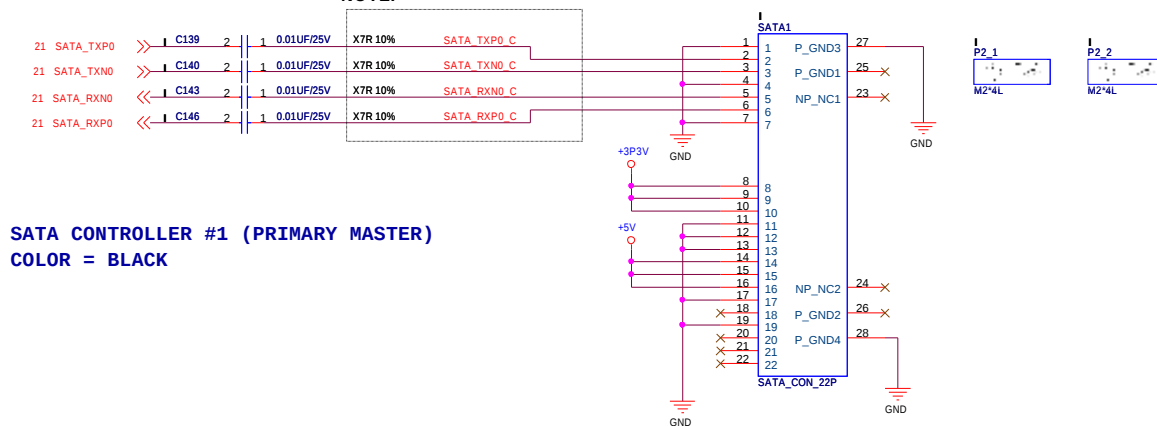
CEC Circuit



LAYOUT NOTE:
 Two Strokes : Matched within 100 mils of each other
 D[0:15] : Matched within +/- 450 mils of two strokes

SATA HDD

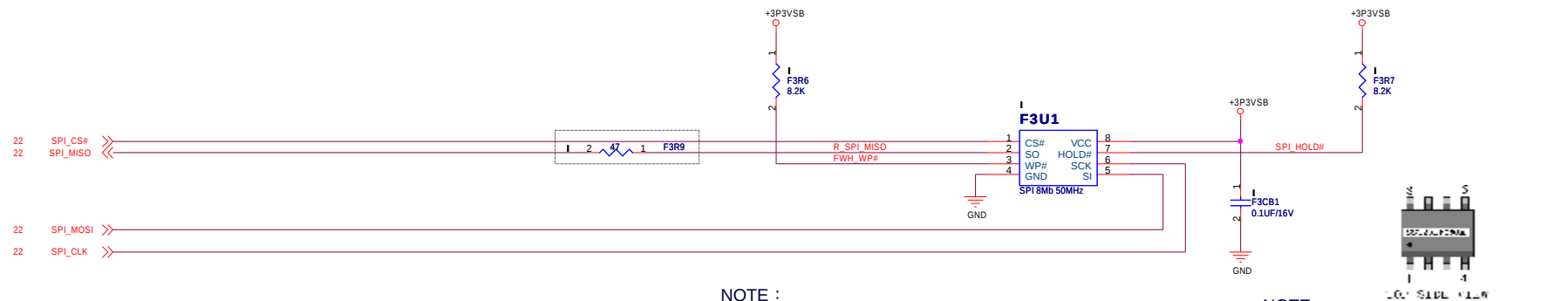
NOTE: Place near SATA connector



PEGATRON DT-MB RESTRICTED SECRET

PEGATRON		Title : SATA CONN.	
PEGATRON CORPORATION		Engineer: Maple Feng	
Size A3	Project Name IPPPV-DCN	Rev 1.00	
Date: Tuesday, June 29, 2010		Sheet 32 of 54	

SPI BIOS ROM - 8Mbit



10/6 Ted : remove SIO F75160

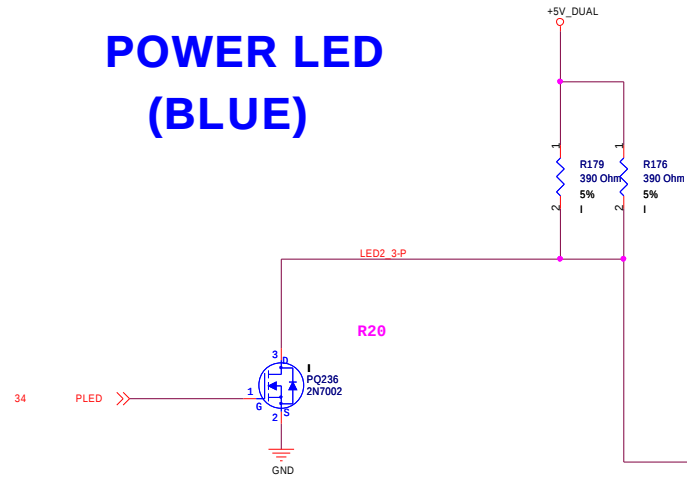
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : SPI FLASH
PEGATRON CORPORATION Engineer: Maple Feng

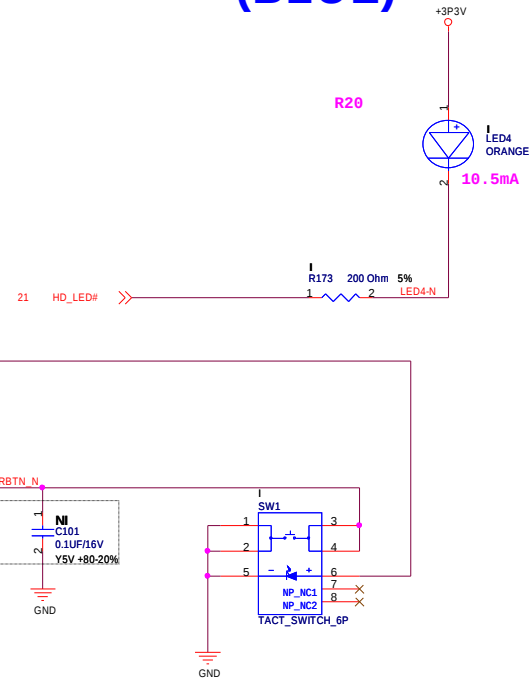
Size A3 Project Name IPPPV-DCN Rev 1.00

Date: Tuesday, June 29, 2010 Sheet 35 of 54

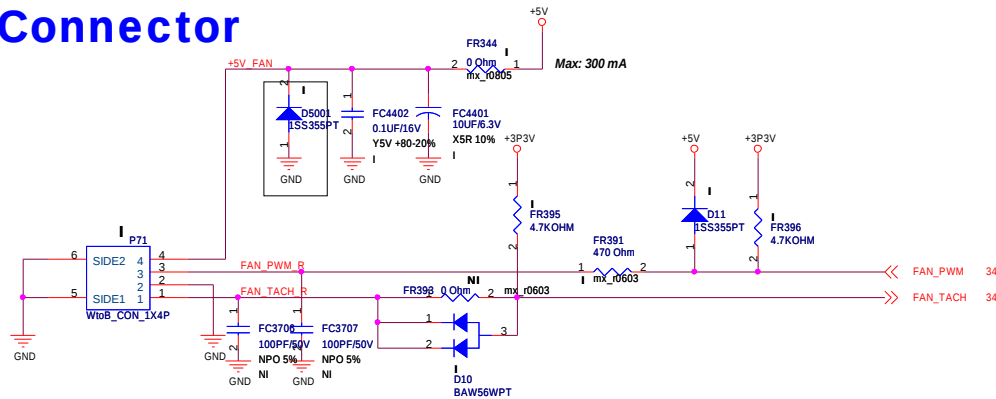
POWER LED (BLUE)



HDD LED (BLUE)



FAN Connector



FAN Conn.:12G17000004B
WtoB CON 4P,1.25mm,S/T,SMT

FAN CABEL:

Pin4:RED +
Pin3:BLUE PWM
Pin2:BLACK -
Pin1:YELLOW TAC

PEGATRON DT-MB RESTRICTED SECRET			
PEGATRON		Title : CONTROL PANEL/SPKR	
PEGATRON CORPORATION		Engineer: Maple Feng	
Size A3	Project Name IPPPV-DCN	Rev 1.00	
Date: Tuesday, June 29, 2010		Sheet 36 of 54	