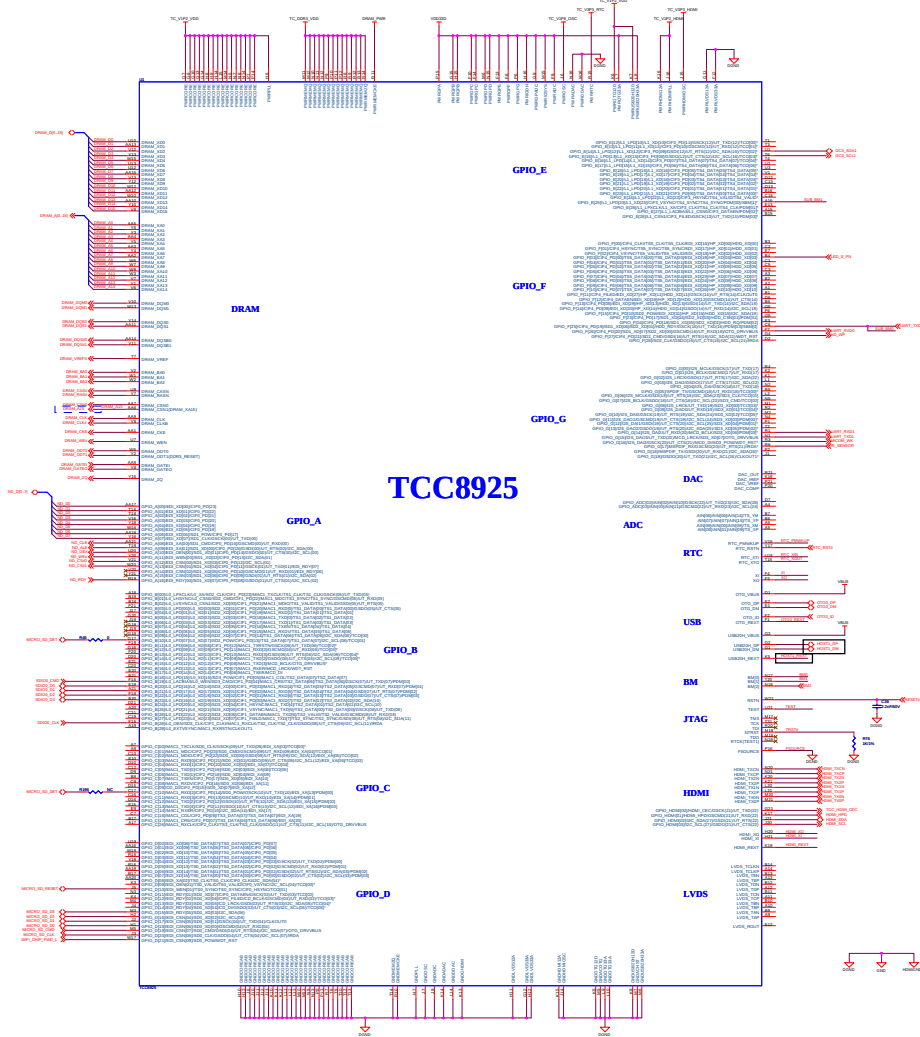
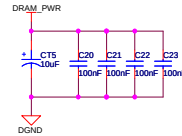


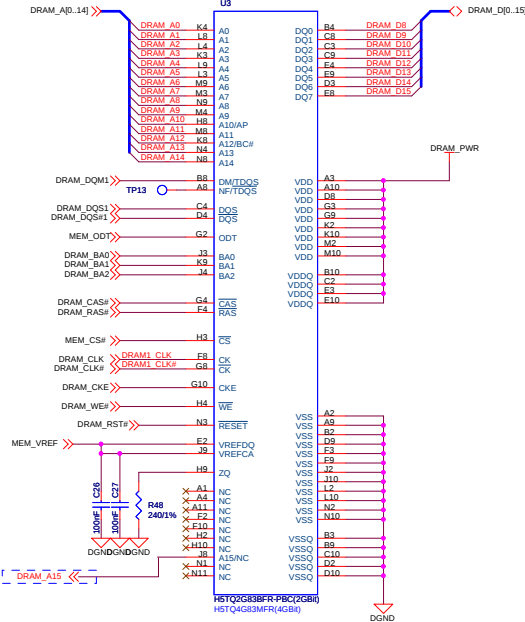
Appendix 5.

Schematics



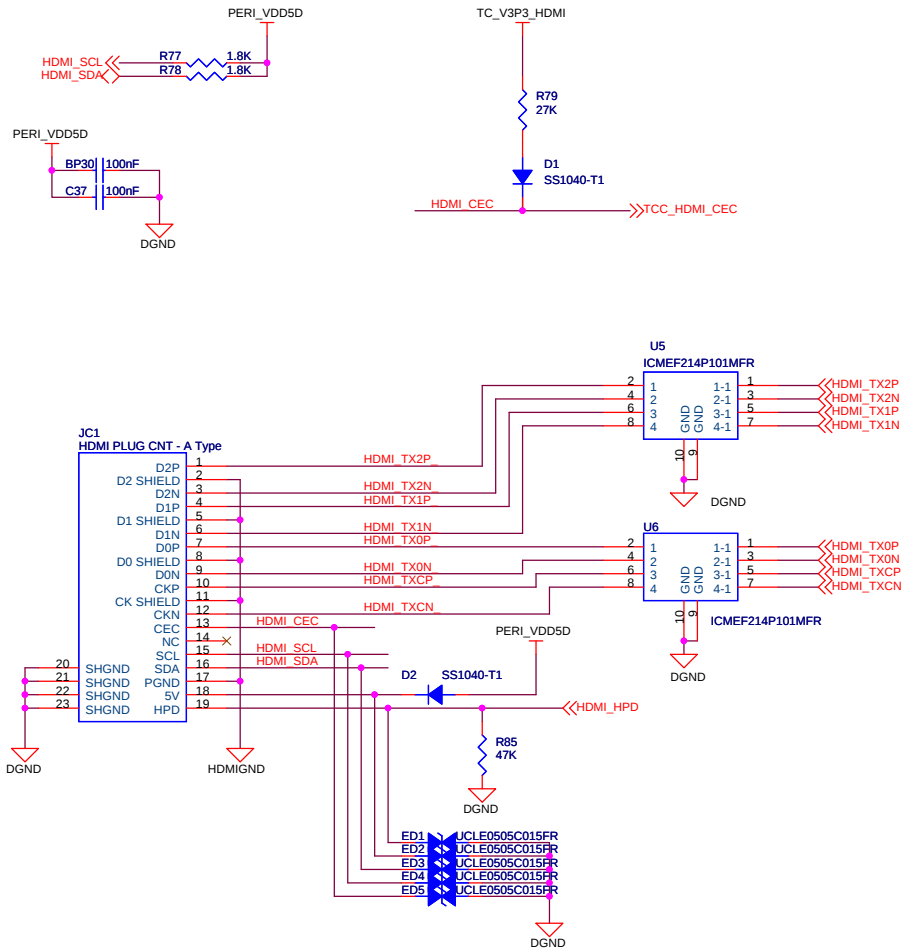


*** Place these parts to CPU as close as possible**



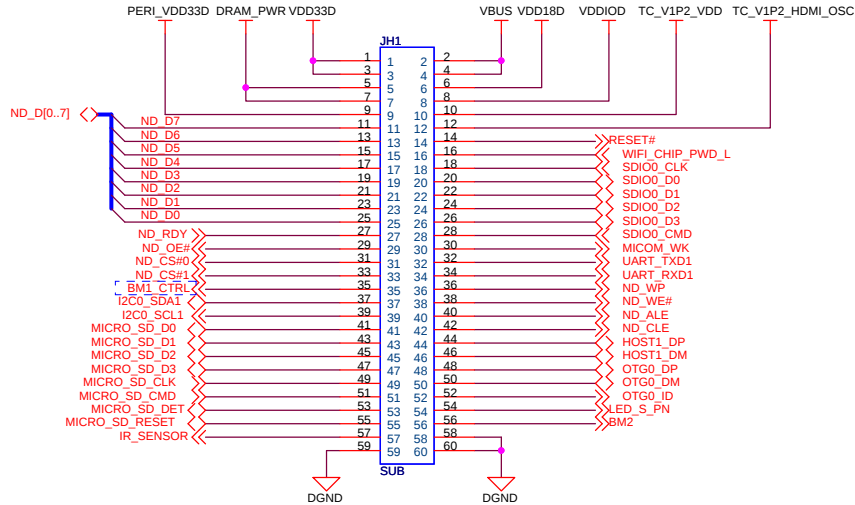
File				
TCC8925_HDMI_DONGLE				
Size	Document Number			Rev
A3	DDR3 512MB DRAM			V0.1
Date:	Thursday, June 21, 2012		Sheet	3 of 6

HDMI

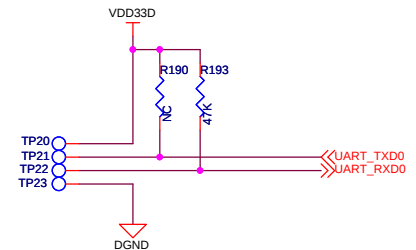


Title		
TCC8925_HDMI_DONGLE		
Size	Document Number	Rev
A4	HDMI & USB	V0.1
Date:	Thursday, June 21, 2012	Sheet 4 of 6

Main Board Header



UART



Title			
TCC8925 HDMI DONGLE			
Size	Document Number		Rev
A4	SUB BOARD HEADER		V0.1
Date:	Thursday, June 21, 2012	Sheet	5 of 6

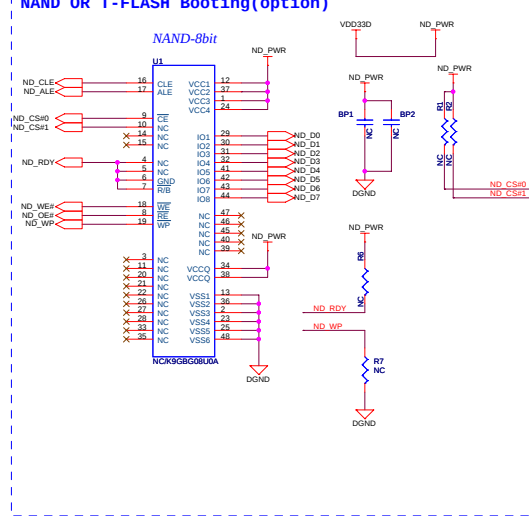
Configuration Description

Sub Boot Mode Selection Pin : GPIOE[25], GPIOF[25]
TCC892x Boot Mode : CPU(288MHz), MBUS(144MHz), IOBUS(144MHz), PLL0

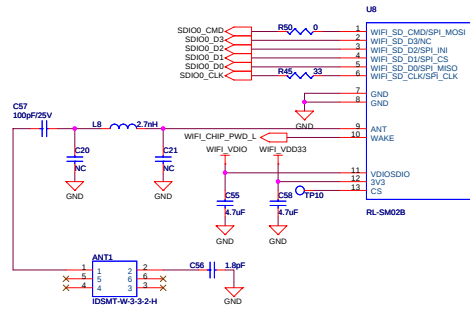
BM[3:0]	MODE	SUBMODE1		SUBMODE0		CLK(TCC892X)	External Sync Clk	GPIO	비고
		GPIOE[25]	Description	GPIOF[25]	Description				
0000b	EH180							1. EH18/ENH16 892x : GPIO_F[22:00]	EH1 8BIT/16BIT (CH0)
0001b	I2C	0	CH0 Port0		0 fail -> UART0	3MHz = XIN/4	300KHz	1. CH0 Port 23 - 892x : GPIOG[7:6] = {SCL,SDA}	
		1	CH0 Port1		0 fail -> UART1			2. CH0 Port 24 - 892x : GPIOG[11:10] = {SCL,SDA}	
0010b	SFLASH	0	fail->UART		0 fail -> UART0	288 / 3 = 96MHz	8MHz =96 / (5+1)*2 DLDV = 5	1. GPSB0, PORT1 - 892x : GPIOE[15:12]	
		1	fail->USB	-	1 fail -> UART1				
0011b	USB								UTMI 0 Port만 사용
0100b	SPI	0	CMD : Low		0 PCK : rising	12MHz (X)	6MHz	1. GPSB0, PORT1 - 892x : GPIOE[15:12]	
		1	CMD : HIGH		0 PCK : falling 1 PCK : rising 1 PCK : falling				
0101b	SDMMC	0	SD0		0 SDMMC	48MHz (PLL0_DIV)	24MHz or 170KHz	1. SD CH0(MAX 8BIT) - GPD[20:11] (892x, SD0)	SD의 0 Ch과 1 Ch만 Boot에 사용
		1	SD1		1 eMMC 0 SDMMC 1 eMMC			2. SD CH1(MAX 8BIT) - GPF[26:17] (892x, SD1)	
0110b	NOR	0			0 8bits NOR			1. 8BIT (892x) - WEN[1], OEN[1] : GPD[10], GPD[09], RDY[1] : GPD[11] - CSN[9] : GPD[21] - XA[23:00] : GPC[20:00],GPD[8],GPA[9:8] - XD : GPA[7:0]	
					1 16bits NOR			1. 16BIT (892x) - WEN[1], OEN[1] : GPD[10], GPD[9], RDY[1] : GPD[11] - CSN[9] : GPD[21] - XA[23:00] : GPC[20:00],GPD[8],GPA[9:8] - XD : GPD[7:0],GPA[7:0]	
	UART	1			0 UART0 port	PCK : 24MHz BAUD RATE 115.2KHz		1. CH0 Port9 (892x) -GPF[26:25]	
					1 UART1 port			1. CH0 Port10 (892x) -GPF[29:28]	
0111b	NAND	0			ND8/16/32bit scan mode (recommand)			1. 8BIT - WEN, OEN : GPA11, GPA10 - CSN : GPA[12], ALE, CLE : GPA[09:08] - XD : GPA[7:0]	
					1 ND 8bit only mode			2. 16BIT - WEN, OEN : GPA11, GPA10 - CSN : GPA[12], ALE, CLE : GPA[09:08] - XD : GPD[7:0],GPA[7:0]	
		1			0 ND16bit scan mode			3. 32BIT - WEN, OEN : GPA11, GPA10 - CSN : GPA[12], ALE, CLE : GPA[09:08] - XD : GPD[15:00],GPD[07:00],GPA[07:00]	
					1 ND 32bit scan mode			4. RDY : GPA[16]	

Title			
BM DESCRIPTION			
Size	Document Number	Rev	
A3	<Doc>	<Rev>	Code>
Date	Thursday, June 21, 2012	Sheet	6 of 6

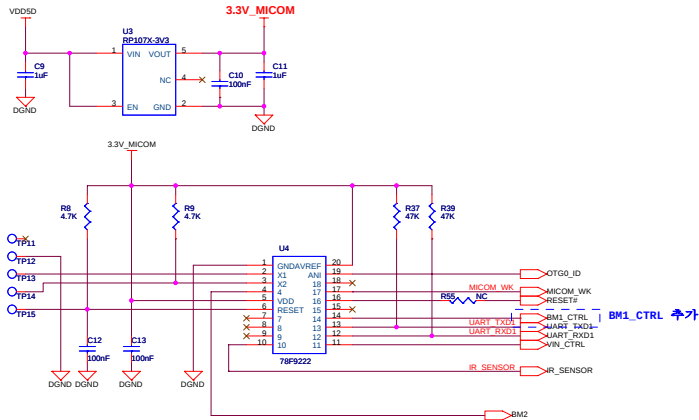
NAND OR T-FLASH Booting(option)



WiFi Module

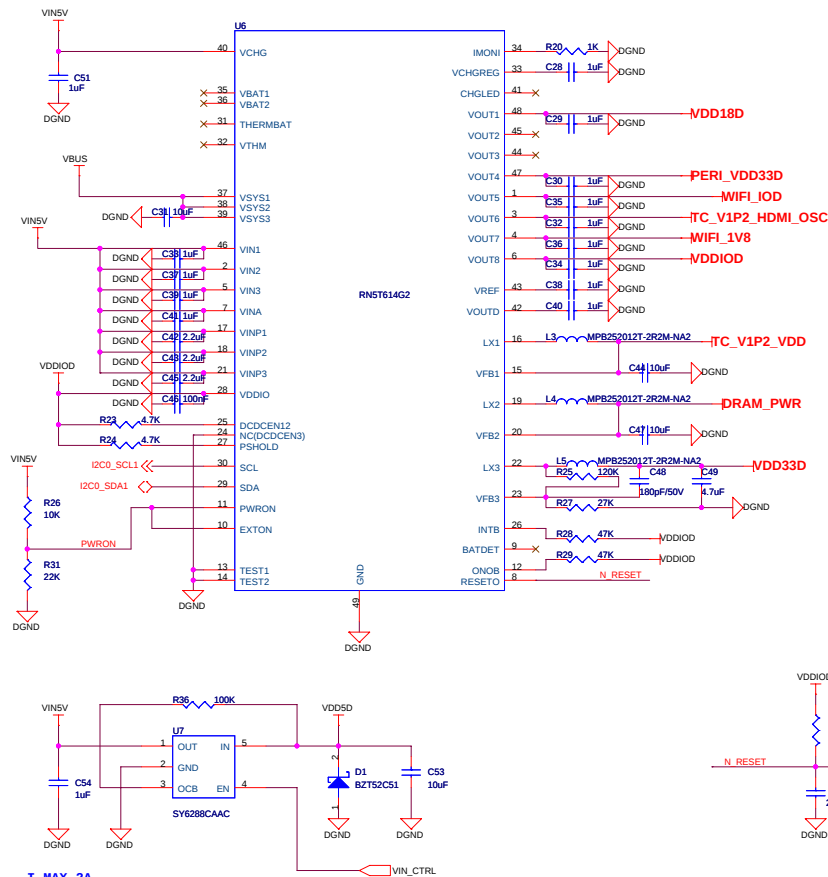


MICOM - (Option)

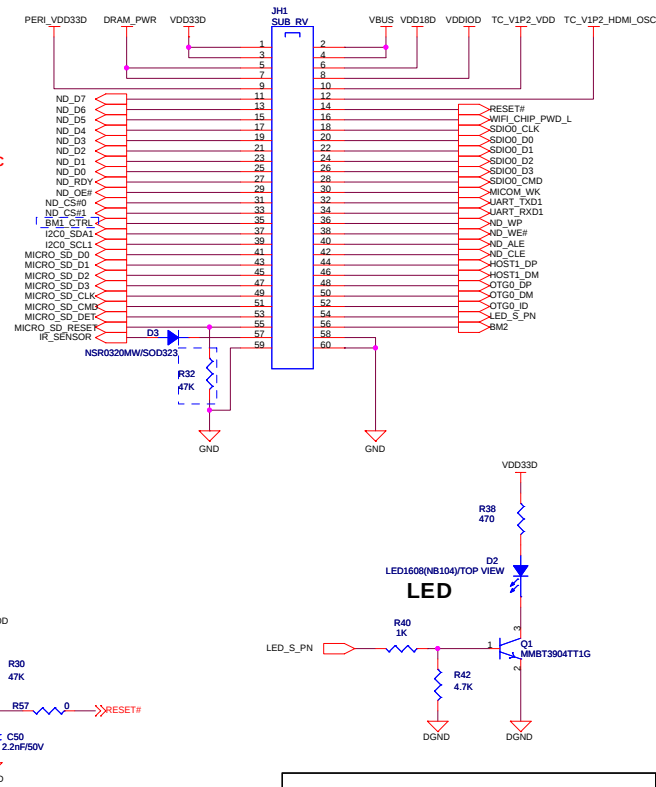




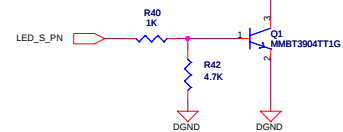
Date: Monday, July 09, 2012 Sheet 2 of 3



SUB Board Socket



LED



File	<Title> TCC8925_HDMI_DONGLE_SUB		
Size	Document Number	Rev	<RevCode>
B	PMIC		
Date:	Wednesday, July 04, 2012	Sheet	3 of 3

