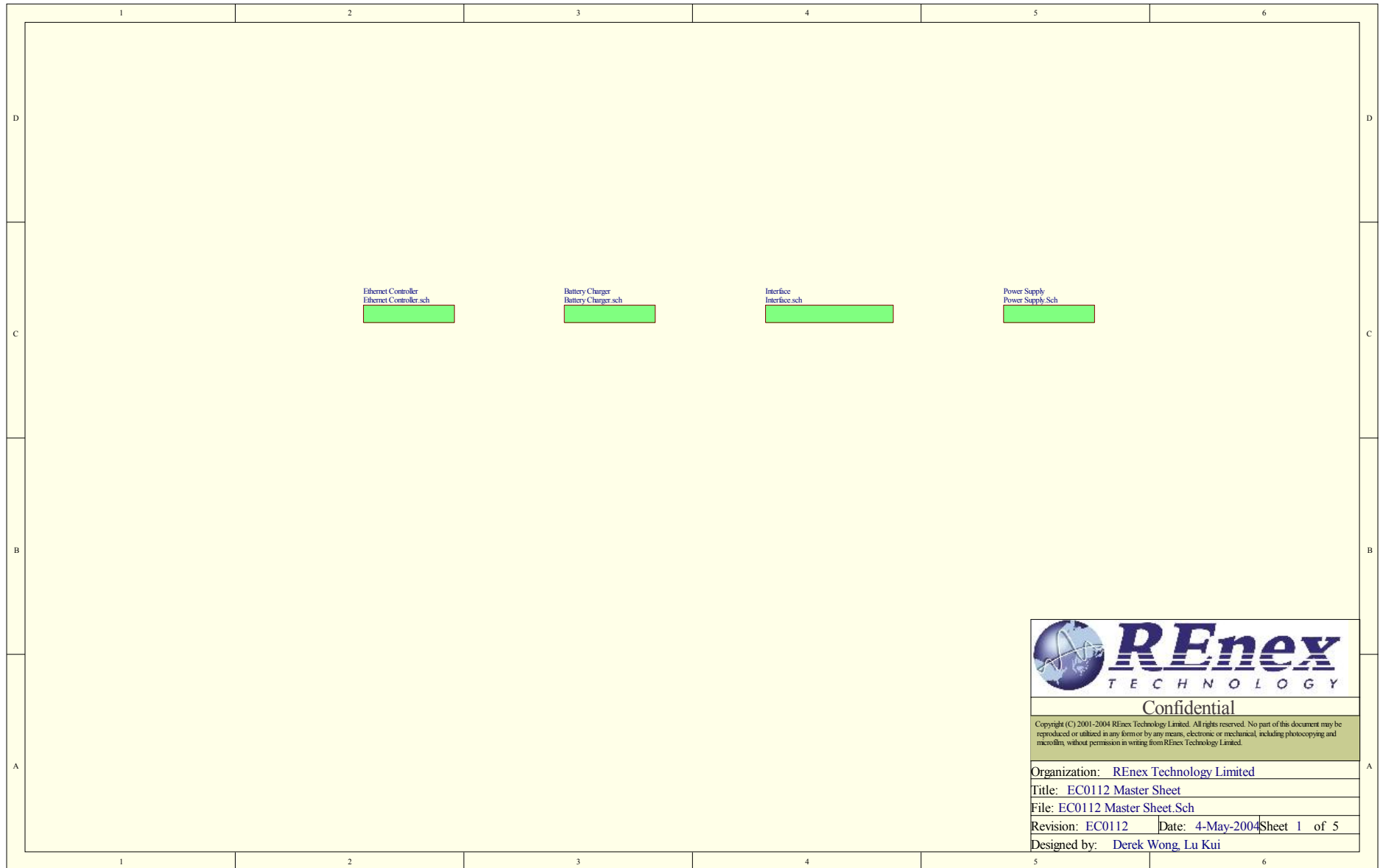
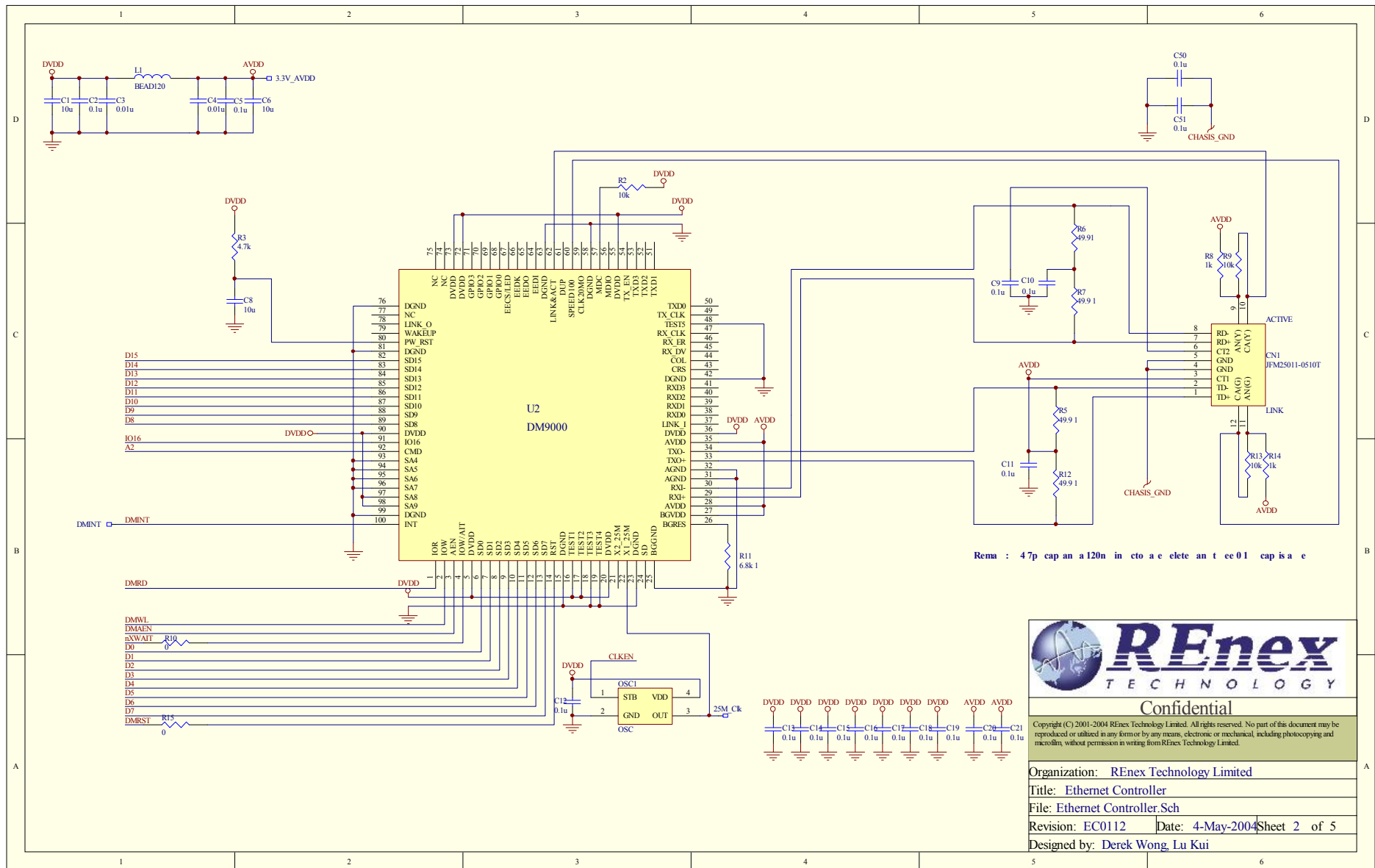
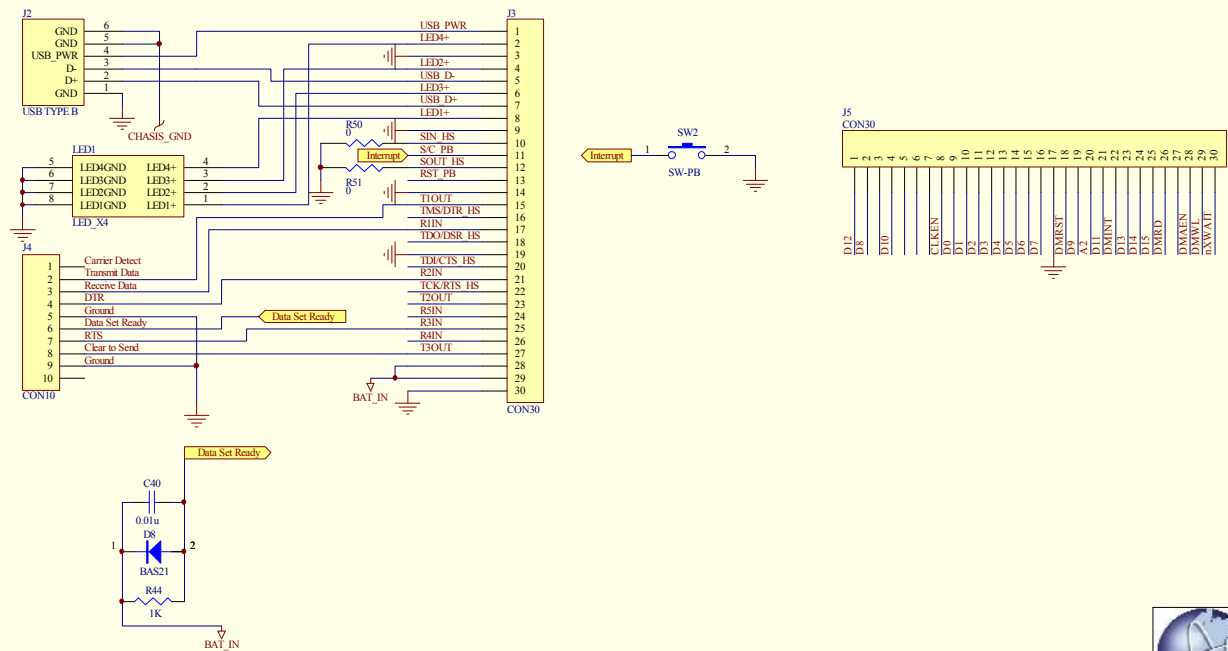




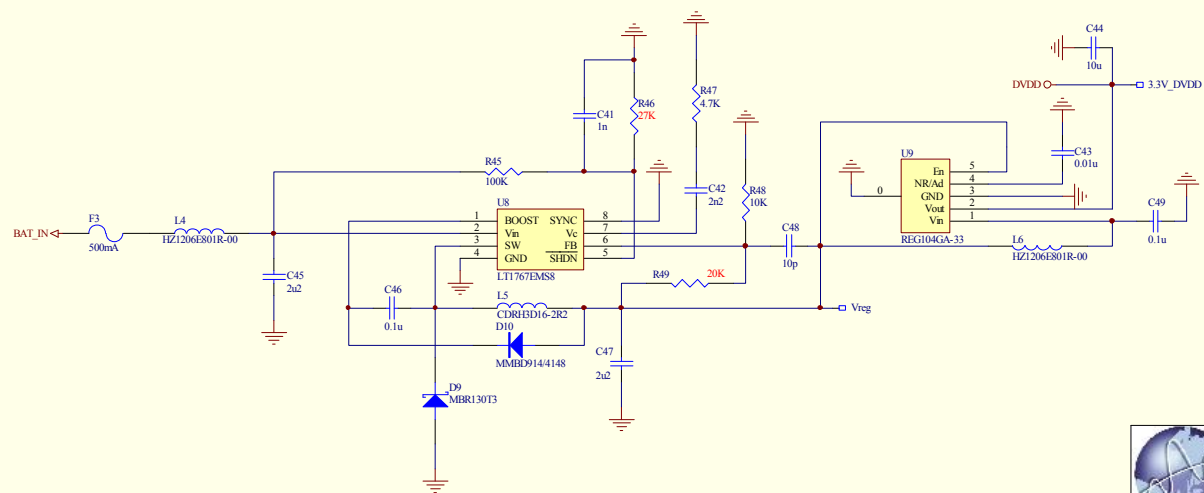




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| Organization: | Renex Technology Limited |
| Title:        | Interface                |
| File:         | Interface.Sch            |
| Revision:     | EC0112                   |
| Date:         | 4-May-2004               |
| Designed by:  | Derek Wong, Lu Kui       |



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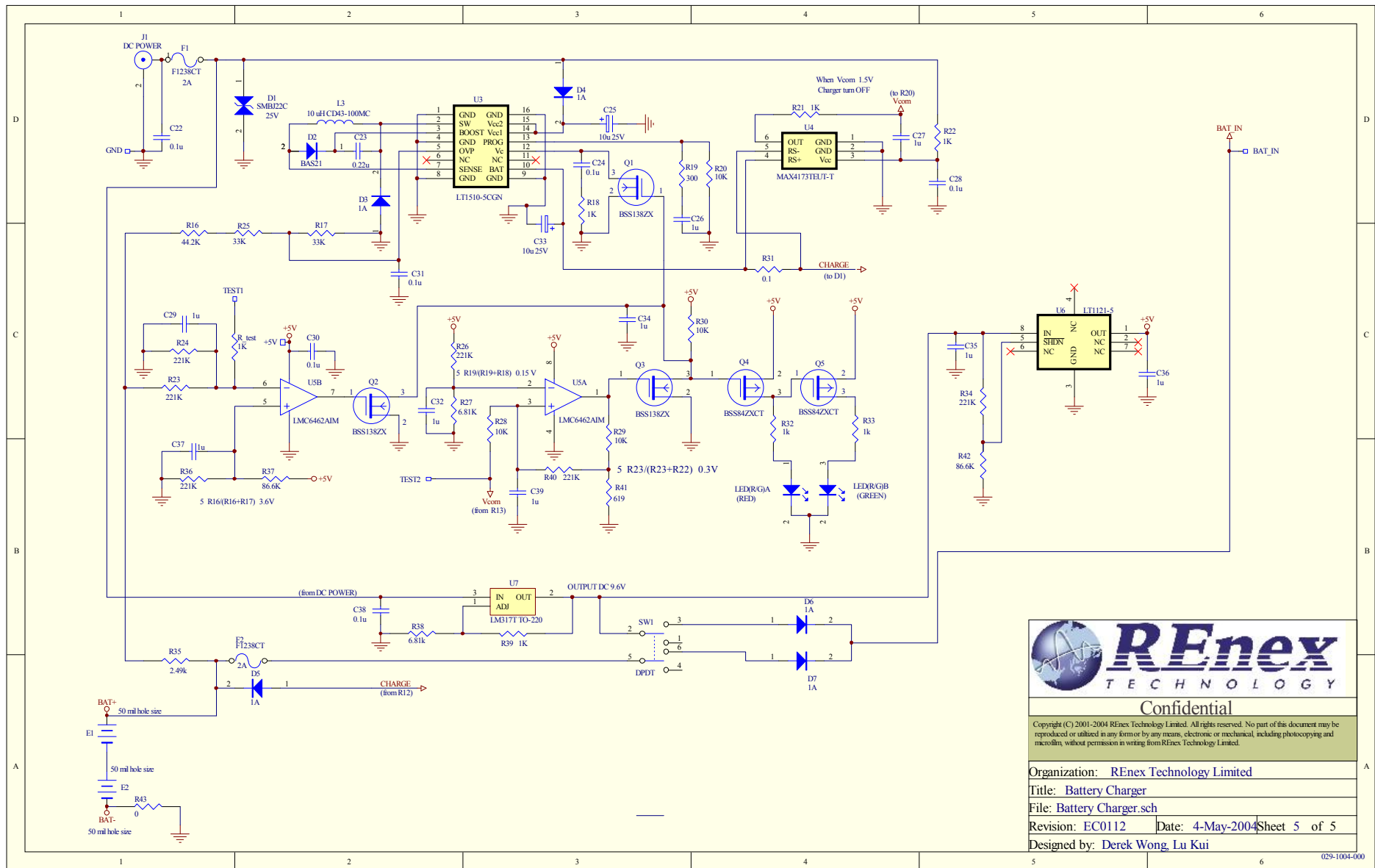
Organization: REnex Technology Limited

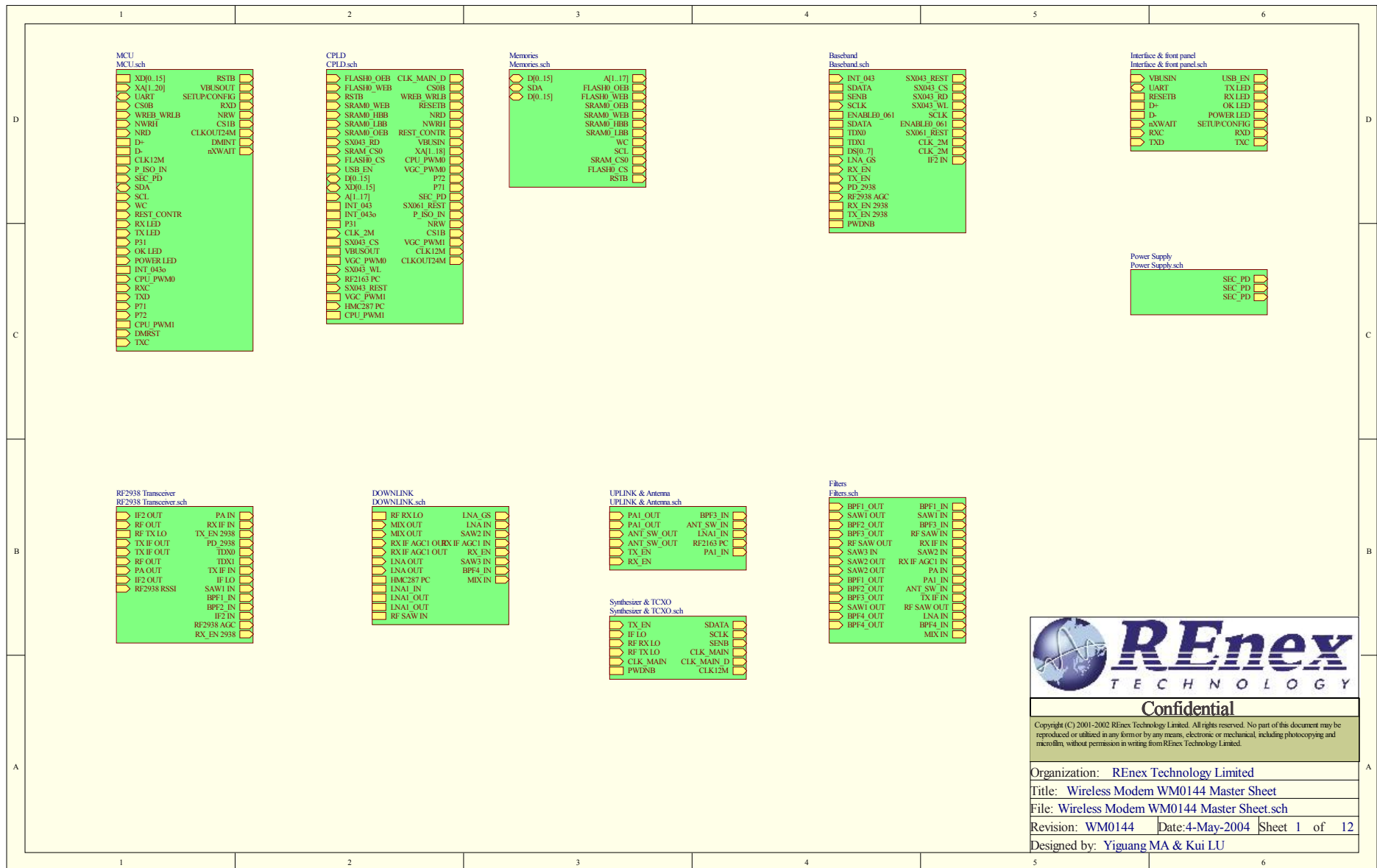
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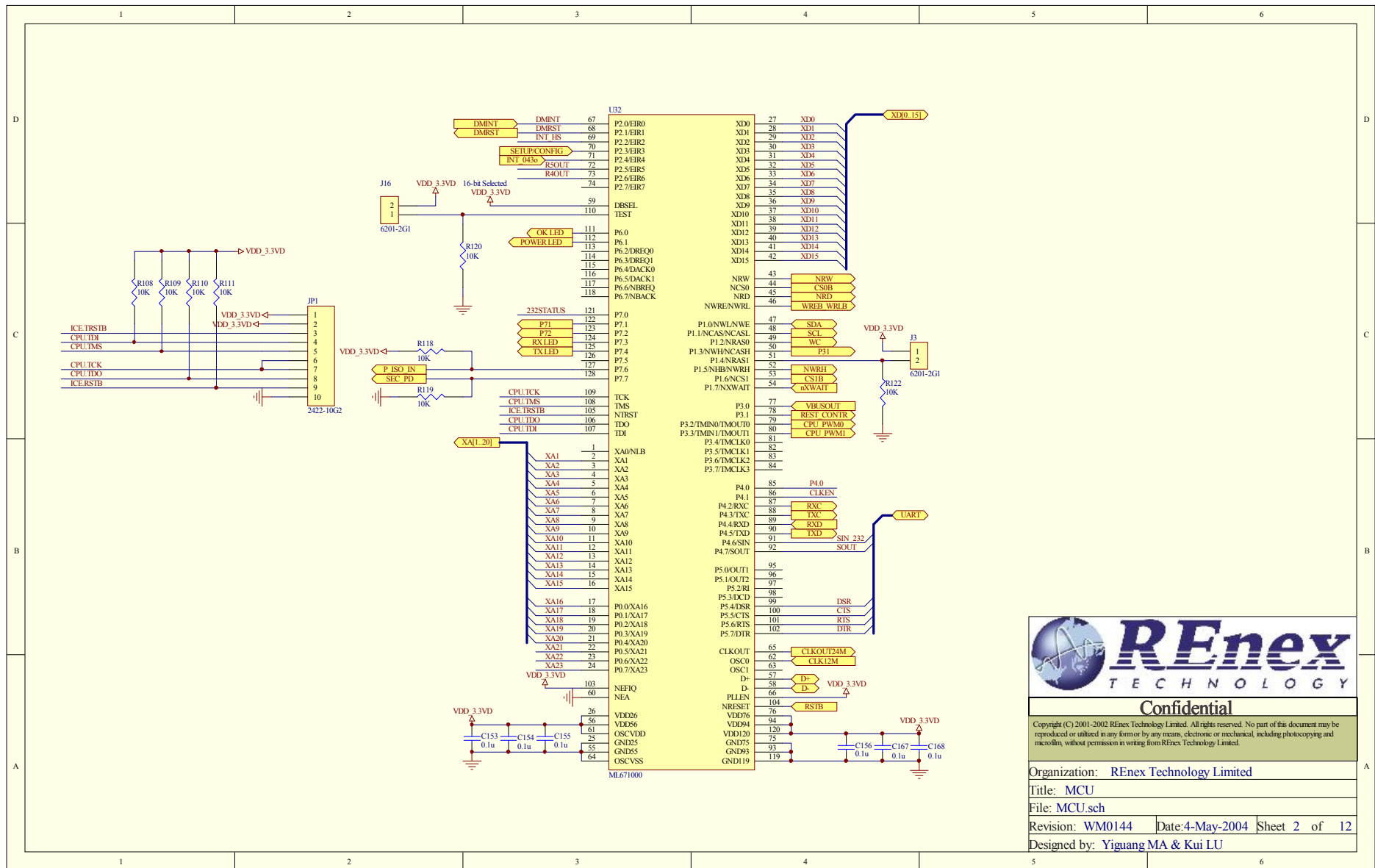
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Revision: EC0112 Date: 4-May-2004 Sheet 4 of 5

Designed by: Derek Wong, Lu Kui



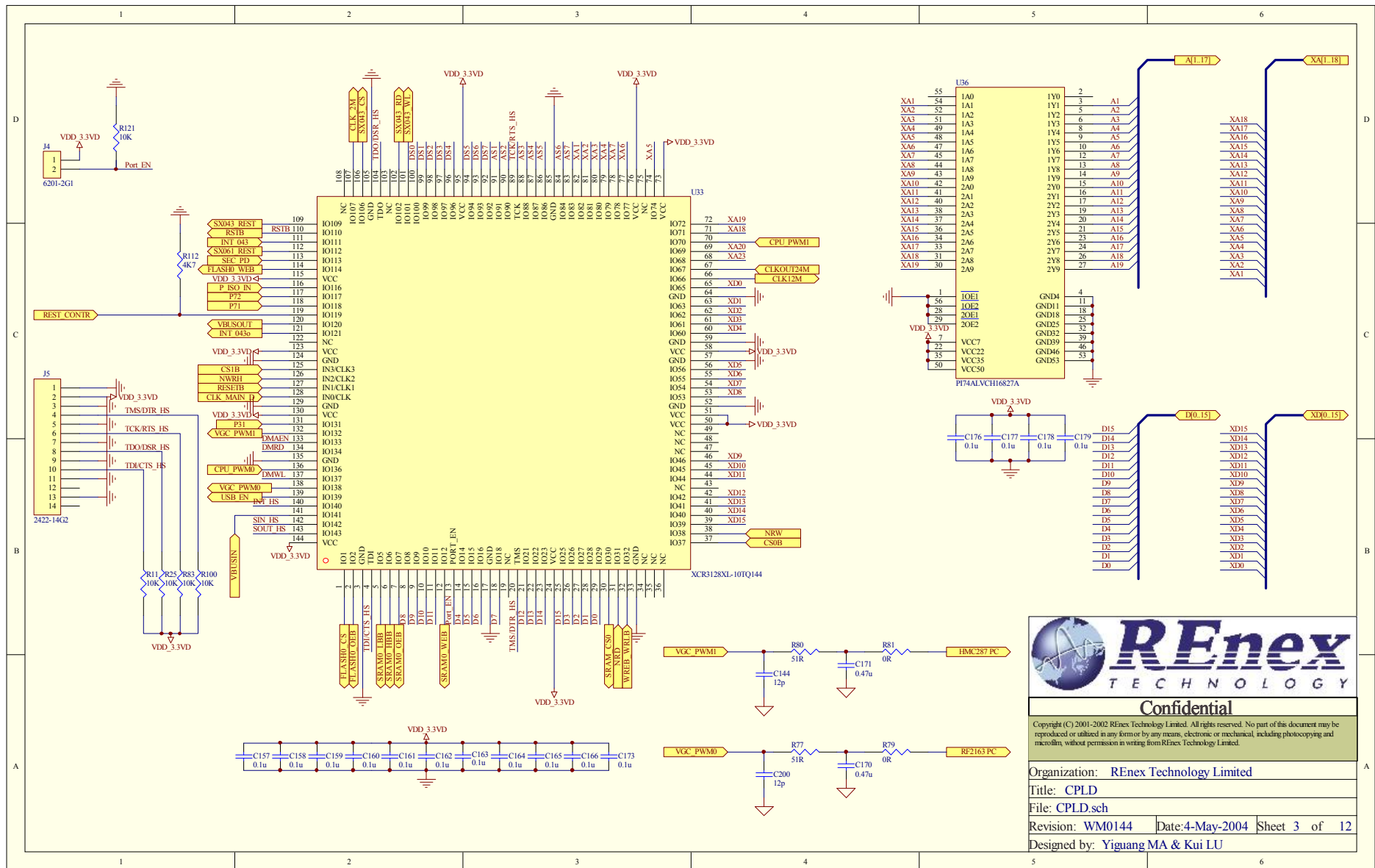




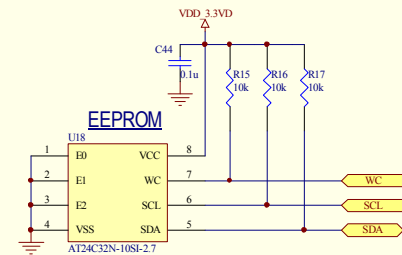
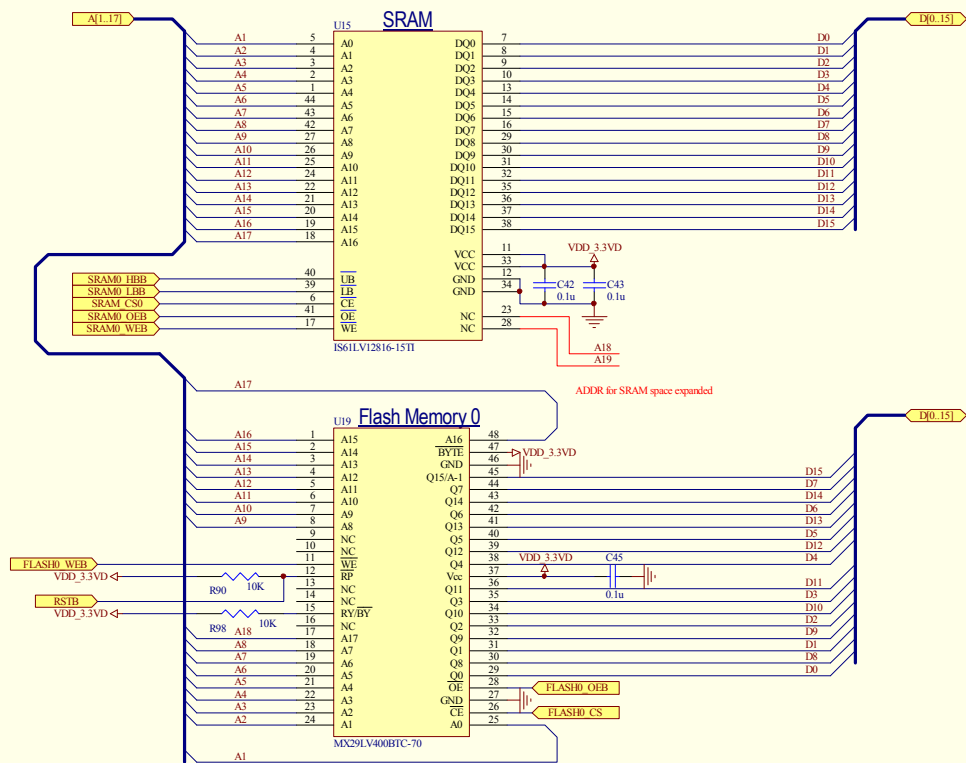
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Organization: REnex Technology Limited  
 Title: MCU  
 File: MCU.sch  
 Revision: WM0144 Date: 4-May-2004 Sheet 2 of 12  
 Designed by: Yiguang MA & Kui LU







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Title: Memories

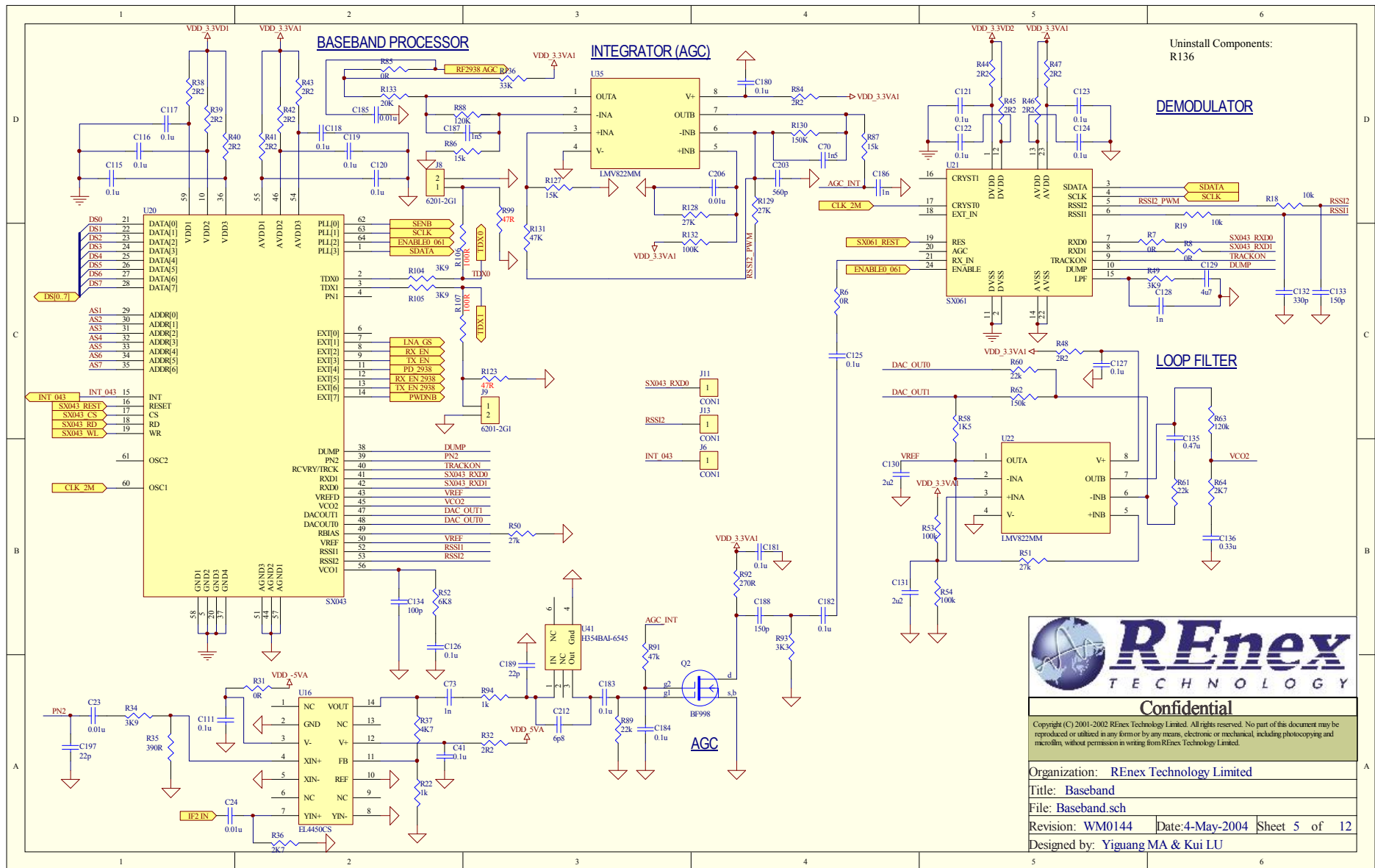
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Revision: WM0144

Date: 4-May-2004

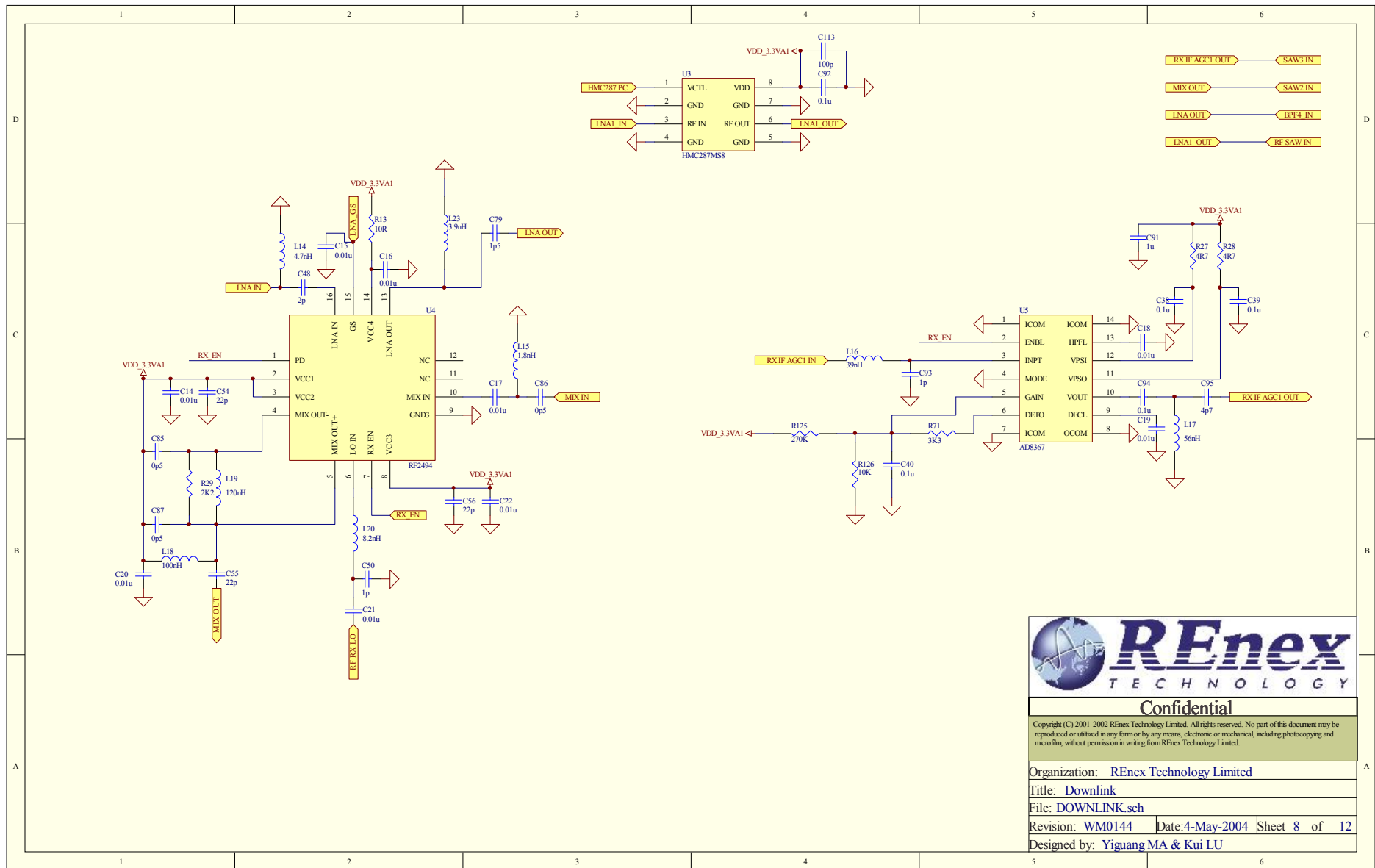
Sheet 4 of 12

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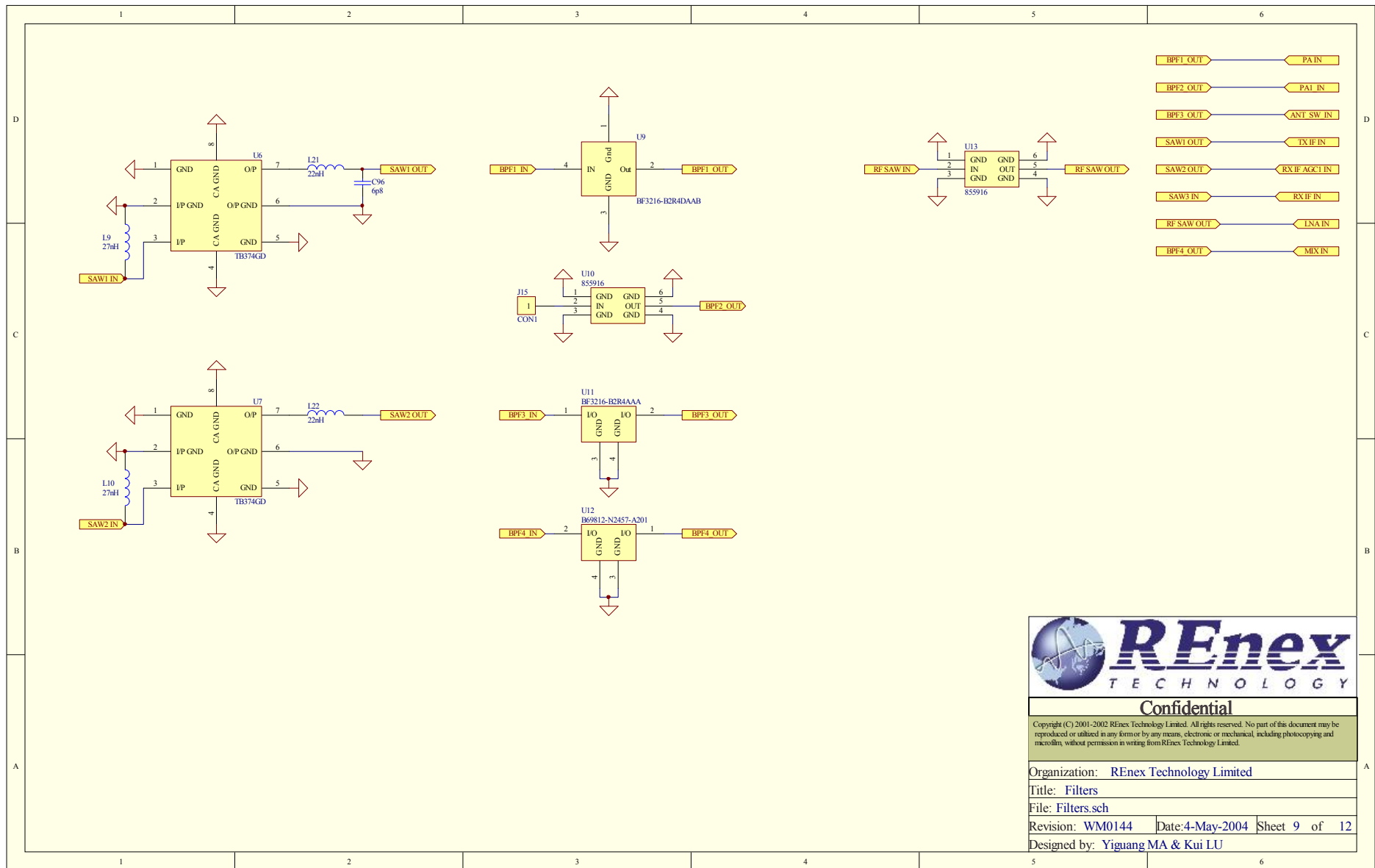




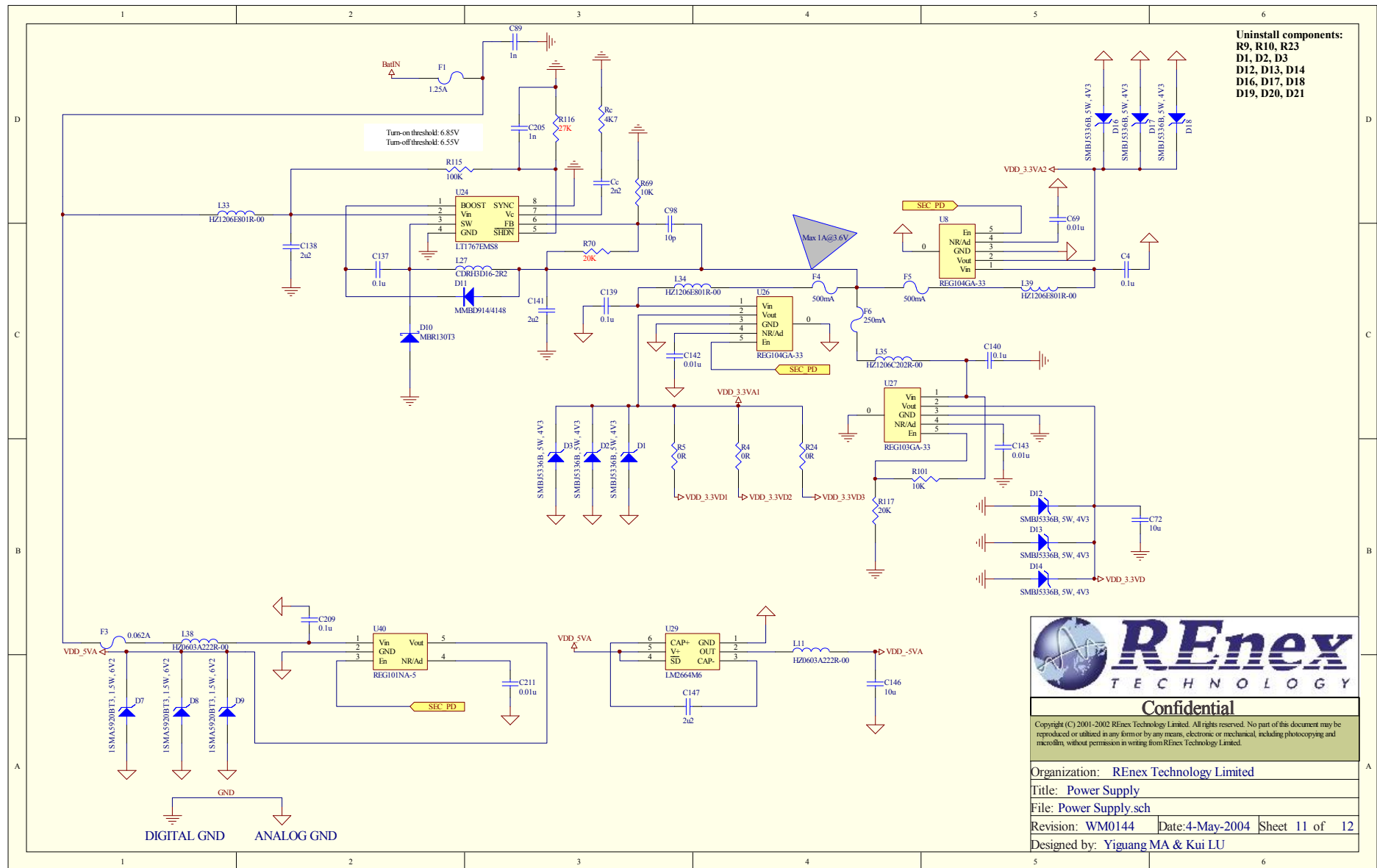
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| Organization: REnex Technology Limited |                  |               |  |
| Title: Downlink                        |                  |               |  |
| File: DOWNLINK.sch                     |                  |               |  |
| Revision: WM0144                       | Date: 4-May-2004 | Sheet 8 of 12 |  |
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Organization: REnex Technology Limited

Title: Power Supply

File: Power Supply.sch

Revision: WM0144

Date: 4-May-2004

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