

WMS-25 BLOCK DIAGRAM

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graph LR; DC[DC +4.5V] --> PS[POWER SW]; DC --> BLC[BATT LOW CONTROL]; PS --> GREEN_LED[GREEN LED]; PS --> BLC; BLC --> RED_LED[RED LED]; AI[AUDIO IN] --> IL[INPUT LEVEL CONTROL IC1]; IL --> AAC[AUDIO AMP CIRCUIT IC4]; AAC --> TC[TONE CIRCUIT]; TC --> VCO[VCO Q9]; BLC --> AAC; BLC --> DCDC[DC TO DC CONNECTOR]; DCDC --> FS[FREQ SELECTOR]; FS --> CPU[CPU IC1]; CPU --> PLL[PLL CONTROL IC2]; PLL --> TXOFF[TX OFF CIRCUIT]; PLL --> VCO; TXOFF --> RF[RF CIRCUIT]; RF --> ANT[ANT];
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The block diagram illustrates the WMS-25 transmitter circuit. It features a power section with a DC +4.5V supply connected to a POWER SW and a BATT LOW CONTROL unit. The POWER SW is connected to a GREEN LED and the BATT LOW CONTROL, which is also connected to a RED LED. The audio input (AUDIO IN) is connected to the INPUT LEVEL CONTROL (IC1), which feeds into the AUDIO AMP CIRCUIT (IC4). The AUDIO AMP CIRCUIT is also connected to the BATT LOW CONTROL. The output of the AUDIO AMP CIRCUIT goes to the TONE CIRCUIT, which then connects to the VCO (Q9). The BATT LOW CONTROL also feeds into the DC TO DC CONNECTOR, which connects to the FREQ SELECTOR. The FREQ SELECTOR connects to the CPU (IC1), which then connects to the PLL CONTROL (IC2). The PLL CONTROL (IC2) also feeds into the VCO (Q9) and the TX OFF CIRCUIT. The TX OFF CIRCUIT connects to the RF CIRCUIT, which is connected to an antenna (ANT).

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