

EXHIBIT 8D

FCC ID: QKGRDX-U3

POWER LIMITING

The transmitter RF Power is regulated by a close loop ALC (Automatic Level Control) circuit. Forward power of the PA output is sensed by a directional coupler and detector network. The detected voltage (V DET) which represents the power level is fed back to the ALC IC. Here it is compared to a reference voltage (V REF) supplied by the D/A IC. The ALC will then vary the control voltage (V CONT) of the RF power amplifier to reduce its gain until the correlation between V DET and V REF is established. Thus the power output is automatically maintained at a constant level regardless of any disturbances that may occur in the transmitting system.



