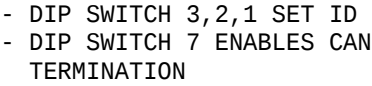
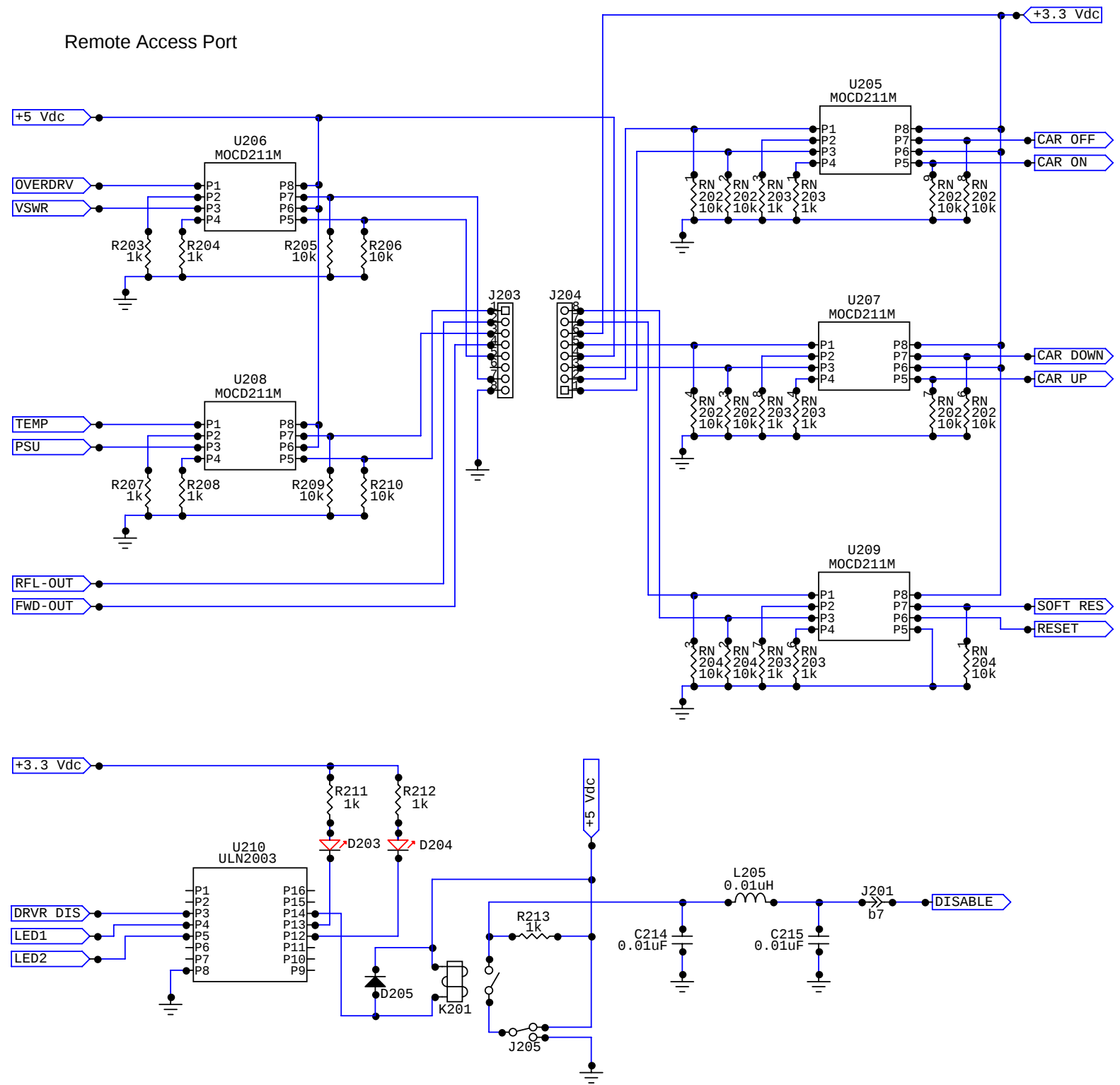


Microcontroller



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Coupler Conditioning Module



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The schematic diagram illustrates the signal processing for FWD and RFL channels. It features two operational amplifiers, U214A (OPA2340UA) and U214B (OPA2340UA), configured as buffers. The FWD channel (top) takes input from FWD-IN (J206C) through a filter (L209, C222, C223) and a buffer (U214A). The RFL channel (bottom) takes input from RFL-IN (J207C) through a filter (L211, C226, C227) and a buffer (U214B). Both channels have feedback resistors (R217, R219, R221, R223) and output filters (L210, L212, C224, C225, C228, C229). Power supplies +5Vdc and +3.3 Vdc are connected to the top of the circuit. Ground connections are labeled FWD-GND (J206A, J206B) and RFL-GND (J207A, J207B).

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