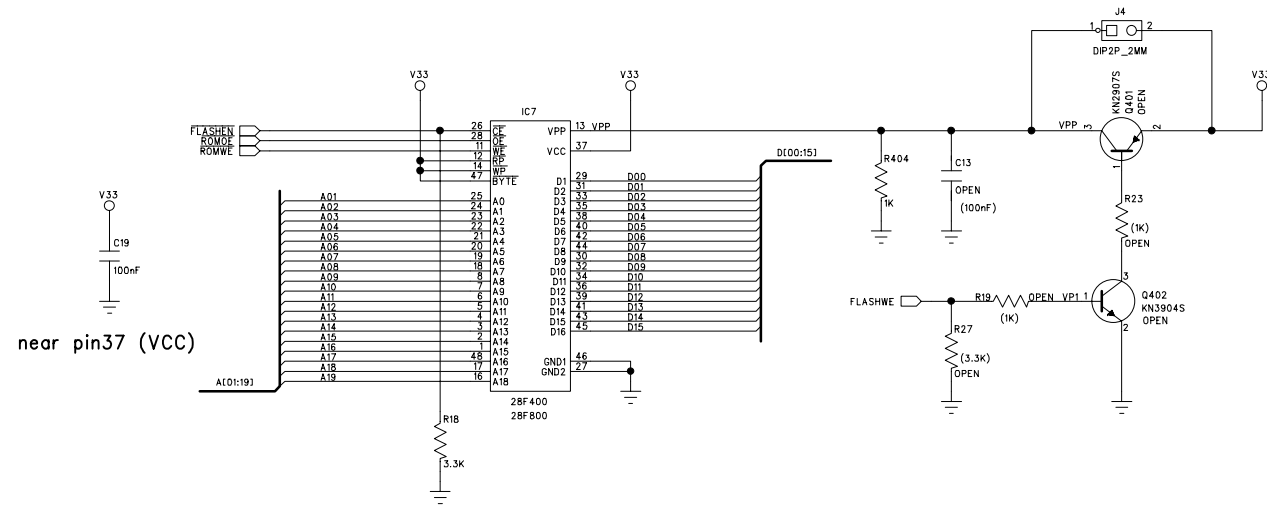
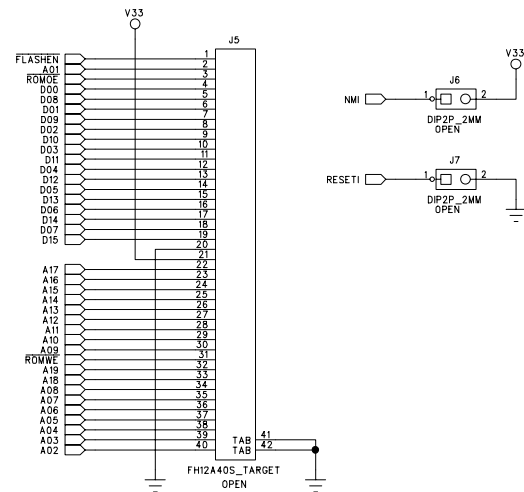
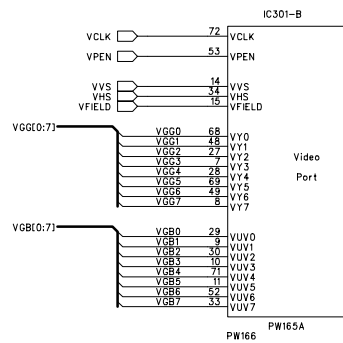
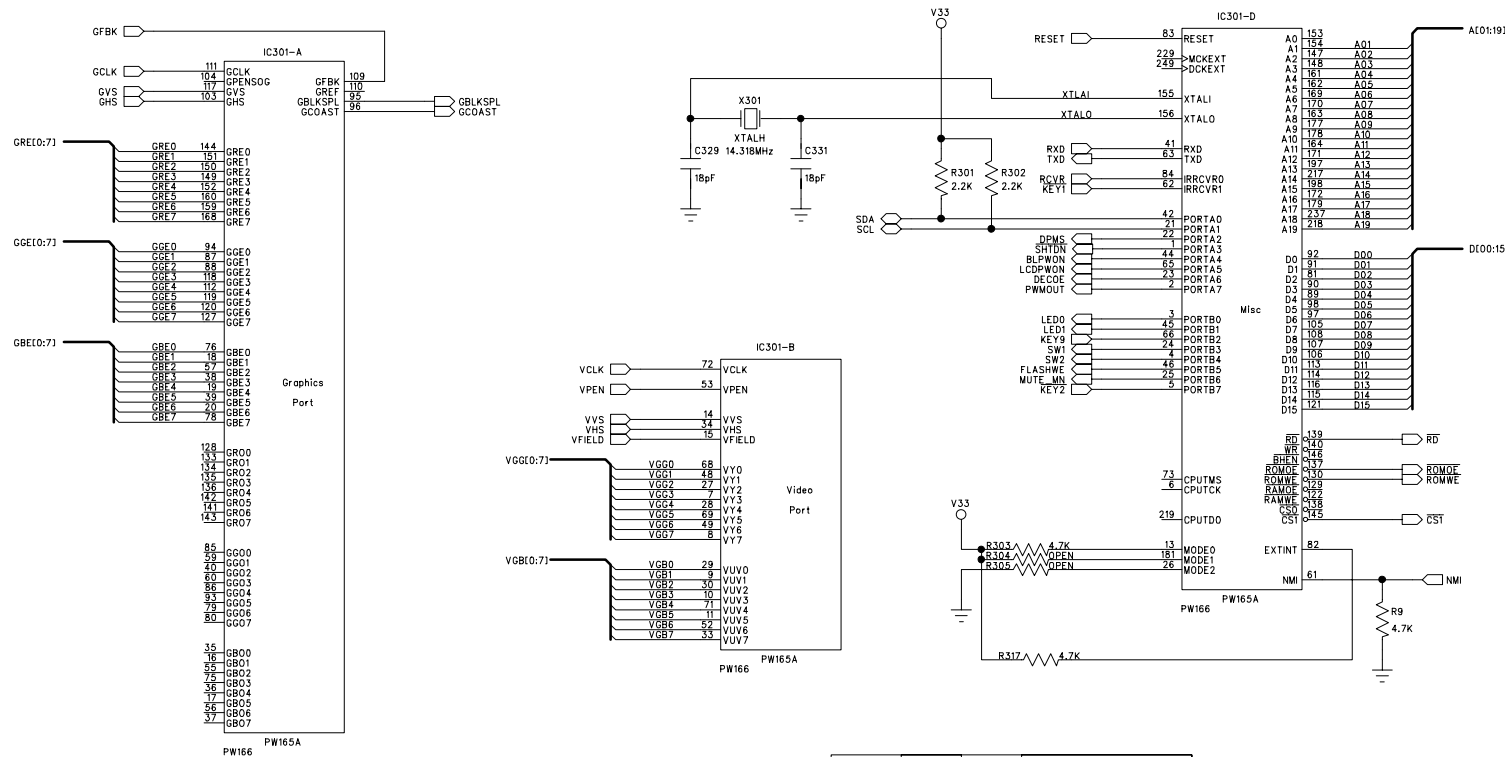
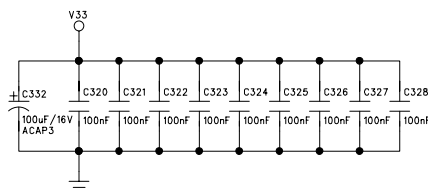
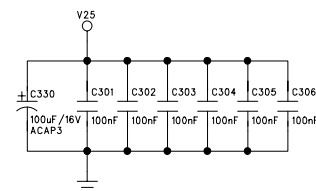
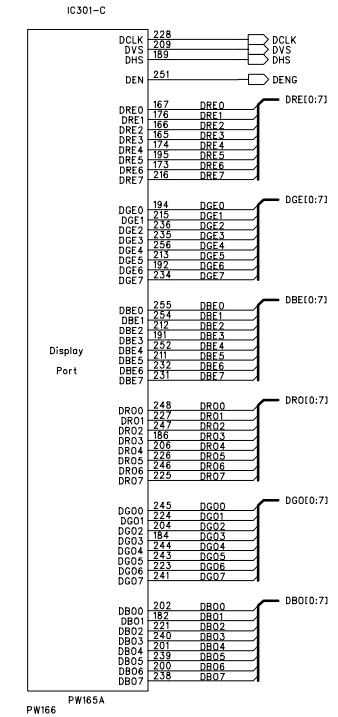
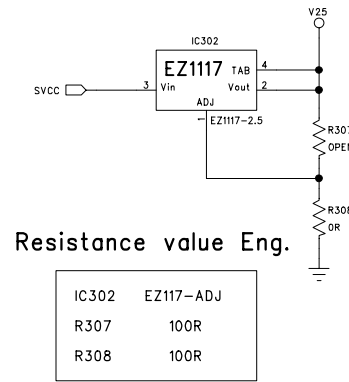


Debugger Connector





R305	R304	R303	Processor Mode
0	NO	NO	Reserved
0	NO	4.7K	Enable Internal PLL, Osc. and JTAG port
0	4.7K	NO	Enable JTAG Port
0	4.7K	4.7K	Reserved
NO	NO	NO	Reserved
NO	NO	4.7K	Enable Internal PLL and Oscillator
NO	4.7K	NO	PW164 Mode
NO	4.7K	4.7K	Enable Internal PLL



6

5

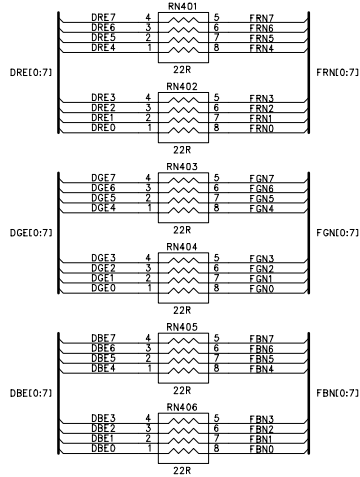
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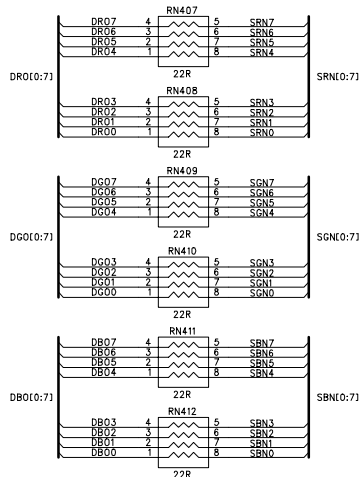
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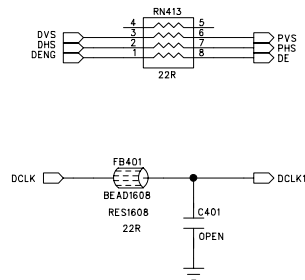
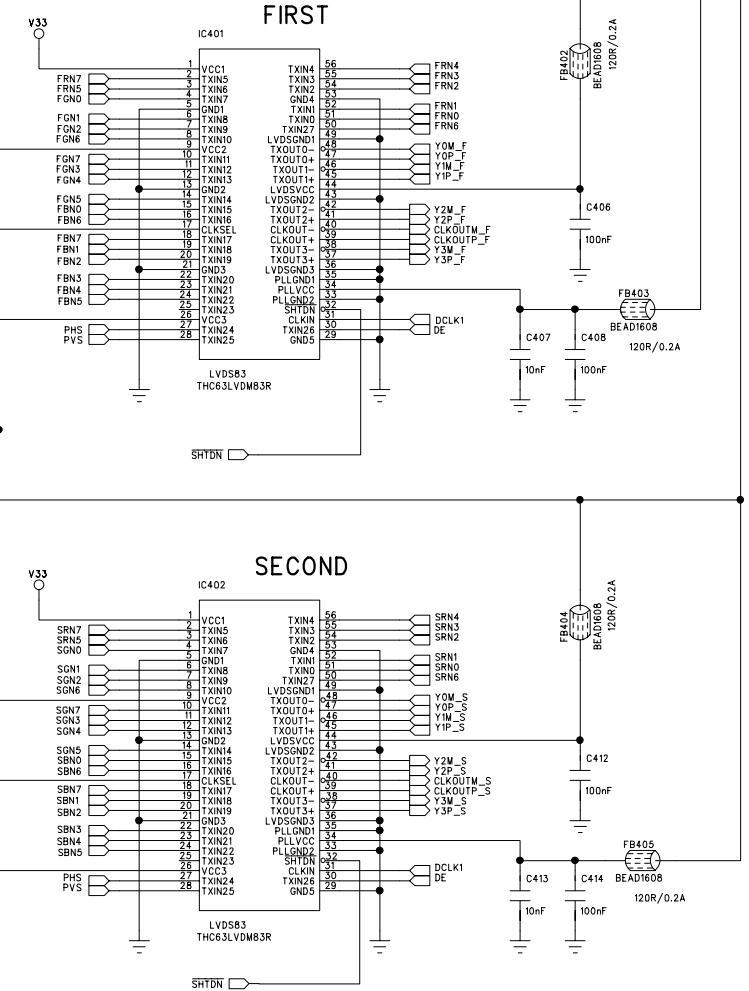
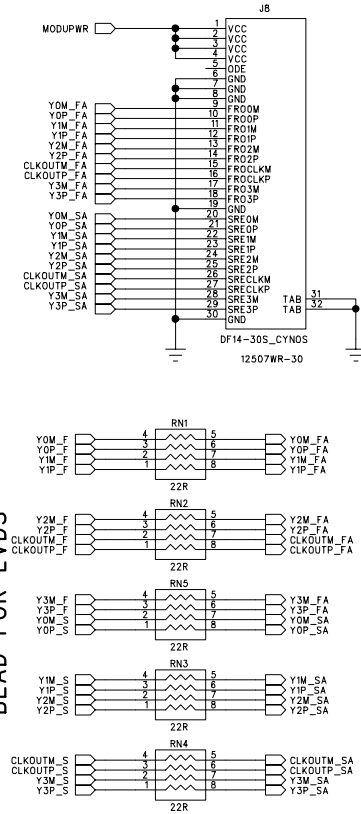
FIRST LVDS

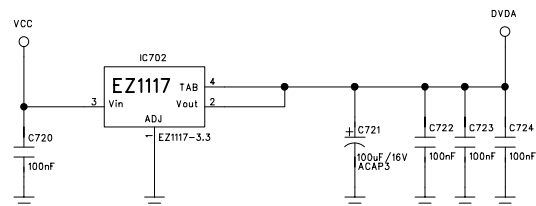
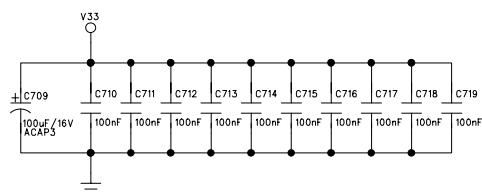
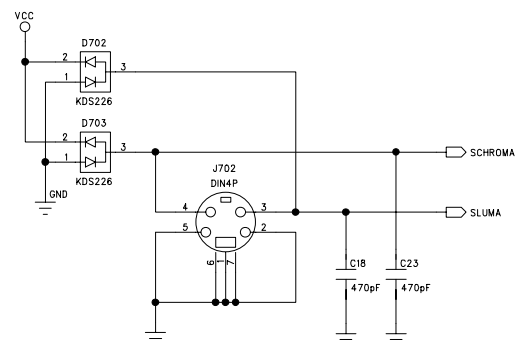
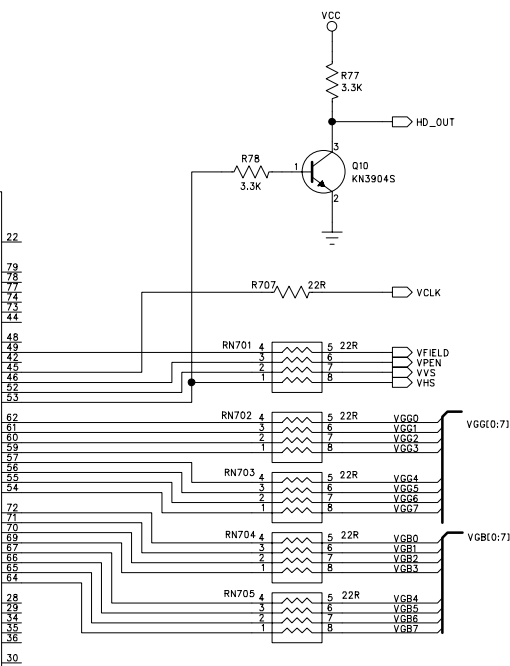
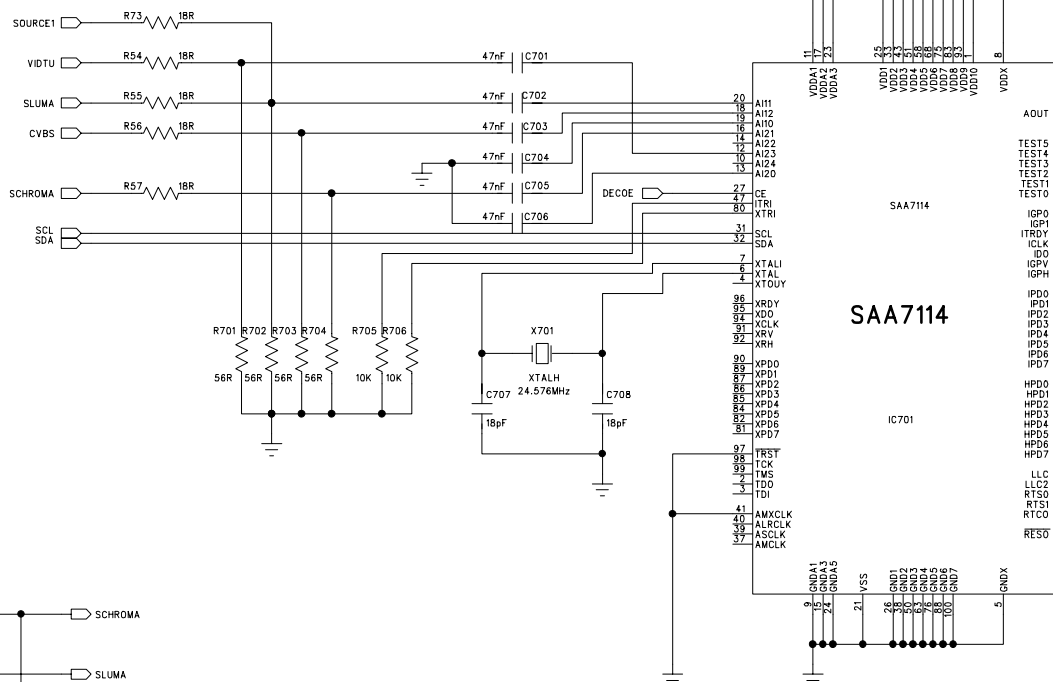
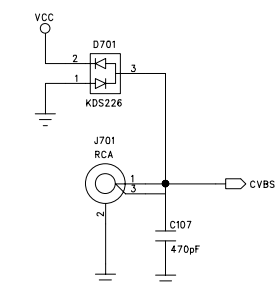


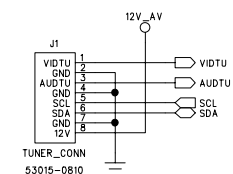
SECOND LVDS



BEAD FOR LVDS







AUDIO SWITCH

