Circuit Operational Description

1. Features

➢Code Format   : POCSAG ,1200BPS

➢Frequency   : 457.5750MHz(450.000MHz ~ 469.000MHz)

➢LED   : 6 EA (RED COLOR LED)

➢LCD   : 128 * 64 dots (B/W)

➢Alert   : Vibrator & Buzzer & LED

➢Code process   : Alpha 64 characters & Numeric message

➢Battery   : 2.4V Re-chargeable

➢ID setting   : 4 CAPCODE

➢Out of range melody and display

➢Low battery melody and display

2. Circuit operational description

- Receiver : super heterodyne receiver

Carrier frequency (457.575MHz), passed thru antenna, amplifies at Q101, Q102.

Q103 receives signals of Q106, Q1, Q2 and makes 1st IF (21.4MHz) to send it to U101.

U101 mixes 1st IF with 2nd local oscillation frequency (20.945MHz) and makes 2nd IF (455KHz).   The 455Khz is demodulated at Dis101(quadrature detect).

U101 Sends FSK out to the Pin no 15 of CPU(U103).