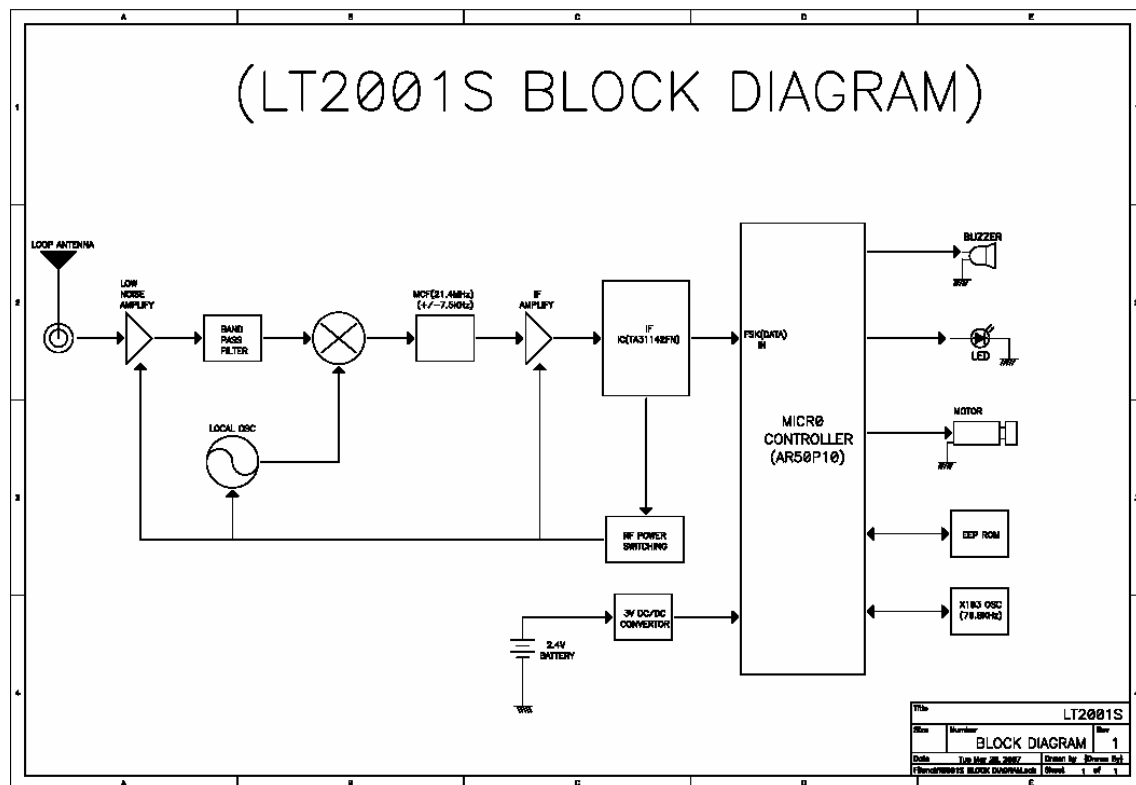


Circuit Description

* Model : LT2001S

* Product : Pager



1. SPECIFICATION

Pocsag Code

Carrier frequency : 457.5750 MHz + / - 200Hz

Frequency Range : 450.000 MHz – 470.000MHz

FM modulation & FSK (frequency shift keying)

Deviation : 4.5 KHz

FM Accuracy : + / - 5 % $4.275 < 4.5 < 4.725$

2. CIRCUIT DESCRIPTION

Pager uses DC 2.4V as its power supply.

Pager receives the frequency from TX through loop antenna. 457.5750MHz is induced into C103(3P) capacitor to match the antenna. First amplification occurs at circuit Q101, Q102. Frequency, 436.175Mhz produced from Local OSC at Q106 and 457.5750MHz are mixed at Q103. The mixed frequency, 21.4Mhz goes to U101 IFIC(TA31142FN) through Fil102 Filter. If a signal is input, 20.9450MHz and oscillation Crystal are mixed and produce 455KHz frequency. The output 455KHz signal goes through IFIC filter and amplified at IFIC and transformed to FSK signal.

This FSK signal is input into cpu number 15 terminal. The CPU is operated by this FSK data.